

THE CRYSTAL MASTER 2024

Crystal Devices Catalogue

kHz Range
Crystal unit

kHz Range
SPXO / TCXO

Real Time Clock
module

MHz Range
Crystal unit
Crystal unit with
Thermistor

MHz Range
SPXO / SPSO
CMOS

MHz Range
SPXO / SPSO
Differential

TCXO

TCXO (For NW)

VCXO / VCSO





RF transmitter
module

Sensor

Automotive

Appendix

► Explanation of the mark that are using it for the catalog

	<p>► Pb free.</p>
	<p>► Complies with EU RoHS directive. *About the products without the Pb-free mark. Contains Pb in products exempted by EU RoHS directive. (Contains Pb in sealing glass, high melting temperature type solder or other.)</p>
	<p>► Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.</p>
	<p>► Designed for automotive applications related to driving safety (Engine Control Unit, Air Bag, ESC etc).</p>

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Crystal unit

kHz Range
SPXO / TCXO

Real Time Clock
module

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MHz Range
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www5.epsondevice.com/en/

Search function

Product Information

Parametric Search

IC Partner





Recommended Products

Part No.	Model	Package	Frequency	Output	Temperature Range	Features	Stock
5S2012AAR	2.0 x 1.6	CMR05	1.2 to 7.5	1.8 to 3.3	+125 to -55 to +125 +150 to -55 to +150 +150 to -55 to +150 +150 to -55 to +150	Real-time clock RTC	SC31 ACUR
5S2012AA (SMD-3288)	2.0 x 1.6	CMR05	1.2 to 7.5	1.8 to 3.3	+125 to -55 to +125 +150 to -55 to +150 +150 to -55 to +150 +150 to -55 to +150	Real-time clock RTC	Contact us
5S112000T	2.0 x 2.0	CMR05	1 to 10	1.8 to 3.3	+125 to -55 to +125 +150 to -55 to +150 +150 to -55 to +150 +150 to -55 to +150	Real-time clock RTC	216319

Search Results

Results for 21222 during the entire 2022 product

Model	Package	Output	Frequency	Temperature	Features	Stock
5S2012AAR	CMR05	1.8 to 3.3	1.2 to 7.5	+125 to -55 to +125	Real-time clock	SC31 ACUR
5S2012AA	CMR05	1.8 to 3.3	1.2 to 7.5	+125 to -55 to +125	Real-time clock	Contact us
5S112000T	CMR05	1.8 to 3.3	1 to 10	+125 to -55 to +125	Real-time clock	216319

Evolution Circuit and Solution Database of Quartz Unit for 5S1002401

Part number of OSC1_A03C32_OUT for 5S1002401 is 5S1002401C32

Part No.	Package	Frequency	Output	Temperature
5S1002401	CMR05	32.768	1.8 to 3.3	+125 to -55 to +125
5S1002401C32	CMR05	32.768	1.8 to 3.3	+125 to -55 to +125

Name	Q1 (S1)	Part number	Qty	Control parts		1-875	Qty
				Cap	Cap		
IC 1038	7.0	3742001410001	30	7	7	270	3.2V
				7	7	270	2.4V
IC 1038	8.0	3742001410001	30	6	6	330	3.2V
				6	6	330	2.4V

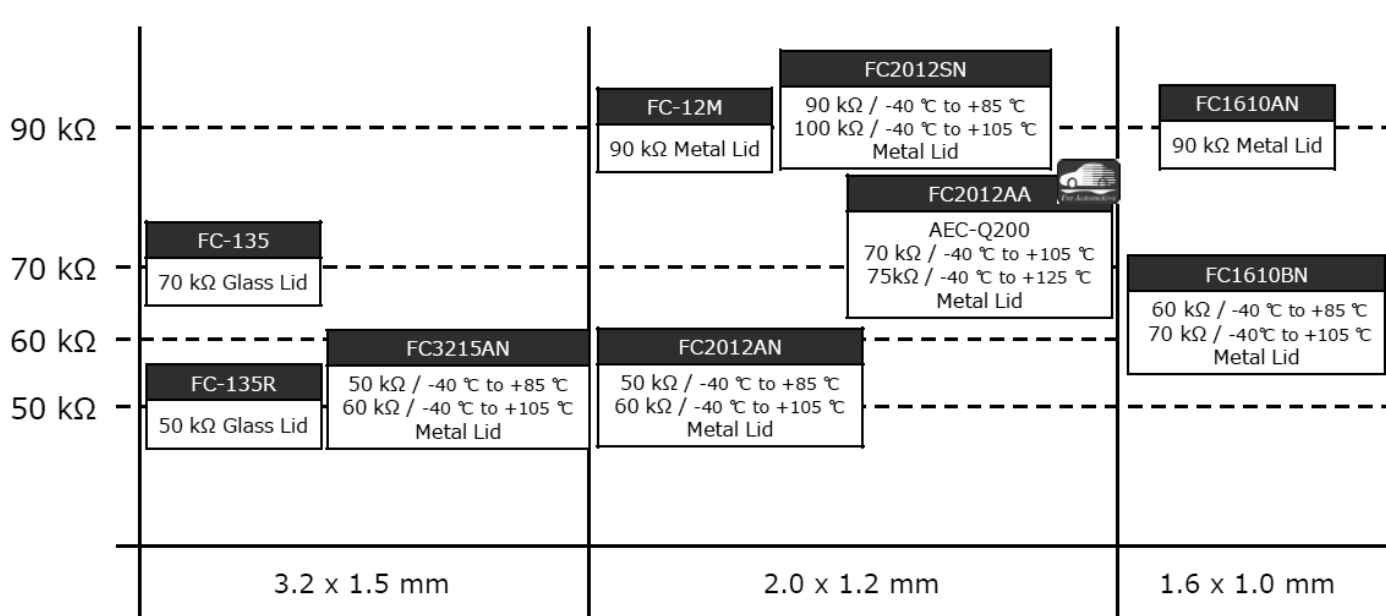
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A list of our products that have been tested for operation in cooperation with semiconductor manufacturers is posted as IC partner information.

kHz Products (Crystal Unit, Crystal Oscillator)

kHz Crystal Unit Recommendation Products



► kHz Crystal Unit

*Please Contact us regarding available frequency other than 32.768 kHz.

P	Model	Size [mm]	Nominal frequency range	Frequency Tolerance (+25 °C) [$\times 10^{-6}$]	Motional resistance Max. [kΩ]	Load capacitance [pF]	Operating temperature [°C]	Frequency vs temperature characteristics (Turnover Temperature: +25 °C Typ.)
9	FC1610AN	1.6 x 1.0 x 0.5t	32.768 kHz	±20	90	7 9 12.5	-40 to +85	(B): $-0.04 \times 10^{-6} / ^\circ\text{C}^2 \text{ Max.}$ $f_{\text{tem}} = B (T - \theta_x)^2$
10	FC1610BN	1.6 x 1.0 x 0.5t	32.768 kHz	±20	45 60 70	7 9 12.5	+25 -40 to +85 -40 to +105	
11	FC2012SN	2.05 x 1.2 x 0.6t	32.768 kHz	±20	90 100	7 9 12.5	-40 to +85 -40 to +105	
12	FC2012AN	2.05 x 1.2 x 0.6t	32.768 kHz	±20	50 60	7 9 12.5	-40 to +85 -40 to +105	
13	FC-12M	2.05 x 1.2 x 0.6t	32.768 kHz 32 kHz — 77.5 kHz	±20 ±30	90 90 to 65	7 9 12.5	-40 to +85 -40 to +85	
14	FC3215AN	3.2 x 1.5 x 0.9t	32.768 kHz	±20	50 60	7 9 12.5	-40 to +85 -40 to +105	
15	FC-135R	3.2 x 1.5 x 0.9t	32.768 kHz	±20	50	7 9 12.5	-40 to +85	
	FC-135		32.768 kHz		70		-40 to +85	
			32 kHz — 77.5 kHz		70 to 45		-40 to +85	
114	FC2012AA (AEC-Q200)	2.05 x 1.2 x 0.6t	32.768 kHz	±20	40 70 75	7 9 12.5	+25 -40 to +105 -40 to +125	

kHz Crystal Unit and kHz Crystal Oscillator Recommendation Products



► kHz Crystal Oscillator

► SPXO

P	Model	Size [mm]	Nominal frequency range	Frequency Tolerance (+25 °C) [x 10 ⁻⁶]	Operating temperature (T _{use}) [°C]	Supply voltage [V]	Current consumption Max. [µA] (V _{CC} = 3.3 V, No load, T _{use})	Output load condition [pF]	Output control
6	SG-3031CM	3.2 × 1.5 × 0.9t	● 32.768 kHz	5 ± 23	-40 to +85	1.1 to 5.5	1.3	15	V _{IO}
115	SG-3031CMA (AEC-Q100)								

► TCXO

P	Model	Size [mm]	Nominal frequency range	Frequency tolerance [x 10 ⁻⁶] / Operating temperature (T _{use}) [°C]	Supply voltage [V]	Current consumption Max. [µA] (V _{CC} = 3.3 V, No load, T _{use})	Output load condition [pF]	Output control
17	TG-3541CE	3.2 × 2.5 × 1.0t	● 32.768 kHz	±3.4 / -40 to +85 ±5.0 / -40 to +85 ±8.0 / +85 to +105	1.5 to 5.5	3.0	30	OE
116	TG-3541CEA (AEC-Q100)							

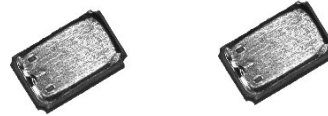


kHz RANGE CRYSTAL UNIT

FC1610AN

Product Number
X1A000121xxx00

- Frequency range : 32.768 kHz
- External dimensions : 1.65 × 1.05 × 0.5 mm
- Overtone order : Fundamental
- Applications : Small communications devices



Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
Nominal frequency range	f_nom	32.768 kHz	
Storage temperature range	T_stg	-55 °C to +125 °C	Storage as single product.
Operating temperature range	T_use	-40 °C to +85 °C	
Level of drive	DL	0.1 μW Typ. (0.5 μW Max.)	
Frequency tolerance (standard)	f_tol	±20 × 10 ⁻⁶	+25 °C, DL = 0.1 μW
Turnover temperature	Ti	+25 °C ± 5 °C	
Parabolic coefficient	B	-0.04 × 10 ⁻⁶ / °C ² Max.	
Load capacitance	CL	7 pF, 9 pF, 12.5 pF	Please contact us other CL values
Motional resistance (ESR)	R1	90 kΩ Max.	
Motional capacitance	C1	6.0 fF Typ.	
Shunt capacitance	C0	1.2 pF Typ.	
Frequency aging	f_age	±3 × 10 ⁻⁶ / year Max.	+25 °C, First year

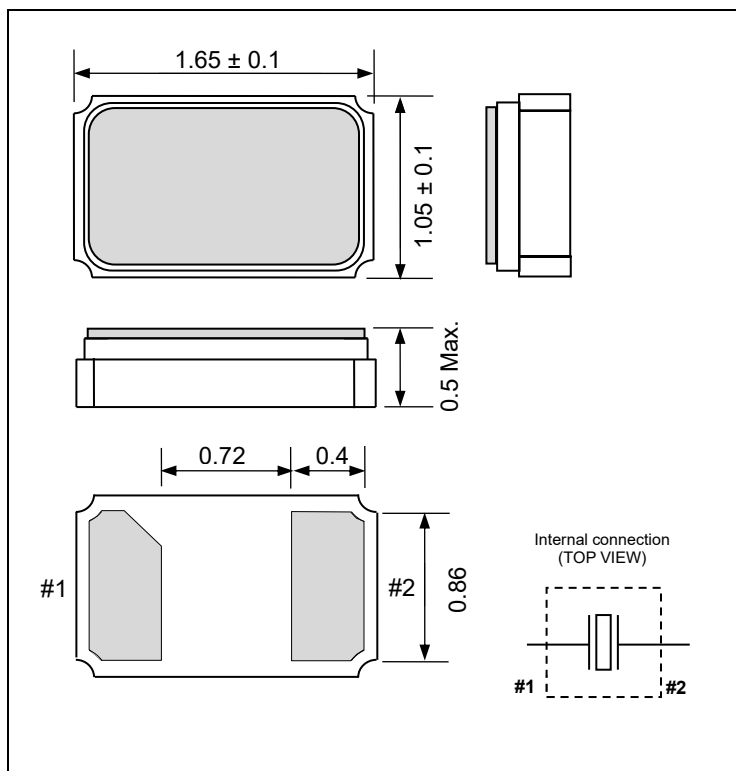
Product name

Product name FC1610AN 32.768000kHz 12.5 +20.0-20.0
 (Standard form) ① ② ③ ④

①Model ②Frequency ③Load capacitance (pF) ④Frequency tolerance (×10⁻⁶, +25 °C)

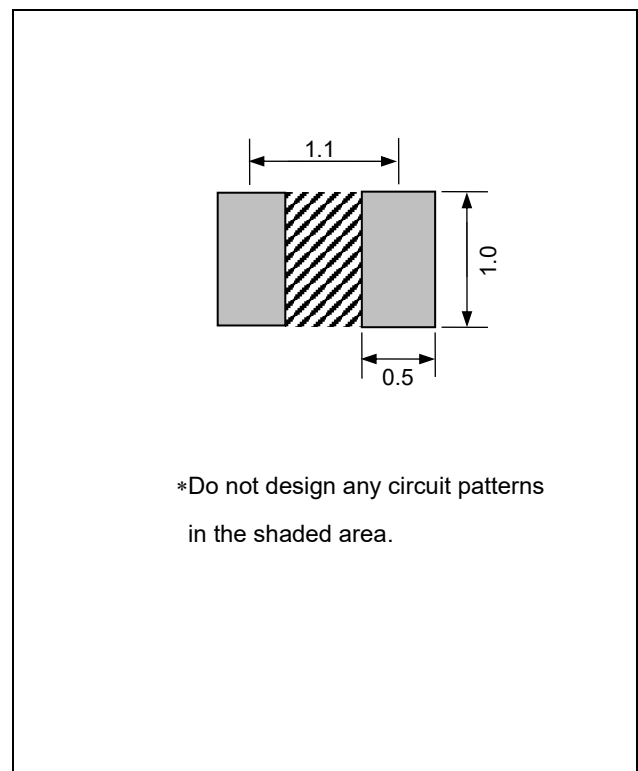
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)





kHz RANGE CRYSTAL UNIT



Product Number
FC1610BN: X1A000191xxxx18

FC1610BN

Low ESR 70kΩ Max. (-40 °C to +105 °C)

- Frequency range : 32.768 kHz
- External dimensions : 1.6 x 1.0 x 0.5 mm
- Overtone order : Fundamental
- Applications : IoT Devices, Modules, etc.



Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
Nominal frequency range	f _{nom}	32.768 kHz	
Storage temperature range	T _{stg}	-55 °C to +125 °C	Storage as single product.
Operating temperature range	T _{use}	-40 °C to +85 °C -40 °C to +105 °C	
Level of drive	DL	0.1 μW Typ. (0.5 μW Max.)	
Frequency tolerance (standard)	f _{tol}	±20 x 10 ⁻⁶	+25 °C, DL = 0.1 μW
Turnover temperature	T _i	+25 °C ± 5 °C	
Parabolic coefficient	B	-0.04 x 10 ⁻⁶ / °C ² Max.	
Load capacitance	CL	7 pF, 9 pF, 12.5 pF	Please contact us other CL values
Motional resistance (ESR)	R1	45 kΩ Typ.	+25 °C
		60 kΩ Max.	-40 °C to +85 °C
		70 kΩ Max.	-40 °C to +105 °C
Motional capacitance	C1	7.9 fF Typ.	
Shunt capacitance	C0	1.5 pF Typ.	
Frequency aging	f _{age}	±3 x 10 ⁻⁶ / year Max.	+25 °C, First year

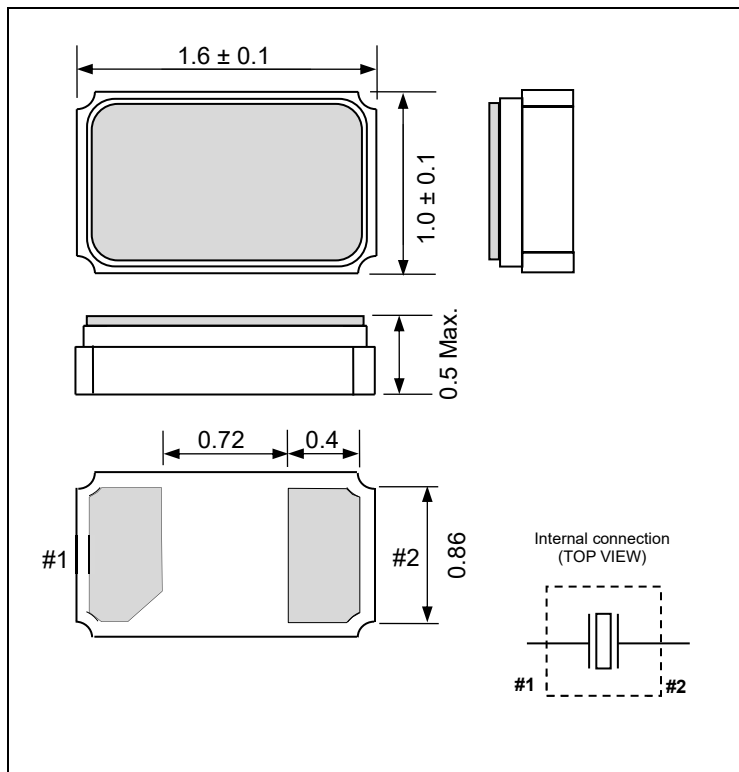
Product name

Product name FC1610BN 32.768000kHz 12.5 +20.0-20.0
 (Standard form) a b c d

a: Model b: Frequency c: Load capacitance (pF) d: Frequency tolerance (x 10⁻⁶, +25 °C)

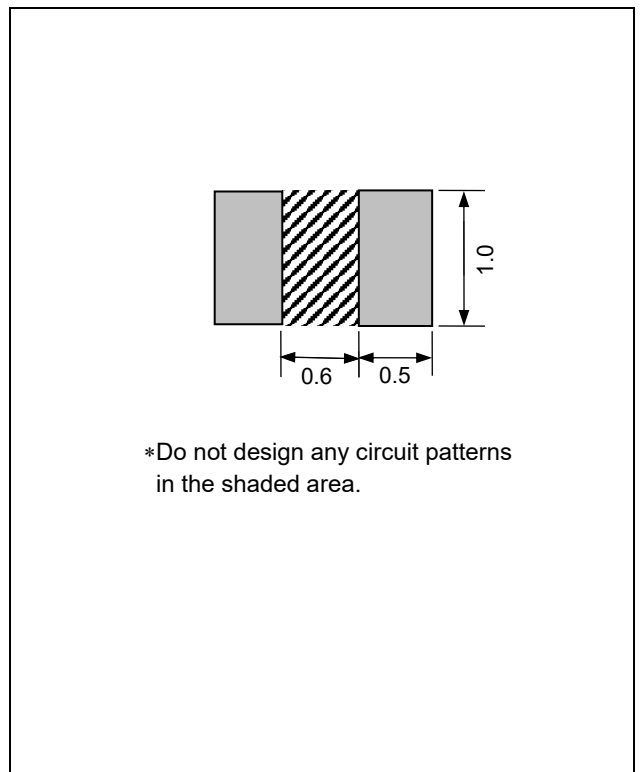
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)





kHz RANGE CRYSTAL UNIT

FC2012SN

Product Number
FC2012SN: X1A000201xxxx18

- Frequency range : 32.768 kHz
- External dimensions : 2.05 x 1.2 x 0.6 mm
- Overtone order : Fundamental
- Applications : IoT Devices, Modules, etc.



Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
Nominal frequency range	f_nom	32.768 kHz	
Storage temperature range	T_stg	-55 °C to +125 °C	Storage as single product.
Operating temperature range	T_use	-40 °C to +105 °C	
Level of drive	DL	0.5 μW Max.	
Frequency tolerance (standard)	f_tol	±20 x 10 ⁻⁶	+25 °C, DL = 0.1 μW
Turnover temperature	Ti	+25 °C ± 5 °C	
Parabolic coefficient	B	-0.04 x 10 ⁻⁶ / °C ² Max.	
Load capacitance	CL	7 pF, 9 pF, 12.5 pF	Please contact us other CL values
Motional resistance (ESR)	R1	90 kΩ Max.	-40 °C to +85 °C
		100 kΩ Max.	-40 °C to +105 °C
Motional capacitance	C1	8.4 fF Typ.	
Shunt capacitance	C0	1.6 pF Typ.	
Frequency aging	f_age	±3 x 10 ⁻⁶ / year Max.	+25 °C, First year

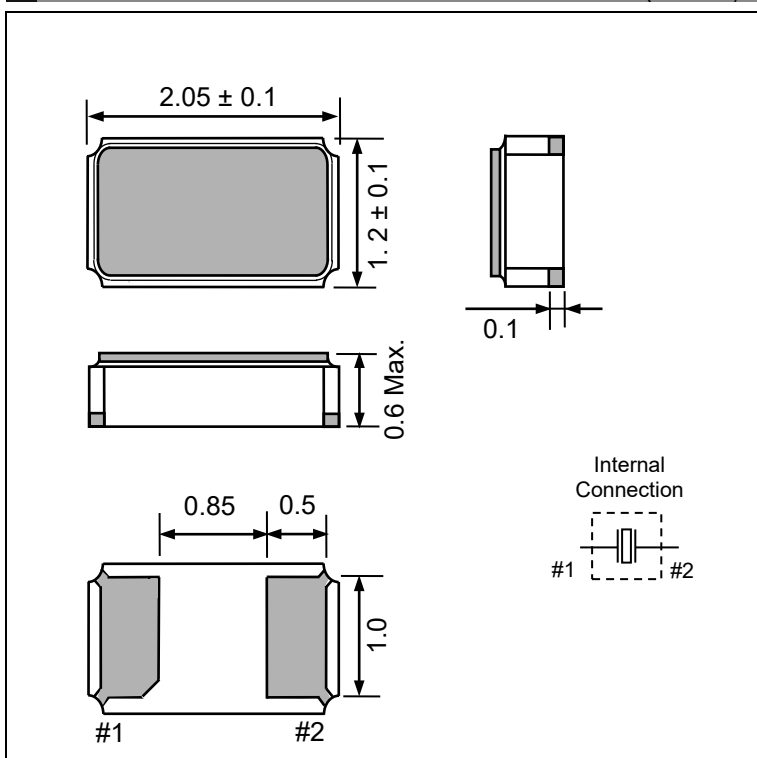
Product name

Product name FC2012SN 32.768000kHz 12.5 +20.0-20.0
 (Standard form) a b c d

a: Model b: Frequency c: Load capacitance (pF) d: Frequency tolerance (× 10⁻⁶, +25 °C)

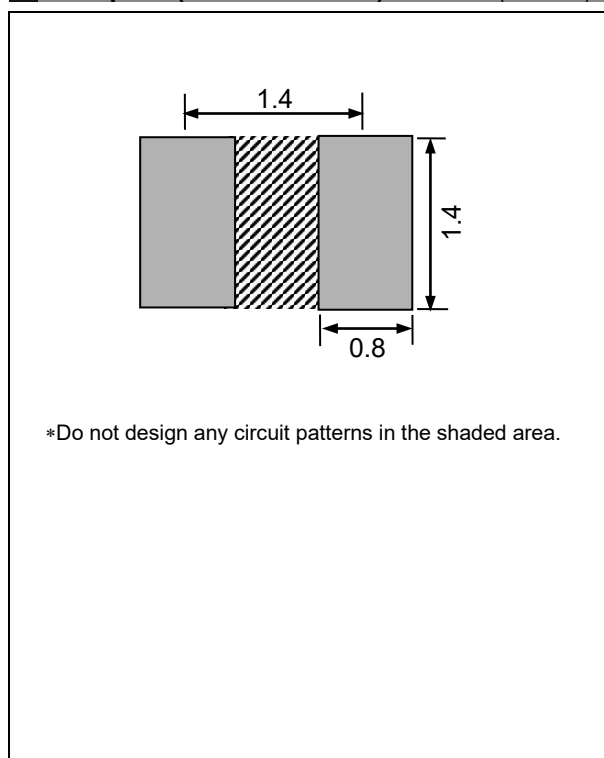
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



kHz RANGE CRYSTAL UNIT

FC2012AN

Low ESR 60 kΩ Max. (-40 °C to +105 °C)


 Product Number
 FC2012AN: X1A000171xxxx18

- Frequency range : 32.768 kHz
- External dimensions : 2.05 x 1.2 x 0.6 mm
- Overtone order : Fundamental
- Applications : IoT Devices, Modules, etc.



Specifications (characteristics)

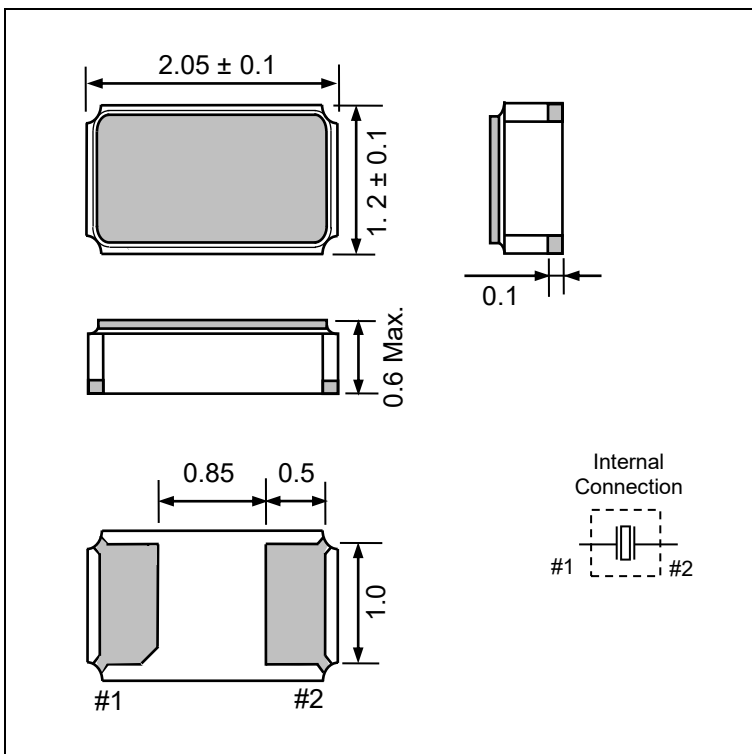
Item	Symbol	Specifications	Conditions / Remarks
Nominal frequency range	f_nom	32.768 kHz	
Storage temperature range	T_stg	-55 °C to +125 °C	Storage as single product.
Operating temperature range	T_use	-40 °C to +105 °C	
Level of drive	DL	0.5 μW Max.	
Frequency tolerance (standard)	f_tol	±20 x 10 ⁻⁶	+25 °C, DL = 0.1 μW
Turnover temperature	Ti	+25 °C ± 5 °C	
Parabolic coefficient	B	-0.04 x 10 ⁻⁶ / °C ² Max.	
Load capacitance	CL	7 pF, 9 pF, 12.5 pF	Please contact us other CL values
Motional resistance (ESR)	R1	35 kΩ Typ..	+25 °C,
		50 kΩ Max.	-40 °C to +85 °C
		60 kΩ Max.	-40 °C to +105 °C
Motional capacitance	C1	8.4 fF Typ.	
Shunt capacitance	C0	1.6 pF Typ.	
Frequency aging	f_age	±3 x 10 ⁻⁶ / year Max.	+25 °C, First year

Product name

Product name FC2012AN 32.768000kHz 12.5 +20.0-20.0
 (Standard form) ① ② ③ ④
 ①Model ②Frequency ③Load capacitance (pF) ④Frequency tolerance (× 10⁻⁶, +25 °C)

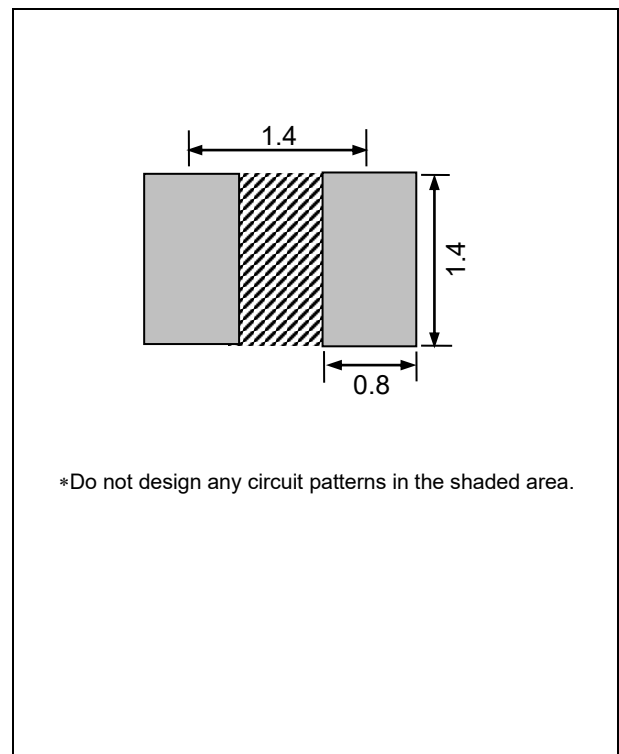
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



kHz RANGE CRYSTAL UNIT

FC-12M / FC-12M TYPE



Product Number
 FC-12M : X1A000061xxxx00
 FC-12M TYPE : X1A000021xxxx00

- Frequency range : FC-12M...32.768 kHz
 : FC-12M TYPE...32 kHz to 77.5 kHz
- External dimensions : 2.05 x 1.2 x 0.6 mm
- Overtone order : Fundamental
- Applications : Small communications devices



Specifications (characteristics)

Item	Symbol	Specifications		Conditions / Remarks
		FC-12M	FC-12M TYPE	
Nominal frequency range	f_nom	32.768 kHz	32 kHz to 77.5 kHz	Please contact us about available frequencies.
Storage temperature range	T_stg	-55 °C to +125 °C		Storage as single product.
Operating temperature range	T_use	-40 °C to +85 °C		
Level of drive	DL	0.5 μW Max.		
Frequency tolerance (standard)	f_tol	±20 x 10 ⁻⁶ ±30 x 10 ⁻⁶		+25 °C, DL = 0.1 μW Please ask for tighter tolerance
Turnover temperature	Ti	+25 °C ± 5 °C		
Parabolic coefficient	B	-0.04 x 10 ⁻⁶ / °C ² Max.		
Load capacitance	CL	7 pF, 9 pF, 12.5 pF		Please contact us other CL values
Motional resistance (ESR)	R1	90 kΩ Max.	90 kΩ to 65 kΩ	
Motional capacitance	C1	6.4 fF Typ.	7.0 fF to 2.7 fF	
Shunt capacitance	C0	1.3 pF Typ.	1.6 pF to 0.8 pF	
Frequency aging	f_age	±3 x 10 ⁻⁶ / year Max.		+25 °C, First year

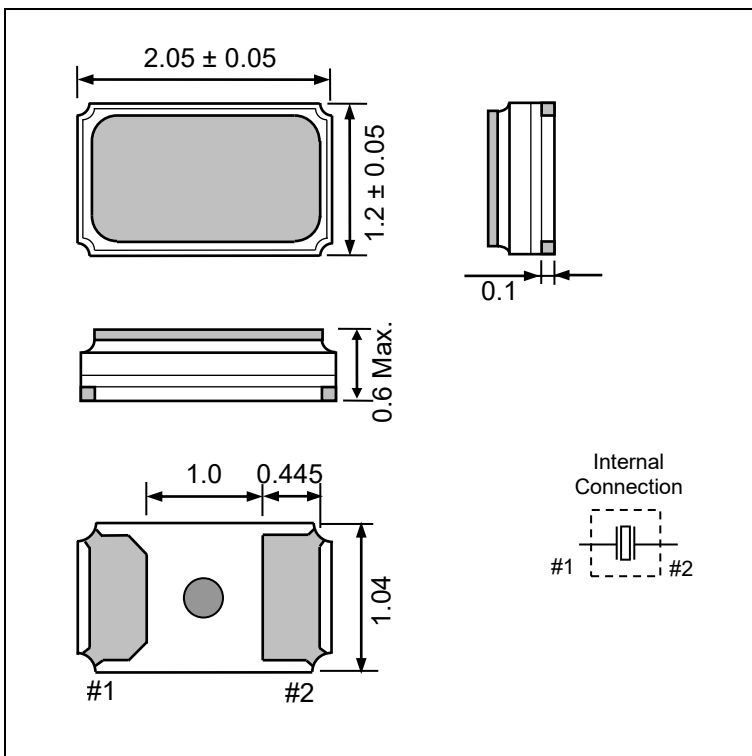
Product name

Product name FC-12M 32.768000kHz 12.5 +20.0-20.0
 (Standard form) ① ② ③ ④

①Model ②Frequency ③Load capacitance (pF) ④Frequency tolerance (× 10⁻⁶, +25 °C)

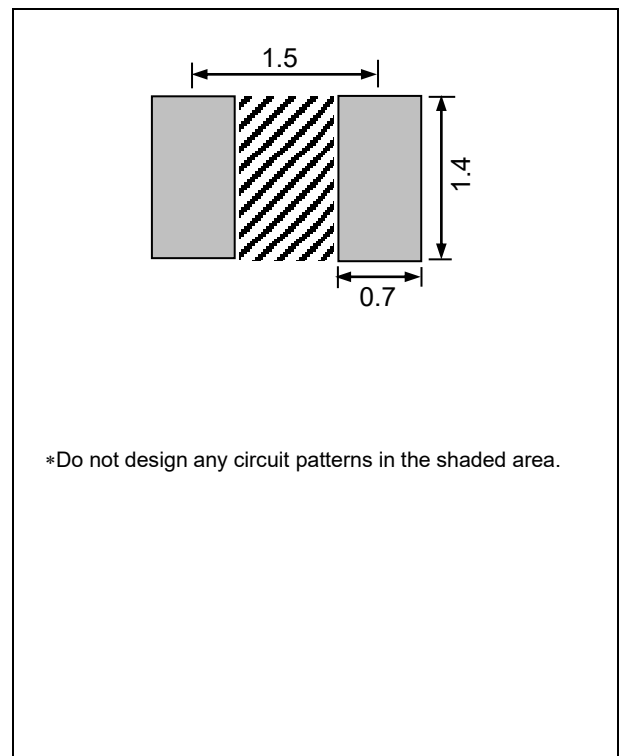
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



kHz RANGE CRYSTAL UNIT

FC3215AN

Low ESR 60 kΩ Max. (-40 °C to +105 °C)


 Product Number
 FC3215AN: X1A000161xxxx16

- Frequency range : 32.768 kHz
- External dimensions : 3.2 x 1.5 x 0.9 mm
- Overtone order : Fundamental
- Applications : IoT Devices, Modules, etc.



Specifications (characteristics)

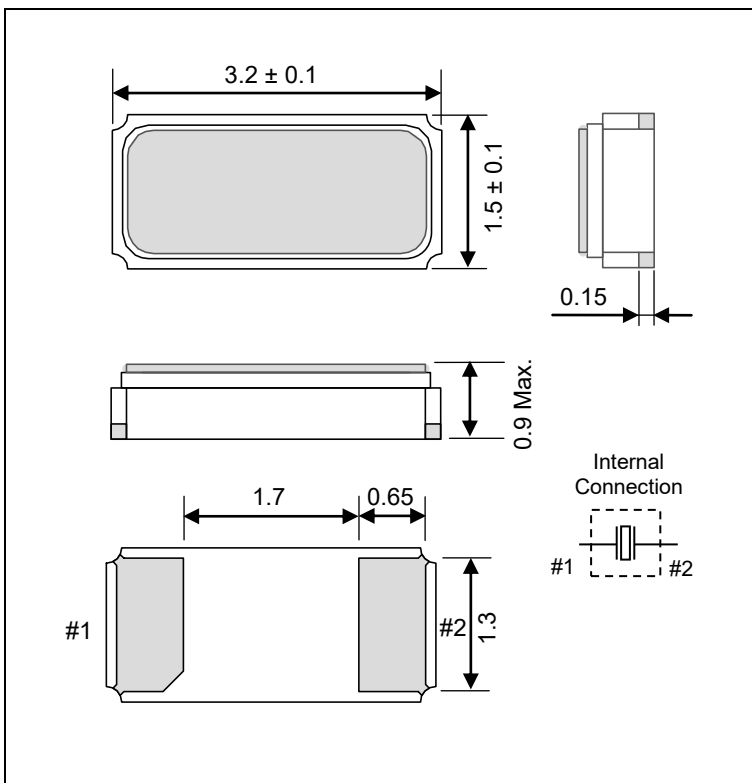
Item	Symbol	Specifications	Conditions / Remarks
Nominal frequency range	f_nom	32.768 kHz	
Storage temperature range	T_stg	-55 °C to +125 °C	Storage as single product.
Operating temperature range	T_use	-40 °C to +105 °C	
Level of drive	DL	0.5 μW Max.	
Frequency tolerance (standard)	f_tol	±20 x 10 ⁻⁶	+25 °C, DL = 0.1 μW
Turnover temperature	Ti	+25 °C ± 5 °C	
Parabolic coefficient	B	-0.04 x 10 ⁻⁶ / °C ² Max.	
Load capacitance	CL	7 pF, 9 pF, 12.5 pF	Please contact us other CL values
Motional resistance (ESR)	R1	35 kΩ Typ..	+25 °C,
		50 kΩ Max.	-40 °C to +85 °C
		60 kΩ Max.	-40 °C to +105 °C
Motional capacitance	C1	8.4 fF Typ.	
Shunt capacitance	C0	1.6 pF Typ.	
Frequency aging	f_age	±3 x 10 ⁻⁶ / year Max.	+25 °C, First year

Product name

Product name FC3215AN 32.768000kHz 12.5 +20.0-20.0
 (Standard form) ① ② ③ ④
 ①Model ②Frequency ③Load capacitance (pF) ④Frequency tolerance (× 10⁻⁶, +25 °C)

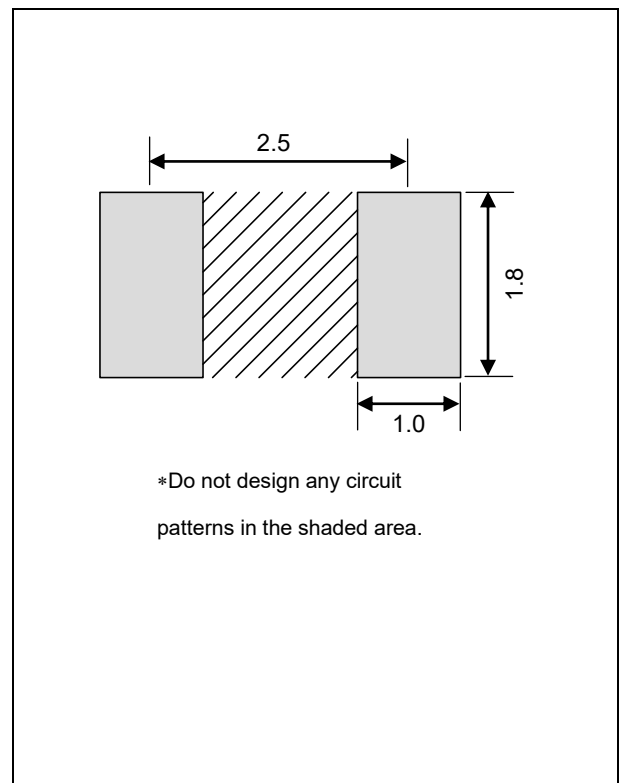
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



kHz RANGE CRYSTAL UNIT



Product Number
 FC-135R : X1A000141xxxx00
 FC-135 : Q13FC1350xxxx00
 FC-135 TYPE : Q14FC1350xxxx00

FC-135R

FC-135 / FC-135 TYPE

- Frequency range : FC-135R / FC-135...32.768 kHz
: FC-135 TYPE...32 kHz to 77.5 kHz
- External dimensions : 3.2 × 1.5 × 0.80 mm
- Overtone order : Fundamental
- Applications : Small communications devices



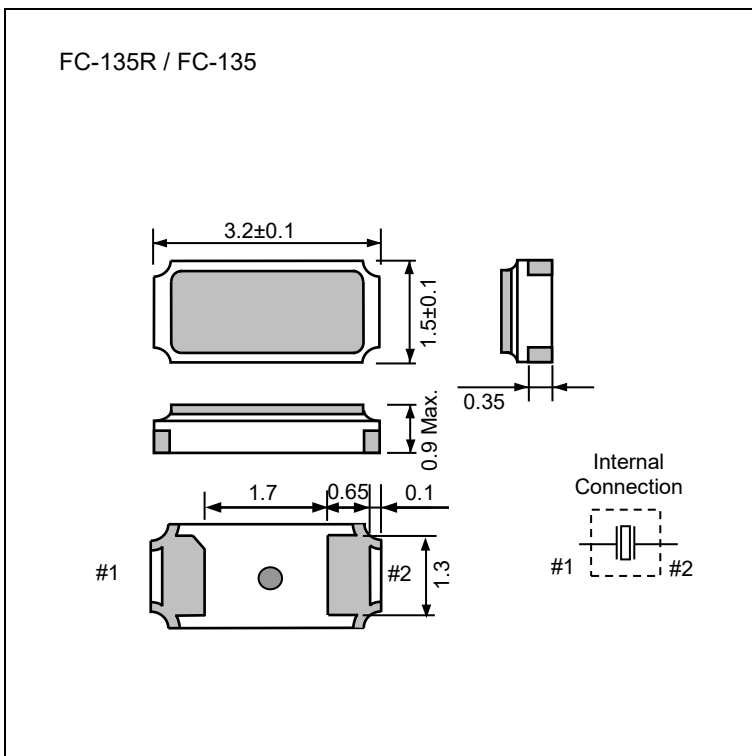
Specifications (characteristics)

Item	Symbol	Specifications			Conditions / Remarks
		FC-135R	FC-135	FC-135 TYPE	
Nominal frequency range	f _{nom}	32.768 kHz	32.768 kHz	32 kHz to 77.5 kHz	Please contact us about available frequencies.
Storage temperature range	T _{stg}	-55 °C to +125 °C			Storage as single product.
Operating temperature range	T _{use}	-40 °C to +85 °C (+105 °C)			Please contact us about +85 °C < T _{use}
Level of drive	DL	0.5 μW (1.0 μW Max.)			Please contact us if you require 1.0 μW Max.
Frequency tolerance (standard)	f _{tol}	±20 × 10 ⁻⁶			+25 °C, DL = 0.1 μW Please ask for tighter tolerance
Turnover temperature	T _i	+25 °C ± 5 °C			
Parabolic coefficient	B	-0.04 × 10 ⁻⁶ / °C ² Max.			
Load capacitance	CL	7 pF, 9 pF, 12.5 pF			Please contact us other CL values
Motional resistance (ESR)	R ₁	50 kΩ Max.	70 kΩ Max.	70 kΩ to 45 kΩ	
Motional capacitance	C ₁	3.4 fF Typ.	3.4 fF Typ.	3.7 fF to 1.6 fF	
Shunt capacitance	C ₀	1.0 pF Typ.	1.0 pF Typ.	1.3 pF to 0.5 pF	
Frequency aging	f _{age}	±3 × 10 ⁻⁶ / year Max.			+25 °C, First year

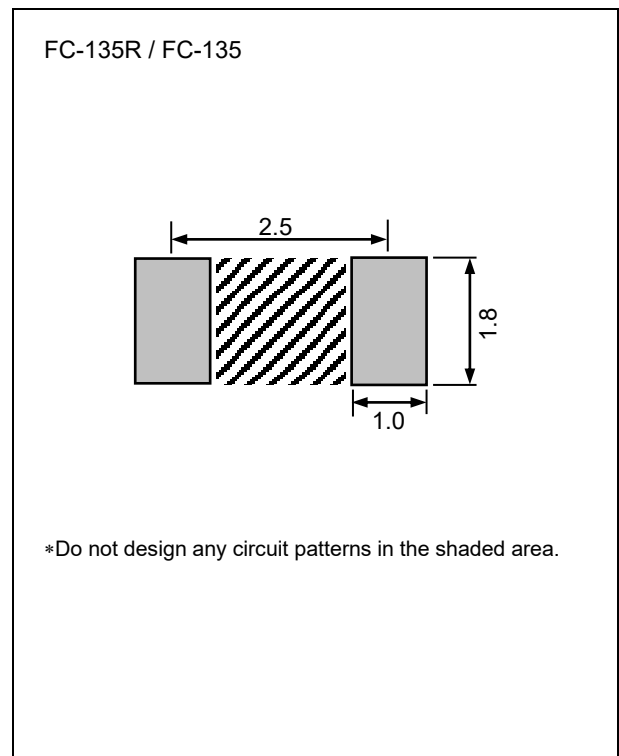
Product name

Product name FC-135R 32.768000kHz 12.5 +20.0-20.0
 (Standard form) ① ② ③ ④
 ①Model ②Frequency ③Load capacitance (pF) ④Frequency tolerance (× 10⁻⁶, +25 °C)

External dimensions (Unit:mm)



Footprint (Recommended) (Unit:mm)



CRYSTAL OSCILLATOR (SPXO)
32.768 kHz



Product Number
SG-3031CM : X1B000391000116

SG-3031CM



- Built-in 32.768 kHz crystal unit allows adjustment-free efficient operation.
- Operation temperature -40 °C to +105 °C
- Use of CMOS IC enables reduction of current consumption.
- V_{IO} controls swing amplitude.

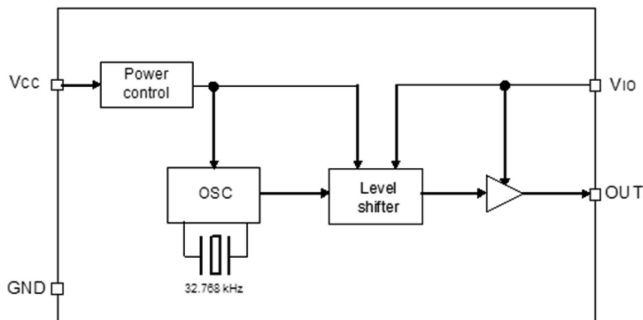
- Applications
Industrial, Security, Smart Meter,
Clock for Time counting and Sleep function

Specifications (characteristics)

Item	Symbol	Specifications	Remarks
Output frequency range	f _o	32.768 kHz	
Supply voltage	V _{CC}	1.2 V to 5.5 V	V _{CC} < 1.5 V, V _{IO} = V _{CC}
Interface power supply voltage	V _{IO}	1.2 V to 5.5 V	
Storage temperature range	T _{stg}	-55 °C to +125 °C	Store as bare product after unpacking
Operating temperature range	T _{use}	-40 °C to +105 °C	
Frequency tolerance	f _{tol}	+5 ± 23 × 10 ⁻⁶	+25 °C, V _{CC} = 3.3 V
Frequency temperature coefficient	fo-Tc	-120 × 10 ⁻⁶ to +10 × 10 ⁻⁶	-20 °C to +70 °C (V _{CC} = 3.3 V, +25 °C is reference)
		-240 × 10 ⁻⁶ to +10 × 10 ⁻⁶	-40 °C to +85 °C (V _{CC} = 3.3 V, +25 °C is reference)
		-420 × 10 ⁻⁶ to +10 × 10 ⁻⁶	-40 °C to +105 °C (V _{CC} = 3.3 V, +25 °C is reference)
Frequency voltage coefficient	fo-V _{CC}	±1 × 10 ⁻⁶ / V Max.	V _{CC} = 1.5 V to 5.5 V
		±5 × 10 ⁻⁶ / V Max.	V _{CC} = 1.2 V to 1.5 V
Current consumption (V _{CC} Pin)	I _{CC}	0.30 μA Typ. / 0.65 μA Max.	V _{CC} = 1.2 V to 5.5 V
Current consumption (V _{CC} +V _{IO} Pin)	I _{CC} +I _{IO}	0.38 μA Typ.	V _{CC} = V _{IO} = 1.2 V, No load condition
		0.65 μA Typ. / 1.3 μA Max. (+105 °C)	V _{CC} = V _{IO} = 3.3 V, No load condition
Symmetry	SYM	45 % to 55 %	1/2V _{CC} (V _{IO}) level, 1.5 V to 5.5 V
		40 % to 60 %	1/2V _{CC} (V _{IO}) level, V _{CC} < 1.5 V
Output voltage	V _{OH} / V _{OL}	V _{IO} - 0.4V Min. / 0.4V Max.	I _{OH} = -0.4mA / I _{OL} = 0.4mA, V _{IO} = 1.5 V to 5.5 V
		V _{IO} - 0.2V Min. / 0.2V Max.	I _{OH} = -0.1mA / I _{OL} = 0.1mA, V _{IO} = 1.2 V to 1.5 V
Output load condition (CMOS)	L _{CMOS}	15 pF Max.	CMOS load
Rise time / Fall time	tr/tf	200 ns Max.	20 % V _{IO} to 80 % V _{IO} level, V _{IO} = 1.2 V to 5.5 V
		100 ns Max.	20 % V _{IO} to 80 % V _{IO} level, V _{IO} = 1.8 V to 5.5 V
Start-up time	t _{str}	0.15 s Typ. / 0.45 s Max.	V _{CC} = 1.5 V to 5.5 V
		1.0 s Max.	V _{CC} = 1.2 V to 1.5 V
Frequency aging	f _{age}	±5 × 10 ⁻⁶ / year Max.	+25 °C, V _{CC} = 3.3 V, First year

Unless otherwise stated, characteristics (specifications) shown in the above table are based on the rated operating temperature and voltage condition.

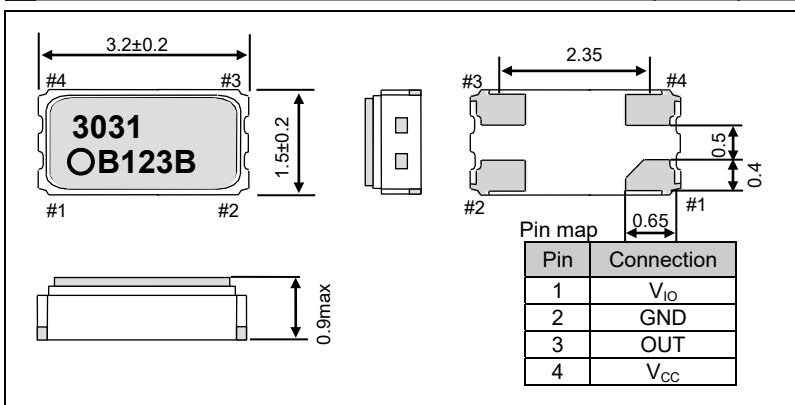
Block diagram



V_{IO} is a power supply pin for OUT output and can also be used as an OE pin. Set V_{IO} to 0 V when setting Disable.
V_{CC} is a power supply pin for operating the 32.768 kHz oscillation. Power consumption can be minimized by minimizing the applied voltage of both power supplies.
However, when V_{CC} < 1.5 V, V_{IO} = V_{CC}.

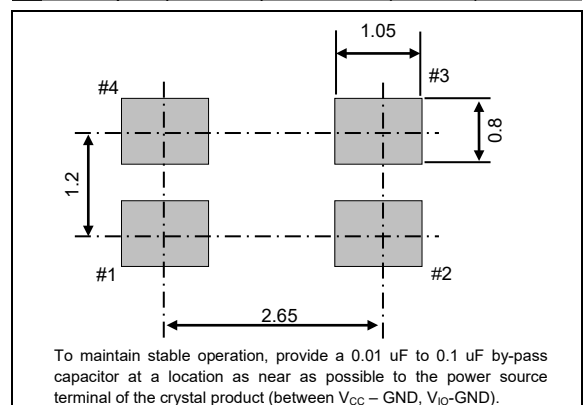
External dimension

(Unit:mm)



Footprint(ference)

(Unit:mm)



TCXO

TG-3541CE



Product Number
 TG-3541CE XA: X1B000351000100
 TG-3541CE XB: X1B000351000200



- Output frequency : 32.768 kHz
- Built-in 32.768 kHz-DTCXO
- Supply voltage : 1.5 V to 5.5 V
- Frequency tolerance :
 - XA $\pm 1.9 \times 10^{-6}$ (0 °C to +50 °C)
 - $\pm 3.4 \times 10^{-6}$ (-40 °C to +85 °C)
 - $\pm 8.0 \times 10^{-6}$ (+85 °C to +105 °C)
 - XB $\pm 3.8 \times 10^{-6}$ (0 °C to +50 °C)
 - $\pm 5.0 \times 10^{-6}$ (-40 °C to +85 °C)
 - $\pm 8.0 \times 10^{-6}$ (+85 °C to +105 °C)

Specifications (characteristics)

Item	Symbol	Specifications	Conditions	
Output frequency range	fo	32.768 kHz		
Supply voltage	V _{CC}	1.5 V to 5.5 V		
Storage temperature range	T _{stg}	-55 °C to +125 °C		
Operating temperature range	T _{use}	-40 °C to +105 °C		
Frequency / Temperature Characteristics	fo_Tc	XA	$\pm 1.9 \times 10^{-6}$	Ta = 0 °C ~ +50 °C
			$\pm 3.4 \times 10^{-6}$	Ta = -40 °C ~ +85 °C
			$\pm 8.0 \times 10^{-6}$	Ta = +85 °C ~ +105 °C
		XB	$\pm 3.8 \times 10^{-6}$	Ta = 0 °C ~ +50 °C
			$\pm 5.0 \times 10^{-6}$	Ta = -40 °C ~ +85 °C
			$\pm 8.0 \times 10^{-6}$	Ta = +85 °C ~ +105 °C
Current consumption	I _{CC}	1.0 μ A Typ./3.0 μ A Max.	No load condition, V _{CC} = 3.0 V	
Disable current	I _{dis}	1.5 μ A Max.	OE=GND, V _{CC} = 3.0 V	
Symmetry	SYM	40 % to 60 %	50 % V _{CC} level	
Output voltage	V _{OH}	80 % V _{CC} Min.	IOH=-0.5 mA, V _{CC} =3.0 V	
	V _{OL}	20% V _{CC} Max.	IOL=0.5 mA, V _{CC} =3.0 V	
Output load condition(CMOS)	L _{CMOS}	30 pF Max.		
Start-up time	t _{str}	1 s Max.	Ta= +25 °C, V _{CC} =1.5 V to 5.5 V	
Frequency aging	f _{aging}	$\pm 3 \times 10^{-6}$	Ta= +25 °C, V _{CC} =3.0 V, first year	

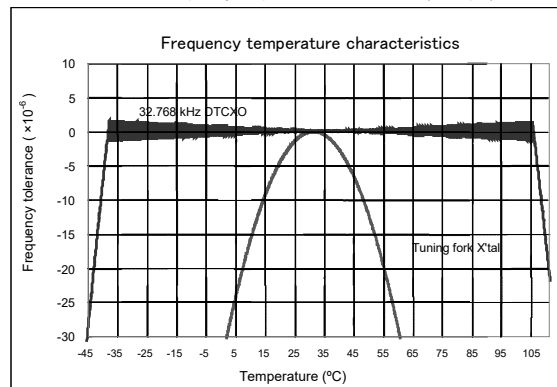
External dimensions (Unit:mm)

Pin	Connection
1	OE
2	V _{CC}
3	V _{CC}
4	OUT
5	GND
6	N.C.
7	GND
8	GND
9	GND
10	N.C.

*OE is active HIGH input, Do not leave floating.
 *N.C. must be left open.
 *Connect all GND / V_{CC} to GND / V_{CC}.

Frequency temperature characteristics

■ 32.768 kHz-DTCXO Frequency temperature characteristics (Example)

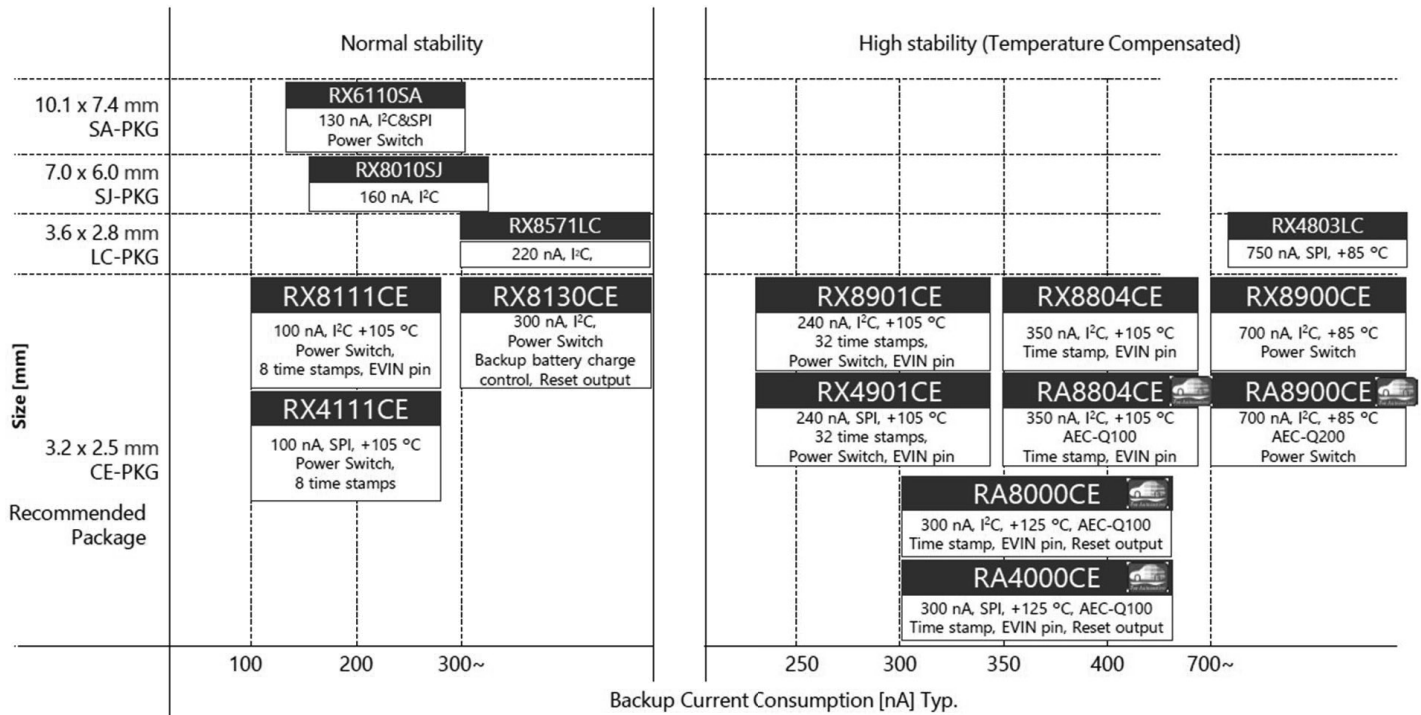


Real Time Clock modules (RTC modules)

Epson's crystal integrated RTC modules offer variety of features such as lower power, accurate clock/date information, Wake-up Timer, Power Switch, and Time-Stamp function. This will help your system lower power with multi-functionality.

Features

- Our lower power design enables our RTC modules to store abnormality on system and time errors even when the main MCU is not in operation
- Tuning Folk Crystal unit and DTCXO (Some products only) integrated RTC modules can assure and manage time information with higher accuracy



Features and Functions

Category	Summary	Use case recommendations, etc
Frequency Tolerance	The variance based on 32.768 kHz under the operating temperature on the integrated crystal inside. For an RTC module, $\pm 23 \times 10^{-6}$ approximately equals to 1 minute per month. Crystal unit inside has a quadratic curve whose peak temperature stays at a room temperature. Therefore, time accuracy varies depending on the external temperature. Epson also offers temperature compensated RTC modules whose clock accuracy has been already adjusted within the operating temperature. (DTCXO)	Temperature compensated models are suitable when.. -your system will be installed outside or a hotter place where the temperature may change drastically. -your system needs time accuracy with a standalone condition (Best accuracy option: $\pm 3.4 \times 10^{-6} \approx 9$ seconds per month)
Backup current consumption	Consumption value when RTC module is individually in operation, whereas the system's main MCU is on sleep mode. " Max." shows the maximum value within the operating temperature (Ta).	Lower power RTC modules are suitable for - your system that operates with a battery. - your system that goes to sleep-mode for a long period Our low power RTC modules can manage time information or system behavior for a long term.
Time-Stamp function	This stores the time data when an event occurs. Time-stamp functions followed by several triggers. For products with EVIN pin, triggers interlock with EVIN pin inputs. For products without EVIN pin, triggers interlock with RTC module's internal operation or Bus access.	RTC modules with time-stamp are suitable for applications such as - anti-tampering for smart meters or security systems - abnormality detection for factory automation
Power Switch	This monitors the main power condition, and this will automatically change the power source to back-up mode when the voltage on the main power reduces. Please refer to application manuals for details since functions vary depending on each product.	By simplifying Diode-OR circuit, it will help reduce a leak current, as well as utilize an engineering resource and PCB area.

3.2 x 2.5 x 1.0t (CE package) Recommendation package

p	Model	Interface	Specifications								Functions					
			Operating temperature Ta [°C]		Frequency Tolerance [x 10 ⁻⁶]				Backup current consumption [μA] 3.0V							
			Min.	Max.	+25 °C	-40 °C to +85 °C	+85 °C to +105 °C	+105 °C to +125 °C	Typ. (25 °C)	Max. (Ta = Max.)	Time Stamp	Power Switch	EVIN pin	User Memory	Timer	Others
21	RX8901CE	I ² C	-40	+105	-	±3.0 ±5.0	±5.0 ±8.0	-	0.24	1.5	32 times	✓	2 or 3	256 bit	24 bit x 1 ch. to 32 years	
23	RX4901CE	SPI	-40	+105	-	±3.0 ±5.0	±5.0 ±8.0	-	0.24	1.5	32 times	✓	0 to 2	256 bit	24 bit x 1 ch. to 32 years	
117	RA8000CE (AEC-Q100)	I ² C	-40	+125	-	±5.0	±8.0	±50.0	0.3	1.7	2 times	-	0 or 2	-	24 bit x 1 ch. to 32 years	Reset output with Delay, SOUT pin
119	RA4000CE (AEC-Q100)	SPI	-40	+125	-	±5.0	±8.0	±50.0	0.3	1.7	2 times	-	0 to 2	-	24 bit x 1 ch. to 32 years	Reset output with Delay, SOUT pin

3.2 x 2.5 x 1.0t (CE package) Recommendation package

P	Model	Interface	Specifications								Functions					
			Operating temperature Ta [°C]		Frequency Tolerance [x 10 ⁻⁶]				Backup current consumption [μA] 3.0V							
			Min.	Max.	+25 °C	-40 °C to +85 °C	+85°C to +105°C	+105°C to +125°C	Typ. (25 °C)	Max. (Ta = Max.)	Time Stamp	Power Switch	EVIN pin	User Memory	Timer	Others
25	RX8111CE	I ² C	-40	+105	±11.5 ±23.0	-	-	-	0.1	0.45	8 times	✓	1	512 bit	24 bit x 1 ch. to 32 years	
26	RX4111CE	SPI	-40	+105			-	-	0.1	0.45	8 times	✓	-	512 bit	24 bit x 1 ch. to 32 years	
27	RX8804CE	I ² C	-40	+105	-	±3.4 ±5.0	±8.0	-	0.35	1.5	1 time	-	1	-	16 bit x 1 ch. to 7.5 years	SOUT pin
28	RX8130CE	I ² C	-40	+85	5±23	-	-	-	0.3	0.5	-	✓	-	-	16 bit x 1 ch. to 7.5 years	Backup battery charge control, Reset output with Delay
29	RX8900CE	I ² C	-40	+85	-	-	-	-	0.7	1.4	-	✓	-	-	12 bit x 1 ch. to 2.8 days	Built-in Temp. Sensor
121	RA8804CE (AEC-Q100)	I ² C	-40	+105	-	±3.4 ±5.0	±8.0	-	0.35	1.5	1 time	-	1	-	16 bit x 1 ch. to 7.5 years	SOUT pin
122	RA8900CE (AEC-Q200)	I ² C	-40	+85	-	-	-	-	0.7	1.4	-	✓	-	-	12 bit x 1 ch. to 2.8 days	Built-in Temp. Sensor

3.6 x 2.8 x 1.2t (LC package / VSOJ-12 pin)

P	Model	Interface	Specifications								Functions					
			Operating temperature Ta [°C]		Frequency Tolerance [x 10 ⁻⁶]				Backup current consumption [μA] 3.0 V							
			Min.	Max.	+25 °C	-40 °C to +85 °C	-40 °C to +105 °C	Typ. (25 °C)	Max. (Ta = Max.)	Time Stamp	Power Switch	EVIN pin	User Memory	Timer	Others	
30	RX-8803LC	I ² C	-40	+85	-	±3.4 ±5.0	-	0.75	2.1	-	-	1	-	-	to 2.8 days	Time sync. with 1 PPS
31	RX-4803LC	SPI						0.75	2.1	-	-	1	-	to 2.8 days	Time sync. with 1 PPS	
32	RX-8035LC	I ² C						5±23	0.4	1.2	1 time	✓	1	-	-	
33	RX-4035LC	SPI			5±5 0±5	0.4	1.2	-	-	1	-	-				
34	RX-8731LC	I ² C			5±23	-	-	0.35	1.4	-	-	-	EEPROM: 80 bit ID-ROM: 48 bit	to 2.8 days		
35	RX-8571LC	I ² C			0.22	0.4	-	-	-	-	-	128 bit	to 7.5 years	DAS pin		
36	RX-4571LC	SPI			0.32	0.95	-	-	-	-	-	-	to 2.8 days			
37	RX-8564LC	I ² C	0.275	0.7	-	-	-	-	-	-	-	to 255 min.				

6.3 x 5.2 x 1.4t (NB package / SON-22 pin)

P	Model	Interface	Specifications								Functions					
			Operating temperature Ta [°C]		Frequency Tolerance [x 10 ⁻⁶]				Backup current consumption [μA] 3.0 V							
			Min.	Max.	+25 °C	-40 °C to +85 °C	-40 °C to +105 °C	Typ. (25 °C)	Max. (Ta = Max.)	Time Stamp	Power Switch	EVIN pin	User Memory	Timer	Others	
Web site	RX-8025NB	I ² C	-40	+85	5±5	-	-	0.48	1.2	-	-	-	-	-	-	
	RX-4045NB	SPI			0±5			0.48	1.2	-	-	-	-	-		
	RX-8571NB	I ² C			5±23			0.2	0.4	-	-	-	128 bit	to 7.5 years	DAS pin	
	RX-4571NB	SPI			0.32			0.95	-	-	-	-	-	to 2.8 days		

7.0 x 6.0 x 2.65t (SJ package / SOP-8 pin)

P	Model	Interface	Specifications								Functions					
			Operating temperature Ta [°C]		Frequency Tolerance [x 10 ⁻⁶]				Backup current consumption [μA] 3.0 V							
			Min.	Max.	+25 °C	-40 °C to +85 °C	-40 °C to +105 °C	Typ. (25 °C)	Max. (Ta = Max.)	Time Stamp	Power Switch	EVIN pin	User Memory	Timer	Others	
38	RX8010SJ	I ² C	-40	+85	5±23	-	-	0.16	0.32	-	-	-	128 bit	to 7.5 years	Common pin connection with SOP-8	

10.1 x 7.4 x 3.3t (SA package / SOP-14 pin)

P	Model	Interface	Specifications						Functions								
			Operating temperature Ta [°C]		Frequency Tolerance [x 10 ⁻⁶]			Backup current consumption [μA] 3.0 V		Time Stamp	Power Switch	EVIN pin	User Memory	Timer	Others		
			Min.	Max.	+25 °C	-40 °C to +85 °C	-40 °C to +105 °C	Typ. (25 °C)	Max. (Ta = Max.)								
39	RX6110SA	SPI & I ² C	-40	+85	5±23	-	-	0.16	0.32	-	✓	-	128 bit	to 7.5 years			
29	RX8900SA	I ² C								0.7	1.4	-	✓	-	-	to 2.8 days	Built-in Temp. Sensor
30	RX-8803SA	I ² C						-	±3.4 ±5.0	0.75	2.1	-	-	1	-	to 2.8 days	Time sync. with 1 PPS
31	RX-4803SA	SPI								0.75	2.1	-	-	1	-	to 2.8 days	Time sync. with 1 PPS
32	RX-8035SA	I ² C						5±23		0.35	1.2	1 time	✓	2	-	-	
33	RX-4035SA	SPI						5±5 0±5		0.35	1.2	1 time	✓	2	-	-	
40	RX-8025SA	I ² C						5±5	-	0.48	1.2	-	-	-	-	-	
41	RX-4045SA	SPI						0±5		0.48	1.2	-	-	-	-	-	
35	RX-8571SA	I ² C						5±23		0.2	0.4	-	-	-	128 bit	to 7.5 years	DAS pin
36	RX-4571SA	SPI								0.32	0.95	-	-	-	-	to 2.8 days	
Web Site	RA8803SA (AEC-Q200)	I ² C							±3.4 ±5.0	0.75	2.1	-	-	1	-	to 2.8 days	Time sync. with 1 PPS
Web Site	RA4803SA (AEC-Q200)	SPI								0.75	2.1	-	-	1	-	to 2.8 days	Time sync. with 1 PPS
Web Site	RA-4565SA (AEC-Q200)	SPI						5±23		0.8	1.6	-	-	-	-	to 255 min.	



Product Number (2,000 pcs / Reel)
RX8901CE XS A0 : X1B000481000115
RX8901CE XB A0 : X1B000481000215
RX8901CE XS B0 : X1B000481000315
RX8901CE XB B0 : X1B000481000415

REAL TIME CLOCK MODULE (I²C-Bus)

Built-in 32.768 kHz-DTCXO, +105°C operating temperature,
 Low current consumption, Built-in power supply switching circuit and
 Time stamp function up to 32 records

RX8901CE

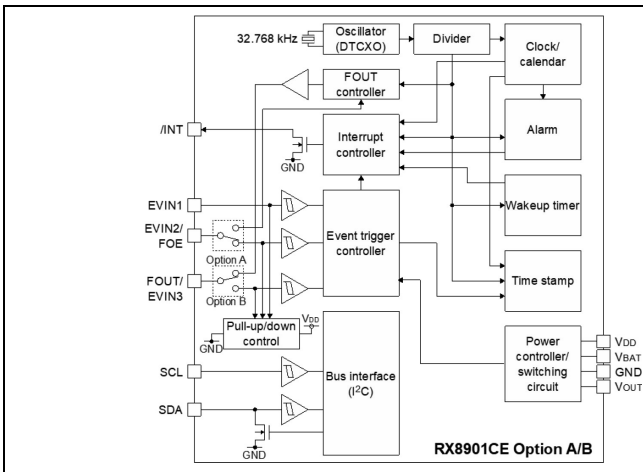
- Built in frequency adjusted 32.768 kHz crystal unit and DTCXO
- Interface Type : I²C-Bus
- Current consumption : 240 nA / 3 V (Typ.)
- Auto power switching function : Automatically switches to backup power supply by monitoring the V_{DD} / V_{BAT} voltage
- Time stamp function : Maximum 32 time stamps
- Interrupt output : Wake up every hour or every minute or every second
- Alarm interruption : Day, date, hour, minute, second
- Auto repeat wakeup timer interruption
- Self-monitoring interruption : Crystal oscillation stop, V_{BAT} low, V_{DD} low



RX8901CE
 (3.2 × 2.5 mm, t = 1.0 mm Max.)

Block diagram

Overview

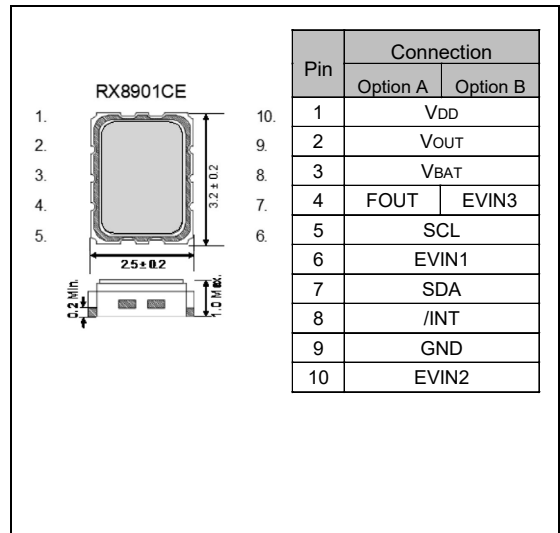


- Interface type
I²C-Bus interface Fast-Mode 400 kHz
- High stability
 XS : ±3.0 × 10⁻⁶ / -40 °C to +85 °C (Monthly rate: ±8 seconds)
 ±5.0 × 10⁻⁶ / +85 °C to +105 °C (Monthly rate: ±13.2 seconds)
 XB : ±5.0 × 10⁻⁶ / -40 °C to +85 °C (Monthly rate: ±13.2 seconds)
 ±8.0 × 10⁻⁶ / +85 °C to +105 °C (Monthly rate: ±21 seconds)
- Time stamp function
 Trigger source: External event (EVIN) input, voltage drop/oscillation stop status detected, command input from the host
 Record data: 1/1024 seconds to 1 second, seconds, minutes, hours, days, months, years
 Number of recordable events: Maximum 32 events
- Backup power supply switching function
 The V_{DD} and V_{BAT} voltages are monitored to switch between Normal mode (V_{DD} operation) and Backup mode (V_{BAT} operation).
- Clock output (FOUT)
 Selectable from 32.768 kHz, 1024 Hz and 1 Hz outputs
 Output can be controlled by a register or FOE input (selectable with a register).

Pin Function

Terminal connection / External dimensions (Unit: mm)

Signal Name	I / O	Function
EVIN1,2,3	Input	External event input pins. Detectable even in Backup mode. Pull-up and pull-down is configurable by the registers
SCL	Input	Serial clock input pin
SDA	Input / Output	Serial data input and output pin
FOUT	Output	Frequency output pin (CMOS). 32.768 kHz (default), 1024 Hz or 1 Hz clock output is selectable. This pin can be switched to the wakeup timer interrupt output (CMOS)
/INT	Output	Interrupt output pin (N-ch. open drain). The wakeup timer, time update, alarm, and/or event detection interrupt signals can be selected to output from this pin. When two or more signals are selected, they are NORed before being output. This pin is effective even in Backup mode.
VDD	-	Power-supply pin
VOUT	-	Internal operating voltage output pin Connect a 1 μF bypass capacitor to this pin
VBAT	-	Backup power supply pin Connect a backup power supply such as a large-size capacitor, secondary battery, or primary battery. The operating power voltage is supplied from this pin to the internal circuits in Backup mode.
GND	-	Ground pin



Specifications (characteristics)

* Refer to application manual for details

Recommended Operating Conditions

Item	Symbol	Condition	Min.	Typ.	Max.	unit
Operating voltage	V _{DD}	-	1.6	3.0	5.5	V
Clock supply voltage	V _{CLK}	-	1.1	3.0	5.5	V
Operating Temperature	T _a	-	-40	+25	+105	°C
VDD detection voltage	-VDET1	V _{DD} , Fall	1.35	1.45	1.55	V

Frequency Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	unit	
Frequency tolerance	Δf/f	XS	T _a = -40 to +85 °C	-3	-	+3	× 10 ⁻⁶
			T _a = +85 to +105 °C	-5	-	+5	
		XB	T _a = -40 to +85 °C	-5	-	+5	
			T _a = +85 to +105 °C	-8	-	+8	
start-up time	t _{STA}	T _a = +25 °C, V _{DD} = 1.6 V ~ 5.5 V	-	0.5	1.0	s	

Current consumption

Item	Symbol	Condition	Min.	Typ.	Max.	unit
I _{DD}	IBAT	V _{BAT} = 3.0 V, /INT= Hi-Z, FOUT: Output OFF (Hi-Z), Temperature compensation interval: 2 s, FSEL1= FSEL0 = 1, INIEN = 1, CHGEN = 0, SCL = SDA = L	-	240	1500	nA
	I _{32k}	V _{DD} = 3.0 V, /INT= Hi-Z, FOUT: 32 kHz output, C _L = 0 pF, Temperature compensation interval: 2 s, FSEL1 = FSEL0 = 1, INIEN = 1, CHGEN = 0, SCL = SDA = H	-	1.0	3.0	μA

Option

I/F	Option	EVIN pin Number	/INT pin Number	FOUT	Number of time stamps recorded by EVIN pin trigger	
					FIFO Mode	Direct Mode
I ² C	A	2	1	Yes	32 times	22 times
	B	3	1	-	32times	32 times



Product name

RX8901CE XS A0
① ② ③

- ① Model CE type package 3.2 x 2.5 x 1.0 mm
- ② Frequency tolerance
 - XS: $\pm 3.0 \times 10^{-6}$ / -40 °C to +85 °C (Monthly rate: ± 8 seconds)
 - $\pm 5.0 \times 10^{-6}$ / +85 °C to +105 °C (Monthly rate: ± 13.2 seconds)
 - XB: $\pm 5.0 \times 10^{-6}$ / -40 °C to +85 °C (Monthly rate: ± 13.2 seconds)
 - $\pm 8.0 \times 10^{-6}$ / +85 °C to +105 °C (Monthly rate: ± 21 seconds)
- ③ Pin Option
 - A: Option A
 - B: Option B



Product Number (2,000 pcs / Reel)
RX4901CE XS A0 : X1B000471000115
RX4901CE XB A0 : X1B000471000215
RX4901CE XS B0 : X1B000471000315
RX4901CE XB B0 : X1B000471000415
RX4901CE XS C0 : X1B000471000515
RX4901CE XB C0 : X1B000471000615
RX4901CE XS D0 : X1B000471000715
RX4901CE XB D0 : X1B000471000815



RX4901CE
 (3.2 × 2.5 mm, t = 1.0 mm Max.)

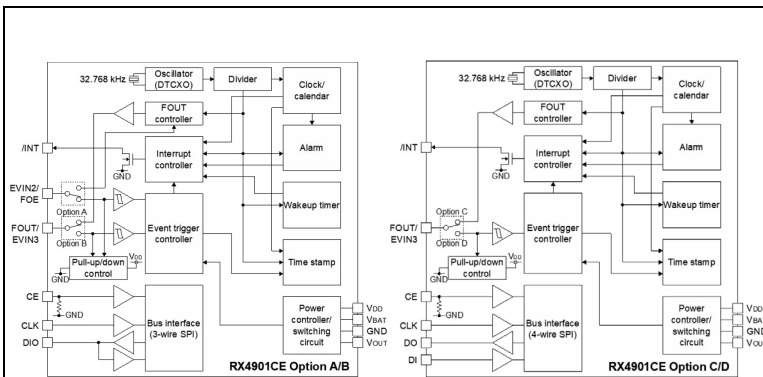
REAL TIME CLOCK MODULE (SPI-Bus)

Built-in 32.768 kHz-DTCXO, +105°C operating temperature,
 Low current consumption, Built-in power supply switching circuit and
 Time stamp function up to 32 records

RX4901CE

- Built in frequency adjusted 32.768 kHz crystal unit and DTCXO
- Interface Type : 3 wire / 4 wire SPI-Bus
- Current consumption : 240 nA / 3 V (Typ.)
- Auto power switching function : Automatically switches to backup power supply by monitoring the V_{DD} / V_{BAT} voltage
- Time stamp function : Maximum 32 time stamps
- Interrupt output : Wake up every hour or every minute or every second
- Alarm interruption : Day, date, hour, minute, second
- Auto repeat wakeup timer interruption
- Self-monitoring interruption : Crystal oscillation stop, V_{BAT} low, V_{DD} low

Block diagram



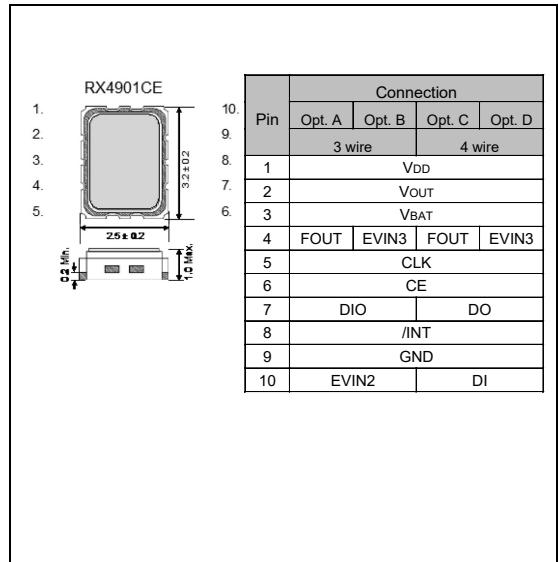
Overview

- Interface type : 3 wire / 4 wire SPI-Bus
- High stability
 XS : ±3.0 × 10⁻⁶ / -40 °C to +85 °C (Monthly rate: ±8 seconds)
 : ±5.0 × 10⁻⁶ / +85 °C to +105 °C (Monthly rate: ±13.2 seconds)
 XB : ±5.0 × 10⁻⁶ / -40 °C to +85 °C (Monthly rate: ±13.2 seconds)
 : ±8.0 × 10⁻⁶ / +85 °C to +105 °C (Monthly rate: ±21 seconds)
- Time stamp function
 Trigger source: External event (EVIN) input, voltage drop/oscillation stop status detected, command input from the host
 Record data: 1/1024 seconds to 1 second, seconds, minutes, hours, days, months, years
 Number of recordable events: Maximum 32 events
- Backup power supply switching function
 The V_{DD} and V_{BAT} voltages are monitored to switch between Normal mode (V_{DD} operation) and Backup mode (V_{BAT} operation).
- Clock output (FOUT)
 Selectable from 32.768 kHz, 1024 Hz and 1 Hz outputs
 Output can be controlled by a register or FOE input (selectable with a register).

Pin Function

Signal Name	I / O	Function
EVIN1,2,3	Input	External event input pins. Detectable even in Backup mode. Pull-up and pull-down is configurable by the registers
CE	Input	Slave select input pin A pull-down resistor (Typ. 300 kΩ) is included
CLK	Input	Serial clock input pin
DI	Input	Serial data input pin (4 wire)
DO	Output	Serial data Output pin (4 wire)
DIO	Input / Output	Serial data input/output pin (3 wire)
FOUT	Output	Frequency output pin (CMOS). 32.768 kHz (default), 1024 Hz or 1 Hz clock output is selectable. This pin can be switched to the wakeup timer interrupt output (CMOS)
/INT	Output	Interrupt output pin (N-ch. open drain). The wakeup timer, time update, alarm, and/or event detection interrupt signals can be selected to output from this pin. When two or more signals are selected, they are NORed before being output. This pin is effective even in Backup mode.
V _{DD}	-	Power-supply pin
V _{OUT}	-	Internal operating voltage output pin Connect a 1.0 μF bypass capacitor to this pin.
V _{BAT}	-	Backup power supply pin Connect a backup power supply such as a large-size capacitor, secondary battery, or primary battery. The operating power voltage is supplied from this pin to the internal circuits in Backup mode.
GND	-	Ground pin

Terminal connection / External dimensions (Unit: mm)



Specifications (characteristics)

* Refer to application manual for details

Recommended Operating Conditions

Item	Symbol	Condition	Min.	Typ.	Max.	unit
Operating voltage	V _{DD}	-	1.6	3.0	5.5	V
Clock supply voltage	V _{CLK}	-	1.1	3.0	5.5	V
Operating Temperature	T _a	-	-40	+25	+105	°C
V _{DD} detection voltage	-VDET1	V _{DD} , Fall	1.35	1.45	1.55	V

Frequency Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	unit	
Frequency tolerance	Δf/f	XS	T _a = -40 to +85 °C	-3	-	+3	× 10 ⁻⁶
			T _a = +85 to +105 °C	-5	-	+5	
		XB	T _a = -40 to +85 °C	-5	-	+5	
			T _a = +85 to +105 °C	-8	-	+8	
start-up time	t _{STA}	T _a = +25 °C, V _{DD} = 1.6 V ~ 5.5 V	-	0.5	1.0	s	

Current consumption

Item	Symbol	Condition	T _a = -40 °C to +105 °C			
			Min.	Typ.	Max.	unit
I _{DD}	IBAT	V _{BAT} = 3.0 V, /INT = Hi-Z, FOUT: Output OFF (Hi-Z), Temperature compensation interval: 2 s, FSEL1 = FSEL0 = 1, INIEN = 1, CHGEN = 0, CE = L	-	240	1500	nA
	I _{32k}	V _{DD} = 3.0 V, /INT = Hi-Z, FOUT: 32 kHz output, C _L = 0 pF, Temperature compensation interval: 2 s, FSEL1 = FSEL0 = 0, INIEN = 1, CHGEN = 0, CE = L	-	1.0	3.0	μA

Option

I/F	Option	EVIN pin Number	/INT pin Number	FOUT	Number of time stamps recorded by EVIN terminal trigge	
					FIFO Mode	Direct Mode
SPI 3 wire	A	1	1	Yes	32 times	12 times
	B	2	1	-	32 times	22 times
SPI 4 wire	C	0	1	Yes	0 time	0 time
	D	1	1	-	32 times	10 times



Product name

RX4901CE XS A0
① ② ③

- ① Model CE type package 3.2 x 2.5 x 1.0 mm
- ② Frequency tolerance
 - XS: $\pm 3.0 \times 10^{-6}$ / -40 °C to +85 °C (Monthly rate: ± 8 seconds)
 - $\pm 5.0 \times 10^{-6}$ / +85 °C to +105 °C (Monthly rate: ± 13.2 seconds)
 - XB: $\pm 5.0 \times 10^{-6}$ / -40 °C to +85 °C (Monthly rate: ± 13.2 seconds)
 - $\pm 8.0 \times 10^{-6}$ / +85 °C to +105 °C (Monthly rate: ± 21 seconds)
- ③ Pin Option
 - A: Option A
 - B: Option B
 - C: Option C
 - D: Option D

REAL TIME CLOCK MODULE (I²C-Bus)

Time stamp function and Low current consumption



Product Number (2,000 pcs / Reel)
RX8111CE (A grade): X1B000421000115
RX8111CE (B grade): X1B000421000215

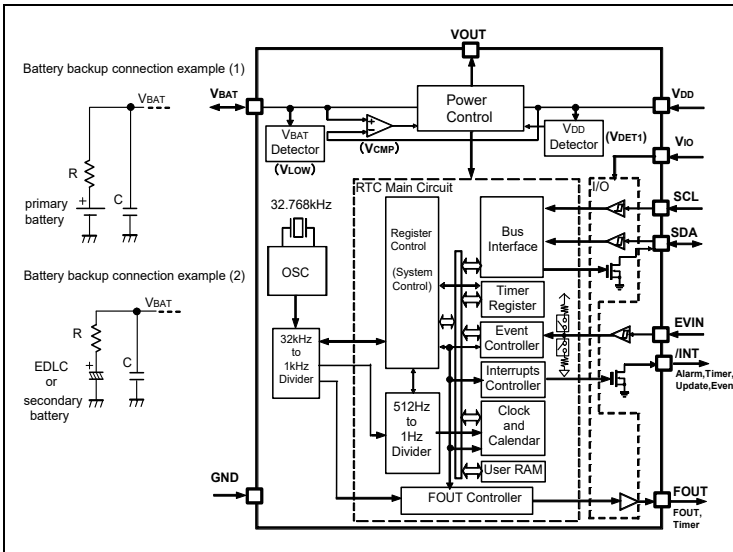
RX8111CE

- Built in frequency adjusted 32.768 kHz crystal unit
- Operating Temperature : -40 °C to +105 °C
- Interface Type : I²C -Bus
- Low backup current : 100 nA Typ. / 3 V
- Auto power switching function : Automatically switches to backup power supply by monitoring the V_{DD} voltage.
- Time stamp function : 8 times stamped from year to 1/256 seconds
- Interrupt output : Wake up every minute or every second
- Alarm interruption : Day, date, hour, minute, second
- Auto repeat wakeup timer interruption
- Self-monitoring interruption : Crystal oscillation stop, V_{BAT} low, V_{DD} low



RX8111CE
 (3.2 x 2.5 mm , t = 1.0 mm Max.)

Block diagram



Overview

- Interface type
I²C-Bus interface Fast-Mode 400 kHz
- Auto power switch function
The V_{DD} voltage is monitored and it switches to the backup power supply by the automatic operation
Backup power supply switching voltage 1.2V Min.
- Clock output function
Output frequency is selectable from 32.768 kHz, 1024 Hz, 1 Hz
- Wakeup timer function
Selectable from 244 μs to 32 years (24 bit 1 ch.)
Timer source clock selectable from 1/60 Hz, 1 Hz, 64 Hz, 4096 Hz
Auto release after interrupt output from /INT pin at timer completes
This operation is auto repeat with a selected cycle, it can be used like a watchdog timer
- Time stamp function
8 times stamped from year to 1/256 seconds
The time stamp trigger inputs from EVIN pin, self-monitoring and I²C software command
EVIN pin has function of chattering-cancel
- Alarm function
It is possible program from year to second
- Self-monitoring interruption
Crystal oscillation stop, V_{BAT} low, V_{DD} low

Pin Functin

Signal Name	I / O	Function
EVIN	Input	External event input pin (Pull up/down and polarity are selectable by software)
SCL	Input	Serial clock input pin
SDA	Input / Output	Data input and output pin
FOUT	Output	Frequency output (CMOS) (frequency selection: 32.768 kHz, 1024 Hz, 1 Hz)
/INT	Output	Interrupts output by Alarm and Timer events (N-ch. open drain)
V _{DD}	-	Power-supply pin Possible to supply different voltage from V _{IO}
V _{IO}	-	Interface power supply pin Input to supply the voltage same as a host
V _{OUT}	-	Internal voltage output pin Connect bypass capacitor of 1.0 μF surely
V _{BAT}	-	This is a power supply pin for backup battery Connect an EDLC, a secondary battery, a primary battery In the backup voltage range, supplied to IC, from this pin
GND	-	Ground pin

Terminal connection / External dimensions (Unit: mm)

The diagram shows the physical layout of the RX8111CE component with 10 pins numbered 1 to 10. Dimensions are provided: width is 2.5 ± 0.2 mm, height is 3.2 ± 0.2 mm, and pin height is 1.0 mm Max. with a 0.2 mm Min. offset. The pin list is as follows:

Pin	Connection
1	V _{DD}
2	V _{OUT}
3	V _{BAT}
4	FOUT
5	SCL
6	EVIN
7	SDA
8	V _{IO}
9	GND
10	/INT

Specifications (characteristics)

* Refer to application manual for details

Recommended Operating Conditions

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Operating supply voltage	V _{DD}	-	1.6	3.0	5.5	V
Clock supply voltage	V _{CLK}	-	1.1	3.0	5.5	V
Operating temperature	T _a	-	-40	+25	+105	°C
V _{DD} detect voltage	-V _{DET1}	V _{DD} , Fall	1.20	1.40	1.60	V

Frequency characteristics

Item	Grade	Symbol	Conditions	Min.	Typ.	Max.	Unit
Frequency tolerance	A	Δf / f	T _a = +25 °C V _{DD} = 3.0 V	-11.5	-	+11.5	× 10 ⁻⁶
	B			-23	-	+23	
Oscillation start-up time		t _{STA}	V _{DD} = 2.75 V to 5.5 V	-	0.3	1.0	s

Current consumption characteristics

T_a = -40 °C to +105 °C

Symbol	Conditions	T _a (°C)	Typ.	Max.	Unit
I _{DD}	Input pins = "L", FOUT = OFF, /INT = OFF, V _{DD} = V _{IO} = 3.0 V, CHGEN = 0b, INIEN = 0b,	-40 ~ +85	100	450	nA
		-40 ~ +105	100	1000	
I _{32k}	Input pins = "L", FOUT = 32.768 kHz, /INT = OFF, V _{DD} = V _{IO} = 3.0 V, FOUT pin CL = 15 pF, CHGEN = 0b, INIEN = 1b	-40 ~ +85	2.0	3.0	μA
		-40 ~ +105	2.0	3.5	

REAL TIME CLOCK MODULE (SPI-Bus)

Time stamp function and Low current consumption

RX4111CE

- Built in frequency adjusted 32.768 kHz crystal unit
- Operating Temperature : -40 °C to +105 °C
- Interface Type : SPI -Bus 4 wire
- Low backup current : 100 nA Typ. / 3 V
- Auto power switching function: Automatically switches to backup power supply by monitoring the VDD voltage.
- Time stamp function : 8 times stamped from year to 1/256 seconds
- Interrupt output : Wake up every minute or every second
- Alarm interruption : Day, date, hour, minute, second
- Auto repeat wakeup timer interruption
- Self-monitoring interruption: Crystal oscillation stop, V_{BAT} low, V_{DD} low



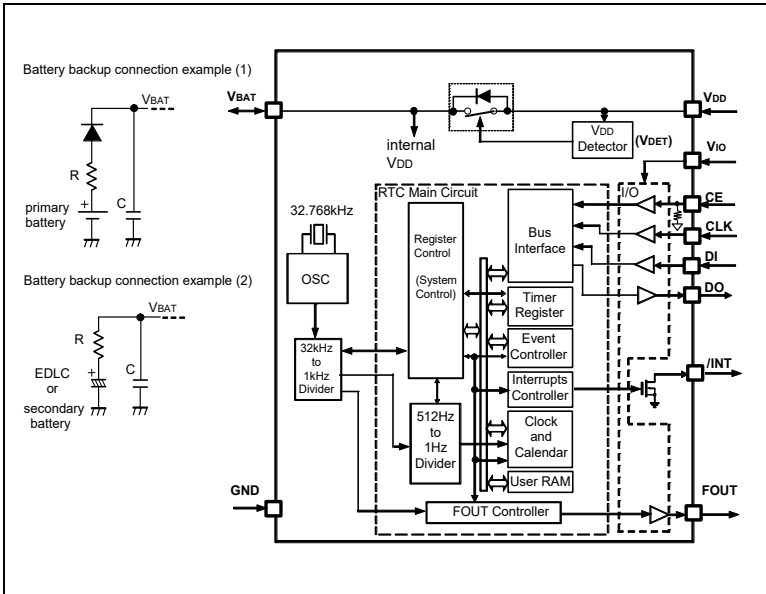
Product Number (2,000 pcs / Reel)
RX4111CE (A grade): X1B000431000115
RX4111CE (B grade): X1B000431000215



RX4111CE
 (3.2 x 2.5 mm , t = 1.0 mm Max.)

Block diagram

Overview

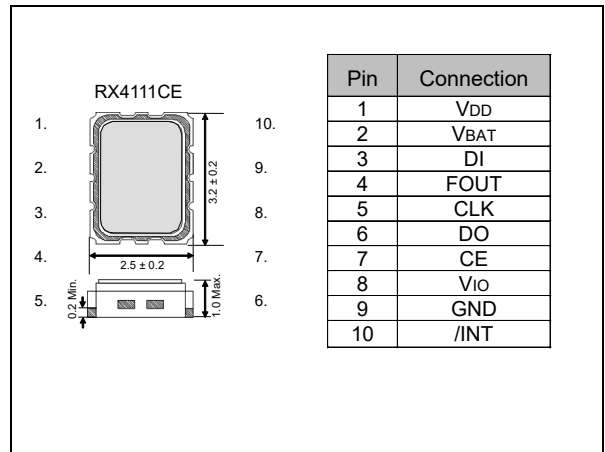


- Interface type
SPI-Bus interface (4 wire, 1 MHz)
- Auto power switch function
The V_{DD} voltage is monitored and it switches to the backup power supply by the automatic operation
Backup power supply switching voltage 1.2V Min.
- Clock output function
Output frequency is selectable from 32.768 kHz, 1024 Hz, 1 Hz
- Wakeup timer function
Selectable from 244 μs to 32 years (24 bit 1 ch.)
Timer source clock selectable from 1/60 Hz, 1 Hz, 64 Hz, 4096 Hz
Auto release after interrupt output from /INT pin at timer completes
This operation is auto repeat with a selected cycle, it can be used like a watchdog timer
- Time stamp function
8 times stamped from year to 1/256 seconds
The time stamp trigger inputs from self-monitoring and SPI command
- Alarm function
It is possible program from year to second
- Self-monitoring interruption
Crystal oscillation stop, V_{BAT} low, V_{DD} low

Pin Functin

Terminal connection / External dimensions (Unit: mm)

Signal Name	I / O	Function
CE	Input	Chip enables input pin
CLK	Input	Serial clock input pin
DI	Input	Data input pin
DO	Output	Data output pin
FOUT	Output	Frequency output (CMOS) (frequency selection: 32.768 kHz, 1024 Hz, 1 Hz)
/INT	Open-Drain Output	Interrupts output by Alarm and Timer events. (N-ch. open drain)
VDD	-	Power supply pin Possible to supply different voltage from V _{IO}
V _{IO}	-	Interface power supply pin Input to supply the voltage same as a host
V _{BAT}	-	Power supply pin for backup battery Connect an EDLC, a secondary battery, a primary battery In the backup voltage range, supplied to IC, from this pin
GND	-	Ground pin



Specifications (characteristics)

* Refer to application manual for details

Recommended Operating Conditions

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Operating supply voltage	V _{DD}	-	1.6	3.0	5.5	V
Clock supply voltage	V _{CLK}	-	1.1	3.0	5.5	V
Operating temperature	T _a	-	-40	+25	+105	°C
VDD detect voltage	-V _{DET1}	V _{DD} , Fall	1.20	1.40	1.60	V

Frequency characteristics

Item	Grade	Symbol	Conditions	Min.	Typ.	Max.	Unit
Frequency tolerance	A	Δf/f	T _a = +25 °C V _{DD} = 3.0 V	-11.5	-	+11.5	× 10 ⁻⁶
	B		-23	-	+23		
Oscillation start-up time		t _{STA}	V _{DD} = 2.75 V to 5.5 V	-	0.3	1.0	s

Current consumption characteristics

T_a = -40 °C to +105 °C

Symbol	Conditions	T _a (°C)	Typ.	Max.	Unit
I _{DD}	Input pins = "L", FOUT = OFF, /INT = OFF, V _{DD} = V _{IO} = 3.0 V, CHGEN = 0b, INIEN = 0b,	-40 ~ +85	100	450	nA
		-40 ~ +105	100	1000	
I _{32k}	Input pins = "L", FOUT = 32.768 kHz, /INT = OFF, V _{DD} = V _{IO} = 3.0 V, FOUT pin CL = 15 pF, CHGEN = 0b, INIEN = 1b	-40 ~ +85	2.0	3.0	μA
		-40 ~ +105	2.0	3.5	

REAL TIME CLOCK MODULE (I²C-Bus)

Built-in 32.768 kHz DTCXO, High Stability



Product Number (2,000 pcs / Reel)
RX8804CE XA: X1B000371000100
RX8804CE XB: X1B000371000200

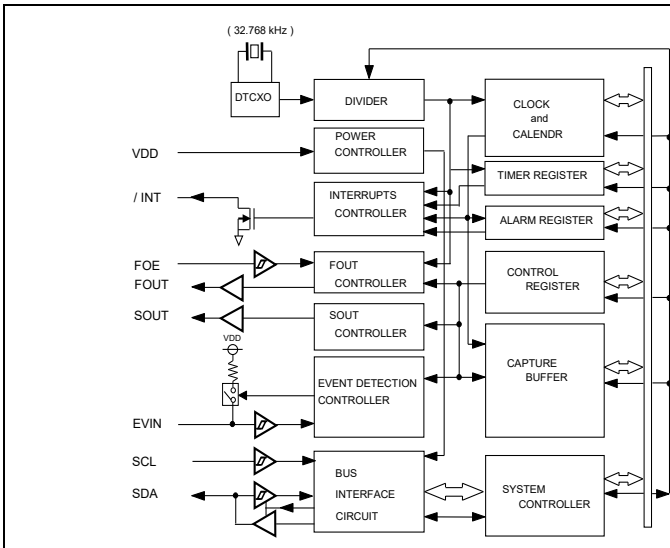
RX8804CE

- Built-in frequency adjusted 32.768 kHz crystal unit and DTCXO
- Interface Type : I²C-Bus
- Selectable clock output : 32.768 kHz, 1024 Hz, 1 Hz
- Time stamp function : 1 time stamped from year to second
- Interrupt output : Wake up every minute or every second
- Alarm interruption : Day, date, hour, minute
- Auto repeat wakeup timer interruption
- Self-monitoring interruption : Crystal oscillation stop, V_{BAT} LOW, V_{DD} low
- SOUT pin outputs that selected flag bit value



RX8804CE
 (3.2 × 2.5 mm, t = 1.0 mm Max.)

Block diagram



Overview

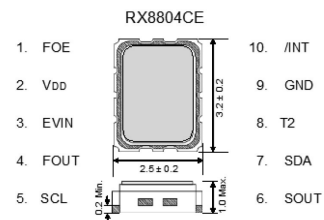
- Interface type
I²C-Bus interface Fast-Mode 400 kHz
- High stability
XA: $\pm 3.4 \times 10^{-6}$ / -40 °C to +85 °C (equivalent to ± 9 s of mo. deviation)
 $\pm 8.0 \times 10^{-6}$ / +85 °C to +105 °C (equivalent to ± 21 s of mo. deviation)
XB: $\pm 5.0 \times 10^{-6}$ / -40 °C to +85 °C (equivalent to ± 13 s of mo. deviation)
 $\pm 8.0 \times 10^{-6}$ / +85 °C to +105 °C (equivalent to ± 21 s of mo. deviation)
- Clock output function
Output frequency is selectable from 32.768 kHz, 1024 Hz, 1 Hz
- Wakeup timer function
Selectable from 244 μ s to 32 years (24 bit x 1 ch.)
Timer source clock selectable from 1/60 Hz, 1 Hz, 64 Hz, 4096 Hz
Auto release after interrupt output from /INT pin at timer completes
This operation is auto repeat with a selected cycle, it can be used like a watchdog timer
- Time stamp function
1 time stamped from year to second
The time stamp trigger inputs from EVIN pin, self-monitoring and I²C software command
EVIN pin has function of chattering-cancel
- Alarm function
It is possible program from day to minute
- Internal state output function
SOUT pin outputs selected flag-bit value or specified value (H or L)

Pin Function

Signal Name	I / O	Function
SOUT	Output	Internal state output pin
SCL	Input	Serial clock input pin
FOUT	Output	Frequency output pin (CMOS) (frequency selection: 32.768 kHz, 1024 Hz, 1 Hz)
EVIN	Input	Event input pin
V _{DD}	-	Power-supply pin
FOE	Input	The FOUT output control pin
/INT	Output	Interrupts output by Alarm and Timer events (N-ch. open drain)
GND	-	Ground pin
T2	-	Test pin in the factory (Do not connect externally)
SDA	Input / Output	Serial data input and output pin.

Terminal connection / External dimensions

(Unit: mm)



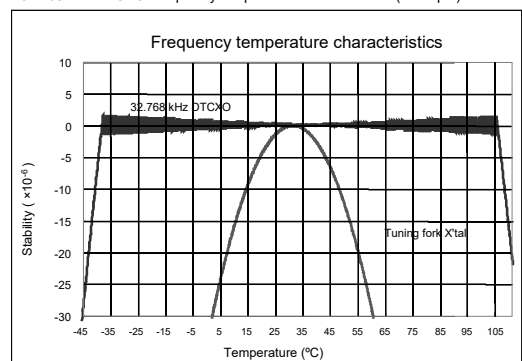
Specifications (characteristics)

* Refer to application manual for details

Electrical Characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit	
Operating voltage	V _{DD}	-	1.6	3.0	5.5	V	
Temp. compensated Voltage	V _{TEM}	-	1.5	3.0	5.5	V	
Clock supply voltage	V _{CLK}	-	1.5	3.0	5.5	V	
Operating temperature	T _a	-	-40	+25	+105	°C	
Stability	$\Delta f / f$	XA	T _a = -40 °C to +85 °C	± 3.4		x 10 ⁻⁶	
			T _a = +85 °C to +105 °C	± 8.0			
		XB	T _a = -40 °C to +85 °C	± 5.0			
			T _a = +85 °C to +105 °C	± 8.0			
Current consumption (1)	I _{DD1}	fSCL = 0 Hz, /INT = V _{DD} , FOE = GND, FOUT: OFF, Temp. Compensation interval 2.0 s	V _{DD} = 5 V	-	0.4	1.6	μ A
Current consumption (2)	I _{DD2}		V _{DD} = 3 V	-	0.35	1.5	μ A

32.768 kHz DTCXO Frequency temperature characteristics (Example)



REAL TIME CLOCK MODULE (I²C-Bus)

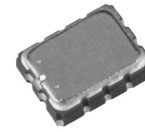
Built-in backup battery charge control function



Product Number (2,000 pcs / Reel)
RX8130CE: X1B000311000100

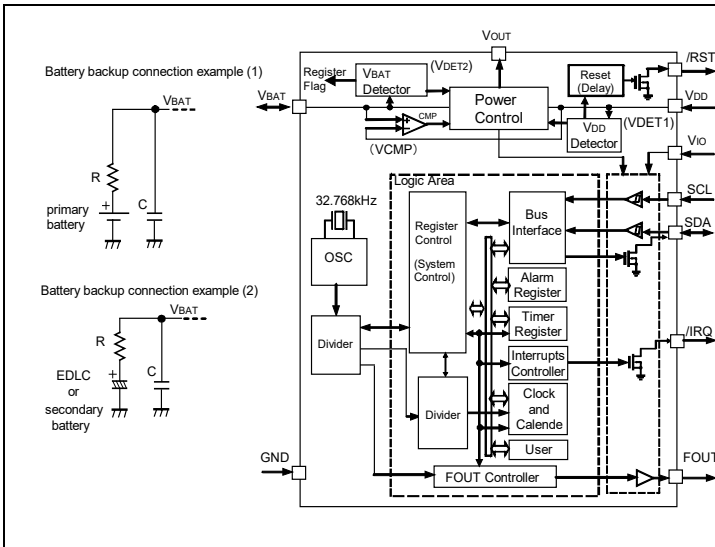
RX8130CE

- Built-in frequency adjusted 32.768 kHz crystal unit
- Interface Type : I²C-Bus
- Low backup current : 300 nA Typ. / 3 V
- Auto power switching function : Automatically switches to backup power supply by monitoring the VDD voltage
- Backup battery charge control function : For the rechargeable battery
- Reset functions with a delay : Detect a main power supply and remove the reset
- Interrupt output : Wake up every minute or every second
- Alarm interruption : Day, date, hour, minute, second
- Auto repeat wakeup timer interruption
- Self-monitoring interruption : Crystal oscillation stop, V_{BAT} low, V_{DD} low



RX8130CE
 (3.2 x 2.5 mm, t = 1.0 mm Max.)

Block diagram



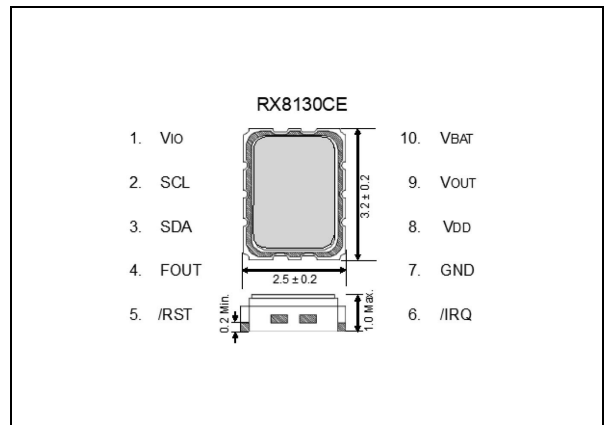
Overview

- Interface type
 I²C-Bus interface Fast-Mode 400 kHz
- Auto power switch function
 The V_{DD} voltage is monitored and it switches to the backup power supply by the automatic operation
 Backup power supply switching voltage 1.2V Min.
- Clock output function
 Output frequency is selectable from 32.768 kHz, 1024 Hz, 1 Hz
- Wakeup timer function
 Selectable from 244 μs to 7.5 years (16 bit x 1 ch.)
 Timer source clock selectable from 1/3600 Hz, 1/60 Hz, 1 Hz, 64 Hz, 4096 Hz. Auto release after interrupt output from /IRQ pin at timer completes
 This operation is auto repeat with a selected cycle, it can be used like a watchdog timer
- Backup battery charge control function
 Stop charging automatically by detecting the full charge.
 Records in the register detecting the backup power supply Voltage decrease
- Reset function with a delay
 When the main power is supplied, reset output is released.
 The reset/release voltage is selected by the register (2 types)
 Delay time of release from backup mode is 60ms Min.

Pin Functin

Signal Name	I / O	Function
SCL	Input	Serial clock input pin
SDA	Input / Output	Serial data input and output pin
FOUT	Output	Frequency output pin (CMOS) (frequency selection: 32.768 kHz, 1024 Hz, 1 Hz)
/RST	Output	Reset output pin (N-ch. open drain) In case of V _{DD} voltage drop detection, a reset signal is outputted In case of V _{DD} voltage rise detection, a released reset signal is outputted
/IRQ	Output	Interrupts output by Alarm and Timer events (N-ch. open drain)
V _{DD}	-	Power-supply pin Possible to supply different voltage from V _{IO}
V _{IO}	-	Interface power supply pin Input to supply the voltage same as a host
V _{OUT}	-	Internal voltage output pin Connect bypass capacitor of 1.0 μF
V _{BAT}	-	This is a power supply pin for backup battery Connect an EDLC, a secondary battery, a primary battery In the backup voltage range, supplied to IC, from this pin
GND	-	Ground pin

Terminal connection / External dimensions (Unit: mm)



Specifications (characteristics)

* Refer to application manual for details

Recommended Operating Conditions

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Operating supply voltage	V _{DD}	-	1.25	3.0	5.5	V
Clock supply voltage	V _{CLK}	-	1.1	3.0	5.5	V
Operating temperature	T _a	-	-40	+25	+85	°C
V _{DD} detect voltage	-V _{DET2}	V _{DD} , Fall	1.20	1.30	1.40	V

Frequency characteristics

Item	Symbol	Condition	Rating	Unit
Frequency tolerance	Δ f / f	T _a = +25 °C V _{DD} = 3.0 V	B: 5 ± 23	x 10 ⁻⁶
Oscillation start-up time	t _{STA}	V _{DD} = 2.75 V to 5.5 V	1 Max.	s

Current consumption characteristics

T_a = -40 °C to +85 °C

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Current consumption	I _{BAT}	SCL = SDA = "L", V _{BAT} = 3.0 V, V _{DD} = V _{IO} = 0.0 V	-	300	500	nA
	I _{32k}	SCL = SDA = "H", FOUT = 32.768 kHz, /IRQ=OFF, V _{DD} = V _{IO} = 3.0 V, FOUT pin CL = 15 pF, CHGEN = L or V _{BAT} ≥ V _{DET3}	-	3.5	4.0	μA

REAL TIME CLOCK MODULE (I²C-Bus)

Built-in 32.768 kHz DTCXO, High Stability,
Power switching



Product Number
 1,000 pcs / Reel
RX8900SA UA: X1B000292000100
RX8900SA UB: X1B000292000200
RX8900SA UC: X1B000292000300
 2,000 pcs / Reel
RX8900CE UA: X1B000301000100
RX8900CE UB: X1B000301000200
RX8900CE UC: X1B000301000300

RX8900SA / RX8900CE

- Built-in frequency adjusted 32.768 kHz crystal unit and DTCXO
- Interface Type : I²C-Bus
- Interface voltage range : 2.5 V to 5.5 V
- Temp. compensated voltage range : 2.0 V to 5.5 V
- Timekeeping voltage range : 1.6 V to 5.5 V
- Auto power switching function : Automatically switches to backup power supply by monitoring the VDD voltage
- Interrupt output : Wake up every minute or every second
- Alarm interruption : Day, date, hour, minute
- Auto repeat wakeup timer interruption



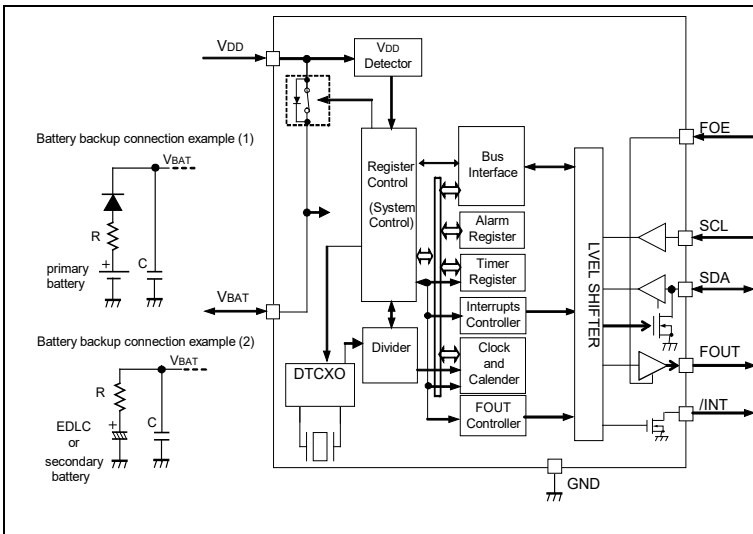
RX8900SA



RX8900CE

(10.1 x 7.4 mm, t = 3.3 mm Max.) (3.2 x 2.5 mm, t = 1.0 mm Max.)

Block diagram



Overview

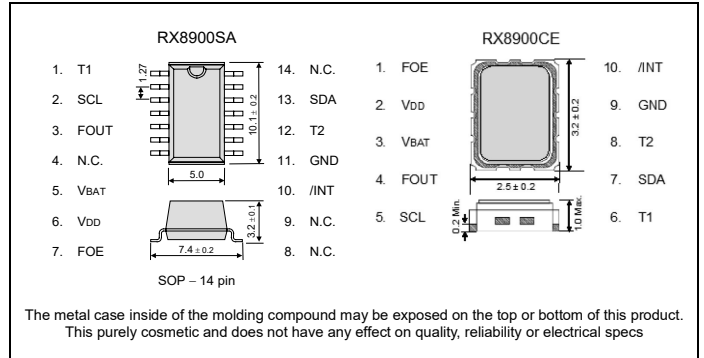
- Interface type
I²C-Bus interface Fast-Mode 400 kHz
- High stability
 UA: $\pm 3.4 \times 10^{-6}$ / -40 °C to +85 °C (equiv. to ± 9 s of mo. deviation)
 UB: $\pm 5.0 \times 10^{-6}$ / -40 °C to +85 °C (equiv. to ± 13 s of mo. deviation)
 UC: $\pm 5.0 \times 10^{-6}$ / -30 °C to +70 °C (equiv. to ± 13 s of mo. deviation)
- Auto power switch function
 The VDD voltage is monitored and it switches to the backup power supply by the automatic operation
 Backup power supply switching voltage 1.9 V Min.
- Clock output function
 Output frequency is selectable from 32.768 kHz, 1024 Hz, 1 Hz
- Wakeup timer function
 Selectable from 244 μ s to 2.8 days (12 bit x 1 ch.)
 Timer source clock selectable from 1/60 Hz, 1 Hz, 64 Hz, 4096 Hz
 Auto release after interrupt output from /INT pin at timer completes
 This operation is auto repeat with a selected cycle, it can be used like a watchdog timer
- Alarm function
 It is possible program from day to minute
- Temp. sensor function
 Available readout temperature data from embedded temp sensor

Pin Function

Signal Name	I / O	Function
T1	-	Test pin in the factory (Do not connect externally)
SCL	Input	Serial clock input pin
FOUT	Output	Frequency output pin (CMOS) (frequency selection: 32.768 kHz, 1024 Hz, 1 Hz)
VBAT	-	This is a power supply pin for backup battery Connect an EDLC, a secondary battery, a primary battery. In the backup voltage range, supplied to IC, from this pin
VDD	-	Power-supply pin
FOE	Input	The FOUT output control pin
/INT	Output	Interrupt output (N-ch. open drain).
GND	-	Ground pin
T2	-	Test pin in the factory (Do not connect externally)
SDA	Input / Output	Serial data input and output pin

Terminal connection / External dimensions

(Unit: mm)



The metal case inside of the molding compound may be exposed on the top or bottom of this product. This purely cosmetic and does not have any effect on quality, reliability or electrical specs

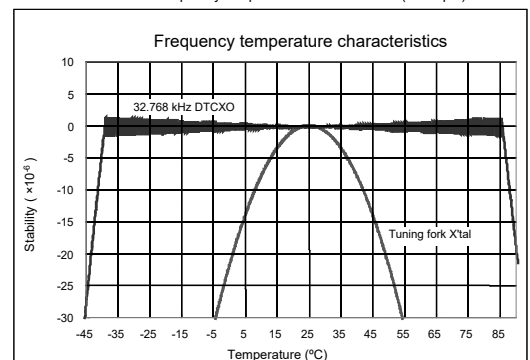
Specifications (characteristics)

* Refer to application manual for details

Electrical Characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit	
Operating voltage	VDD	-	2.5	3.0	5.5	V	
Temp. compensated Voltage	VTEM	-	2.0	3.0	5.5	V	
Clock supply voltage	VCLK	-	1.6	3.0	5.5	V	
VDD detect voltage (3)	VDET3	-	2.3	2.4	2.5	V	
Operating temperature	Ta	-	-40	+25	+85	°C	
Stability	$\Delta f / f$	UA	Ta = -40 °C to +85 °C		± 3.4	$\times 10^{-6}$	
		UB	Ta = -40 °C to +85 °C		± 5.0		
		UC	Ta = -30 °C to +70 °C				
Current consumption (1)	I _{DD1}	fSCL = 0 Hz, /INT = VDD, FOE = GND, VDD = VBAT, FOUT: OFF, Temp. Compensation interval 2.0 s	VDD = 5 V	-	0.72	1.5	μ A
Current consumption (2)	I _{DD2}		VDD = 3 V	-	0.70	1.4	μ A

32.768 kHz-DTCXO Frequency temperature characteristics (Example)



REAL TIME CLOCK MODULE (I²C-Bus)
Built-in 32.768 kHz-DTCXO, High Stability

RX-8803SA / LC

- Built in frequency adjusted 32.768 kHz crystal unit and DTCXO.
- 1/100s resolution Time register
- Interface Type : I²C-Bus interface (400kHz)
- Interface voltage range : 1.6 V to 5.5 V
- Temp. compensated voltage range : 2.2 V to 5.5 V
- Clock supply voltage range : 1.6 V to 5.5 V
- Selectable clock output (32.768 kHz, 1024 Hz, 1 Hz)
- The various functions include full calendar, alarm, timer, EVIN input.

- Product Number
 RX-8803SA UB : X1B000151000100
 RX-8803SA UA : X1B000151000200
 RX-8803SA UC : X1B000151000300
 RX-8803SA AA : X1B000151000400
 RX-8803LC UA : X1B000142000100
 RX-8803LC UB : X1B000142000200
 RX-8803LC UC : X1B000142000300
 RX-8803LC AA : X1B000142000400



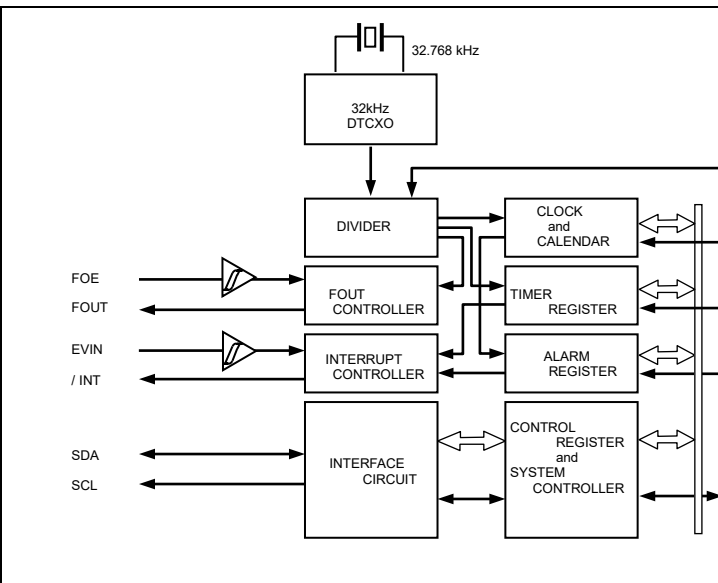
RX-8803SA



RX-8803LC

Epson prepared Linux driver for development.
 (http://www5.epsondevice.com/en/information/support/linux_rtc/)
 The registered trademark Linux® is used pursuant to a sublicense from LMI(Linux Mark Institute)

Block diagram



Overview

- **High Stability**
 - UA ± 3.4 x 10⁻⁶ / -40 °C to +85 °C (Equivalent to ±9 seconds of month deviation)
 - UB ± 5.0 x 10⁻⁶ / -40 °C to +85 °C (Equivalent to ±13 seconds of month deviation)
 - UC ± 5.0 x 10⁻⁶ / -30 °C to +70 °C
 - AA (+5 ± 5.0) x 10⁻⁶ / +25 °C
- **High Resolution:** 1/100s Time register with capture buffer
- **32.768 kHz frequency output function**
 - FOUT pin output (C-MOS output), CL=30 pF
 - Output selectable: 32.768 kHz, 1024 Hz, 1 Hz
- **The various interrupt**
 - Timer Function can be set between 1/ 4096 second and 4095 minutes.
 - Alarm Function can be set to day of week, day, hour, or minute.
 - EVIN input.
- **Time synchronize function with 1PPS signal input**
- **Register compatibility:** upper compatible with RX-8801.

*It is possible to use it by the terminal connection as 32.768 kHz-DTCXO.

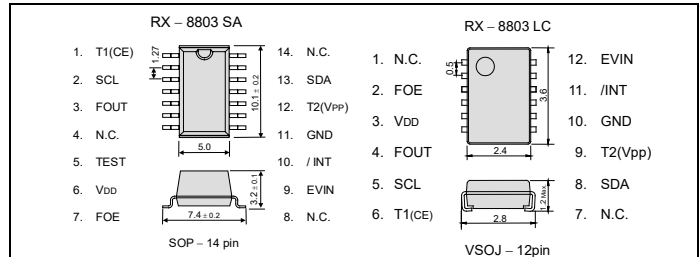
Pin Function

Signal Name	I / O	Function
T1(CE)	input	Use by the manufacture for testing. (Do not connect externally.)
SCL	input	Serial clock input pin.
FOUT	Output	The pin outputs the reference clock signal. (CMOS output)
TEST	input	Use by the manufacture for testing. (Do not connect externally. RX-8803SA only.)
VDD	-	Connected to a positive power supply
FOE	input	The input pin for the FOUT output control.
EVIN	input	External event input.
/INT	Output	Interrupt output (N-ch. open drain).
GND	-	Connected to a ground
T2(VPP)	-	Use by the manufacture for testing. (Do not connect externally.)
SDA	I/O	Data input and output pin.

When it is replaced to RX8803SA from RX8801SA, please do not make open state of 9pin.

Terminal connection / External dimensions

(Unit:mm)



The metal case inside of the molding compound may be exposed on the top or bottom of this product. This purely cosmetic and does not have any effect on quality, reliability or electrical specs.

Prohibition of use of glue after a mount of a product
 LC package product cannot use glue and resin coating.
 When such a processing is necessary, please examine a CE package product.

Specifications (characteristics)

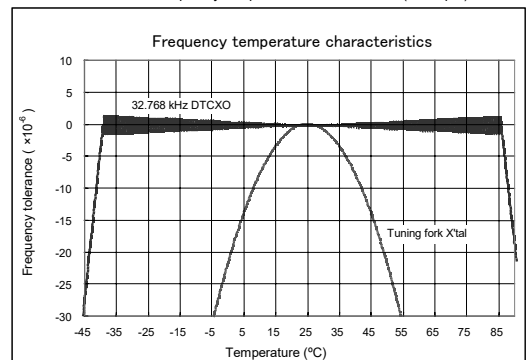
* Refer to application manual for details.

■ Electrical Characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit	
Operating voltage	V _{DD}	Interface voltage	1.6	3.0	5.5	V	
Temp. compensated Voltage	V _{TEM}	Temp. compensated voltage	2.2	3.0	5.5	V	
Clock supply voltage	V _{CLK}	-	1.6	3.0	5.5	V	
Operating temperature	T _{OPR}	-	-40	+25	+85	°C	
Stability	Δf/f	UA Ta = -40 °C to +85 °C	±3.4 *1		× 10 ⁻⁶		
		UB Ta = -40 °C to +85 °C	±5.0 *2				
		UC Ta = -30 °C to +70 °C					
		AA Ta = +25 °C	5 ± 5.0 *3				
Current consumption (1)	I _{DD1}	Backup Mode FOE = GND, /INT = V _{DD} FOUT output : OFF	V _{DD} = 5V	-	0.75	3.4	μA
Current consumption (2)	I _{DD2}		V _{DD} = 3V	-	0.75	2.1	μA

*1) Equivalent to ±9 seconds of month deviation. *2) Equivalent to ±13 seconds of month deviation.
 *3) Equivalent to ±13 seconds of month deviation. (excluding offset)

■ 32.768 kHz-DTCXO Frequency temperature characteristics (Example)



REAL TIME CLOCK MODULE (SPI-Bus)

Built-in 32.768 kHz-DTCXO, High Stability

RX-4803SA / LC

Product Number
 RX-4803SA : X1B000131xxxx00
 RX-4803LC : X1B000122xxxx00
 RX-4803LC UB : X1B000122000200

- Built in frequency adjusted 32.768 kHz crystal unit and DTCXO.
 - 1/100s resolution Time register
 - Interface Type : 4-wire serial interface
 - Interface voltage range : 1.6 V to 5.5 V
 - Temp. compensated voltage range : 2.2 V to 5.5 V
 - Clock supply voltage range : 1.6 V to 5.5 V
 - Selectable clock output (32.768 kHz, 1024 Hz, 1 Hz)
 - The various functions include full calendar, alarm, timer, EVIN input.
- Epson prepared Linux driver for development.
http://www5.epsondevice.com/en/information/support/linux_rtc/
 The registered trademark Linux® is used pursuant to a sublicense from LMI(Linux Mark Institute)

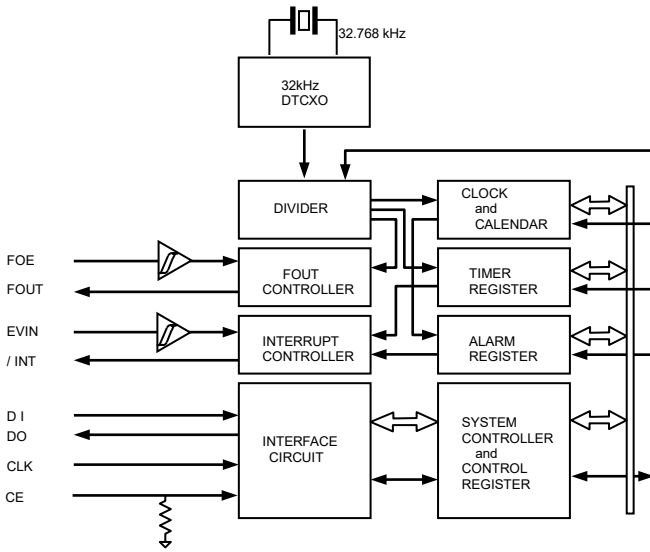


RX-4803SA



RX-4803LC

Block diagram



Overview

- **High Stability**
 - UA $\pm 3.4 \times 10^{-6}$ / -40°C to $+85^\circ\text{C}$ (Equivalent to ± 9 seconds of month deviation)
 - UB $\pm 5.0 \times 10^{-6}$ / -40°C to $+85^\circ\text{C}$ (Equivalent to ± 13 seconds of month deviation)
 - UC $\pm 5.0 \times 10^{-6}$ / -30°C to $+70^\circ\text{C}$
 - AA $(+5 \pm 5.0) \times 10^{-6}$ / $+25^\circ\text{C}$
- **High Resolution:** 1/100s Time register with capture buffer
- **32.768 kHz frequency output function**
 - FOUT pin output (C-MOS output), CL=30 pF
 - Output selectable: 32.768 kHz, 1024 Hz, 1 Hz
- **The various interrupt**
 - Timer Function can be set between 1/4096 second and 4095 minutes.
 - Alarm Function can be set to day of week, day, hour, or minute.
 - EVIN input.
- **Time synchronize function with 1PPS signal input**
- **Register compatibility:** upper compatible with RX-4801.

*It is possible to use it by the terminal connection as 32.768 kHz-DTCXO.

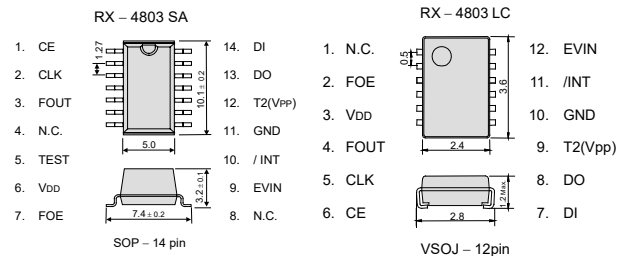
Pin Function

Signal Name	I / O	Function
CE	input	The chip enable input pin.
CLK	input	The shift clock input pin for serial data transfer.
FOUT	Output	The pin outputs the reference clock signal. (CMOS output)
TEST	input	Use by the manufacture for testing. (Do not connect externally. RX-4803SA only.)
VDD	-	Connected to a positive power supply
FOE	input	The input pin for the FOUT output control.
EVIN	input	External event input. Open is prohibited
/INT	Output	Interrupt output (N-ch. open drain).
GND	-	Connected to a ground
T2(VPP)	-	Use by the manufacture for testing. (Do not connect externally.)
DO	Output	The data output pin for serial data transfer.
DI	input	The data input pin for serial data transfer.

When it is replaced to 4803SA from 4801SA, please do not make open state of 9pin.

Terminal connection / External dimensions

(Unit:mm)



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Prohibition of use of glue after a mount of a product
 An LC package product cannot use glue and resin coating.
 When such a processing is necessary, please examine a CE package product.

Specifications (characteristics)

* Refer to application manual for details.

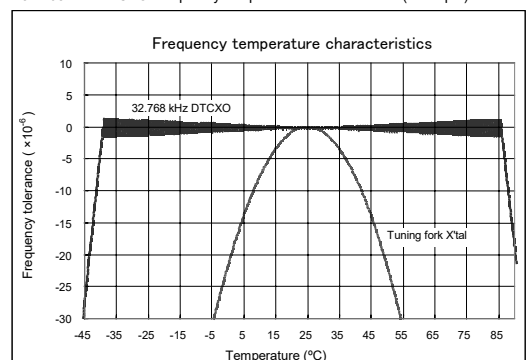
Electrical Characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit	
Interface voltage	V _{DD}	Interface voltage	1.6	3.0	5.5	V	
Temp. compensated Voltage	V _{TEM}	Temp. compensated voltage	2.2	3.0	5.5	V	
Clock supply voltage	V _{CLK}	-	1.6	3.0	5.5	V	
Operating temperature	T _{OPR}	No condensation	-40	+25	+85	°C	
Stability	Δf/f	UA Ta = -40 °C to +85 °C	±3.4 ^{*1}			× 10 ⁻⁶	
		UB Ta = -40 °C to +85 °C	±5.0 ^{*2}				
		UC Ta = -30 °C to +70 °C					
		AA Ta = +25 °C	5 ± 5.0 ^{*3}				
Current consumption (1)	I _{DD1}	Backup Mode FOE = GND, /INT = V _{DD} FOUT output : OFF	V _{DD} = 5V	-	0.75	3.4	μA
Current consumption (2)	I _{DD2}		V _{DD} = 3V	-	0.75	2.1	μA

^{*1}) Equivalent to ±9 seconds of month deviation. ^{*2}) Equivalent to ±13 seconds of month deviation.

^{*3}) Equivalent to ±13 seconds of month deviation. (excluding offset)

32.768 kHz-DTCXO Frequency temperature characteristics (Example)



REAL TIME CLOCK MODULE (I²C-Bus) High-Stability Frequency with Built in Timestamp and Power Switching RX-8035SA / LC



Product Number
 RX-8035SA B : X1B000172000100
 RX-8035SA AC : X1B000172000200
 RX-8035SA AA : X1B000172000300
 RX-8035LC B : X1B000182000100
 RX-8035LC AC : X1B000182000200
 RX-8035LC AA : X1B000182000300



RX-8035SA



RX-8035LC

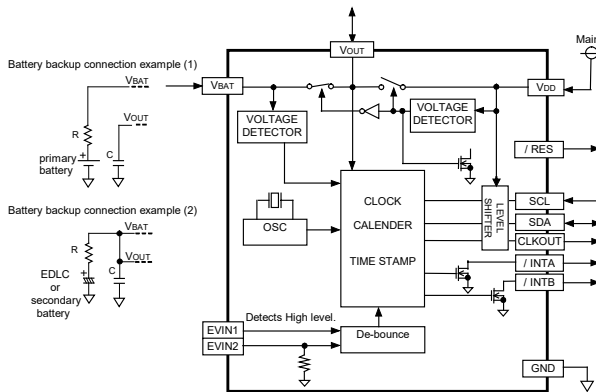
- Built-in 32.768 kHz crystal unit : Frequency adjusted for high accuracy. ($\pm 5 \times 10^{-6} / T_a = +25^\circ\text{C}$)
- Interface Type : I²C-Bus Interface (400kHz)
- Operating voltage range : 2.4 V to 5.5 V
- Wide voltage for Timekeeping. : 1.0 V to 5.5 V
- Low backup current : 350 nA (SA) 400 nA (LC) / 3 V (Typ.)
- Event detection and Time stamp : One-shot full timestamp and interrupt.
- Dual event detection ports : Each terminal has a de-bounce circuit.
- Auto power switching functions : It switches VDD and VBAT, automatically.
- Dual Alarm, Periodic interruption.

Epson prepared Linux driver for development.

(http://www5.epsondevice.com/en/information/support/linux_rtc/)

The registered trademark Linux® is used pursuant to a sublicense from LMI(Linux Mark Institute)

Block diagram



Overview

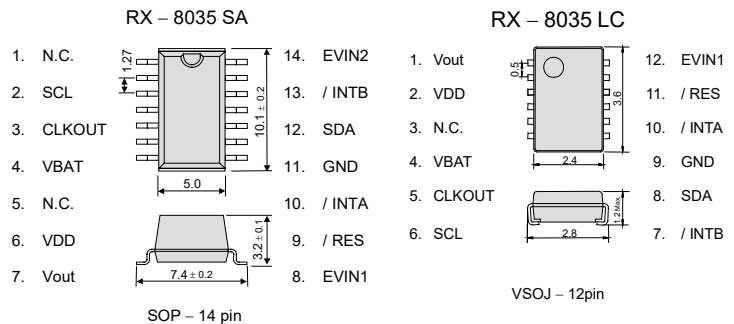
- **The event detection and Timestamp function**
Dual event detection terminals.
Selectable de-bounce time 35ms or 2s.
Available event detection interrupt output.
- **Power switching functions.**
 - An external diode is unnecessary to have a reverse current prevention switch built-in in the VBAT side to connect a primary cell to.
 - When VDD is less than 2.4V, an internal source is switched to VBAT, and /RES is Low level. When VDD voltage rises to higher than 2.52V, an internal source is switched to VDD, and /RES is released with 105ms delay.
 - Note: When the supply from VBAT, SCL and SDA are disabled.
- **Alarm, Periodic interrupt, 32.768kHz clock output.**
 - Available monthly-alarm and weekly- Dual alarm.
 - Interrupt period are selectable from 2Hz to Monthly.
 - CLKOUT outputs 32.768kHz, it powered from VDD.

Pin function

Signal Name	Input / Output	Function
SCL	Input	I ² C serial clock.
SDA	In/Out	I ² C data in/out.
VDD	—	Main power supply.
VBAT	—	Power supply for backup.
Vout	Output	Switched power out. (maximum output current 20mA)
/ RES	Output	VDD voltage state.
GND	—	Ground
EVIN1	Input	Event detection input 1
EVIN2	Input	Event detection input 2
/ INTA	Output	Interrupt out A.
/ INTB	Output	Interrupt out B.
CLKOUT	Output	32.768kHz output. (CMOS. Can not inhibit.)
N.C.	—	Do not connect.

Terminal connection / External dimensions

(Unit:mm)



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Prohibition of use of glue after a mount of a product

An LC package product cannot use glue and resin coating. When such a processing is necessary, please examine a CE package product.

Specifications (characteristics)

* Refer to application manual for details.

Recommended Operating Conditions

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Operating voltage	VACCESS	VDD	2.4	3.0	5.5	V
Time keeping voltage	VCLK	VBAT	1.0	3.0	5.5	V
Operating temperature	TOPR	—	-40	+25	+85	°C
Storage temperature	TSTG	—	-55	—	+125	°C

Frequency characteristics

Item	Symbol	Conditions	Rating	Unit
Frequency tolerance	$\Delta f/f$	T _a = +25 °C V _{BAT} = 3.0 V	B: 5 ± 23 ^{(*)1} AA: 5 ± 5 ^{(*)2} AC: 0 ± 5 ^{(*)2}	× 10 ⁻⁶
Oscillation start-up time	t _{STA}	T _a = +25 °C V _{DD} = 3.0 V	1 Max.	s
Frequency / voltage characteristics	f / V	T _a = +25 °C V _{DD} = 2.4 V to 5.5 V	± 1 Max.	× 10 ⁻⁶

^{(*)1} Equivalent to ±1 minute of monthly deviation (excluding offset).
^{(*)2} Equivalent to ±13 seconds of monthly deviation (excluding offset).

Current consumption characteristics

T_a = -40 °C to +85 °C

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Current Consumption	I _{BAT}	RX-8035SA V _{BAT} = 3.0V, V _{DD} = 0.0V SCL=SDA = GND	-	350	1200	nA
		RX-8035LC V _{BAT} = 3.0V, V _{DD} = 0.0V SCL=SDA = GND	-	400	-	-
	I _{DD}	V _{DD} = 3.0V SCL=SDA = GND CLKOUT = open	-	1.40	2.50	μA

Power supply detection voltage

T_a = -40 °C to +85 °C

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Voltage of low battery voltage.	V _{LOW}	-	1.10	1.25	1.40	V
Power switching voltage (VDD to VBAT)	V _{D2B}	+25 °C	2.328	2.40	2.472	V

REAL TIME CLOCK MODULE (SPI-Bus)
High-Stability Frequency with Built in Timestamp and Power Switching



Product Number
 RX-4035SA B: X1B000192000100
 RX-4035SA AC: X1B000192000200
 RX-4035SA AA: X1B000192000300
 RX-4035LC B: X1B000202000100
 RX-4035LC AC: X1B000202000200
 RX-4035LC AA: X1B000202000300

RX-4035SA / LC

- Built-in 32.768 kHz crystal unit : Frequency adjusted for high accuracy. ($\pm 5 \times 10^{-6} / T_a = +25^\circ\text{C}$)
- Interface Type : SPI-Bus (1MHz)
- Operating voltage range : 2.4 V to 5.5 V
- Timekeeping voltage range : 1.0 V to 5.5 V
- Low backup current : 350 nA (SA) 400 nA (LC) / 3 V (Typ.)
- Event detection and Time stamp : One-shot full timestamp and interrupt.
- Dual event detection ports : Each terminal has a de-bounce circuit.
- Auto power switching functions : When VDD deteriorates than 2.4V, internal source is switched to VBAT.

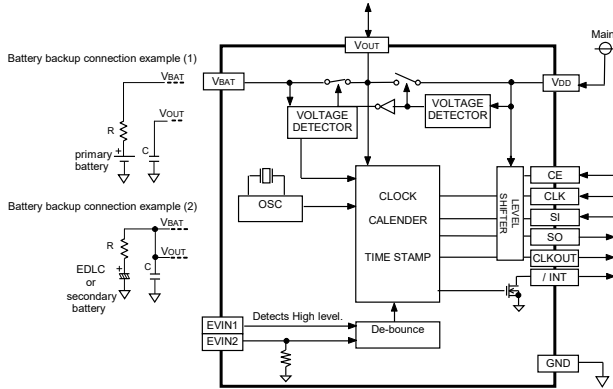


RX-4035SA



RX-4035LC

Block diagram



Overview

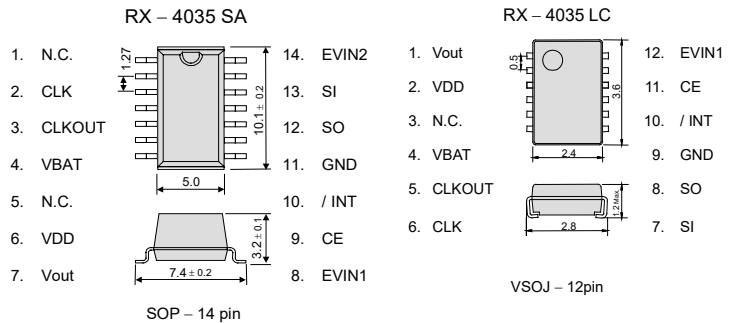
- **The event detection and Timestamp function**
 Dual event detection terminals.
 Selectable de-bounce time 35ms or 2s.
 Available event detection interrupt output.
- **Power switching functions.**
 - An external diode is unnecessary to have a reverse current prevention switch built-in in the VBAT side to connect a primary cell to.
 - When VDD is less than 2.4V, an internal source is switched to VBAT.
 - Note: When the supply from VBAT, SPI interface are disabled.
- **Alarm, Periodic interrupt, 32.768kHz clock output.**
 - Available monthly-alarm and weekly-alarm.
 - Interrupt period are selectable from 2Hz to Monthly.
 - CLKOUT outputs 32.768kHz clock powered by VDD.

Pin function

Signal Name	Input / Output	Function
VBAT	—	Power supply for backup.
Vout	Output	Switched power out. (maximum output current 20mA)
CE	Input	SPI chip enable.
CLK	Input	SPI serial clock.
SO	Output	SPI data out.
SI	Input	SPI data in.
GND	—	Ground
EVIN1	Input	Event detection input 1
EVIN2	Input	Event detection input 2
/ INT	Output	Interrupt out.
CLKOUT	Output	32.768kHz output. (CMOS. Can not inhibit.)
N.C.	—	Do not connect.
VDD	—	Main power supply.

Terminal connection / External dimensions

(Unit:mm)



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 An LC package product cannot use glue and resin coating. When such a processing is necessary, please examine a CE package product.

Specifications (characteristics)

* Refer to application manual for details.

Recommended Operating Conditions

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Operating voltage	VACCESS	VDD	2.4	3.0	5.5	V
Time keeping voltage	VCLK	VBAT	1.0	3.0	5.5	V
Operating temperature	TOPR	—	-40	+25	+85	°C
Storage temperature	TSTG	—	-55	—	+125	°C

Frequency characteristics

Item	Symbol	Conditions	Rating	Unit
Frequency tolerance	$\Delta f / f$	Ta = +25°C VBAT = 3.0 V	B: 5 ± 23 ^{*1} AA: 5 ± 5 ^{*2} AC: 0 ± 5 ^{*2}	$\times 10^{-6}$
Oscillation start-up time	tSTA	Ta = +25°C VDD = 3.0 V	1 Max.	s
Frequency / voltage characteristics	f / V	Ta = +25°C VDD = 2.4 V to 5.5 V	± 1 Max.	$\times 10^{-6}$

*1) Equivalent to ± 1 minute of monthly deviation (excluding offset.)
 *2) Equivalent to ± 13 seconds of monthly deviation (excluding offset.)

Current consumption characteristics

Ta = -40 °C to +85 °C

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Current Consumption	IBAT	RX-4035SA VBAT = 3.0V, VDD = 0.0V CE = 0V, CLKOUT = open	-	350	1200	nA
		RX-4035LC VBAT = 3.0V, VDD = 0.0V CE = 0V, CLKOUT = open	-	400		
	IDD	VDD = 3.0V CE = 0V CLKOUT = open	-	1.40	2.50	μA

Power supply detection voltage

Ta = -40 °C to +85 °C

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Voltage of low battery detection	VLOW	-	1.10	1.25	1.40	V
Power switching voltage (VDD to VBAT)	VD2B	+25 °C	2.328	2.40	2.472	V

REAL TIME CLOCK MODULE (I²C-Bus)

Built-in EEPROM and Unique ID-ROM



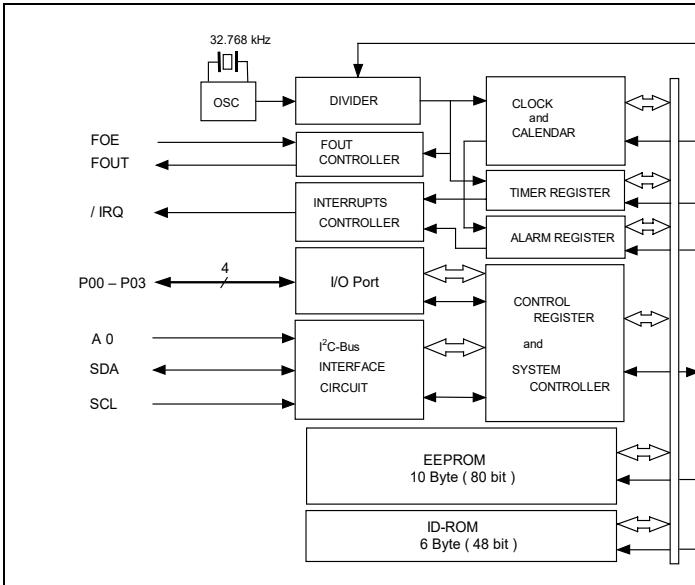
Product Number
RX-8731LC : Q418731C2000100

RX-8731LC

- Built in frequency adjusted 32.768 kHz crystal unit.
- Interface Type : I²C-Bus interface (400 kHz)
- Operating voltage range : 1.7 V to 5.5 V
- Wide voltage for Timekeeping : 1.3 V to 5.5 V
- Low backup current : 0.35 μ A / 3 V (Typ.)
- 32.768 kHz frequency output function : C-MOS output With Control Pin
- The various functions include full calendar, alarm, timer.



Block diagram



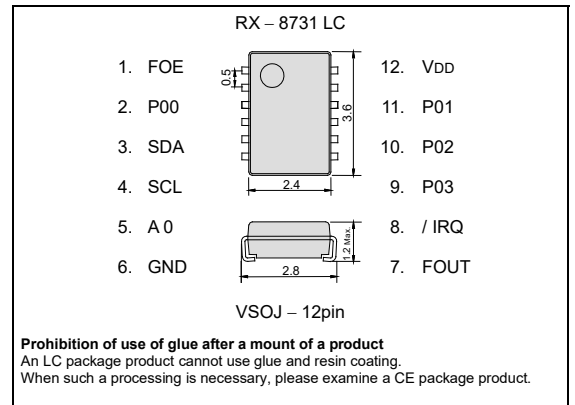
Overview

- **Built in EEPROM and ID-ROM**
 - Built in 10 Byte (80 bit) EEPROM
 - Built in 6 Byte (48 bit) ID-ROM
- **Programmable I/O ports**
 - 4 Programmable I/O ports
- **Interface Type**
 - I²C-Bus high-speed bus specifications. (400 kHz)
- **32.768 kHz frequency output function**
 - FOUT pin output (C-MOS output), CL=30 pF
 - FOE pin enables output on/off control.
 - Output frequency is selectable.
< 32.768 kHz, 1024 Hz, 1 Hz >
- **The various interrupt function**
 - Alarm interrupt function
 - Timer interrupt function
 - Update interrupt function

Pin Function

Signal Name	Input / Output	Function
SCL	Input	Serial Clock input pin.
SDA	Bi-directional	Data input and output pin.
A 0	Input	Device address A0 input pin.
FOUT	Output	FOUT pin is 32.768 kHz clock output pin (C-MOS) that output control is possible.
FOE	Input	FOE pin control the frequency output from FOUT pin with FSEL1-bit and FSEL0-bit.
/ IRQ	Output	Interrupt output pin. (N-ch open drain)
P00 P01 P02 P03	Bi-directional	Programmable I/O ports.
VDD	—	Connected to a positive power supply.
GND	—	Connected to a ground.

Terminal connection / External dimensions (Unit:mm)



Specifications (characteristics)

* Refer to application manual for details.

Recommended Operating Conditions

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Power voltage	VDD	—	1.7	3.0	5.5	V
Clock voltage	VCLK	—	1.3	3.0	5.5	V
Operating temperature	TOPR	—	-40	+25	+85	°C

Frequency characteristics

Item	Symbol	Conditions	Rating	Unit
Frequency tolerance	$\Delta f / f$	T _a = +25 °C VDD = 3.0 V	B: 5 ± 23 *	× 10 ⁻⁶
Oscillation Start-up time	t _{STA}	T _a = +25 °C VDD = 1.6 V	1 Max.	s
		T _a = -40 °C to +85 °C VDD = 1.6 V	3 Max.	s

*Equivalent to ±1 minute of monthly deviation

Current consumption characteristics

T_a = -40 °C to +85 °C

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit	
Current Consumption	I _{BK}	fSCL = 0 Hz / IRQ = OFF	VDD = 5 V	-	0.45	1.5	μ A
		FOUT : output OFF (Hi - z)	VDD = 3 V	-	0.35	1.4	
Current Consumption	I _{32k}	fSCL = 0 Hz / IRQ = OFF	VDD = 5 V	-	8.0	16.0	μ A
		FOUT : 32.768 kHz output CL = 30 pF	VDD = 3 V	-	5.0	10.0	

REAL TIME CLOCK MODULE (I²C-Bus)

Low current consumption



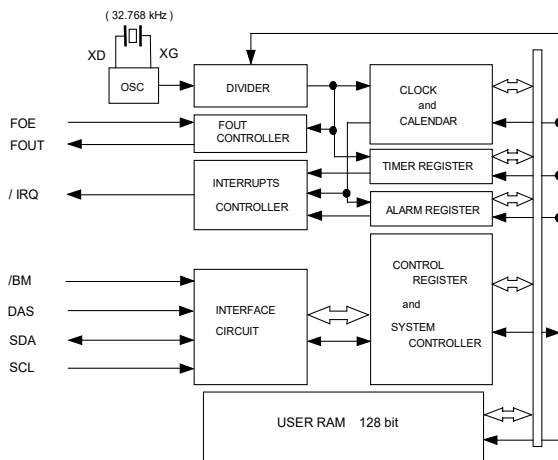
Product Number
 RX-8571SA : X1B000072000100
 RX-8571LC : X1B000052000100

RX-8571SA / LC

- Built-in frequency adjusted 32.768 kHz crystal unit.
- Interface Type : I²C-Bus Interface (400 kHz)
- Operating voltage range : 1.6 V to 5.5 V
- Wide voltage for timekeeping : 1.3 V to 5.5 V
- Low backup current : 220 nA / 3 V (Typ.)
- 32.768 kHz frequency output function : C-MOS output With Control Pin
- Built-in user RAM : 128 bit (8 bit x 16, SRAM)
- The various functions include full calendar, alarm, timer, etc.
 (Long-running timer : 65535 hours)



Block diagram

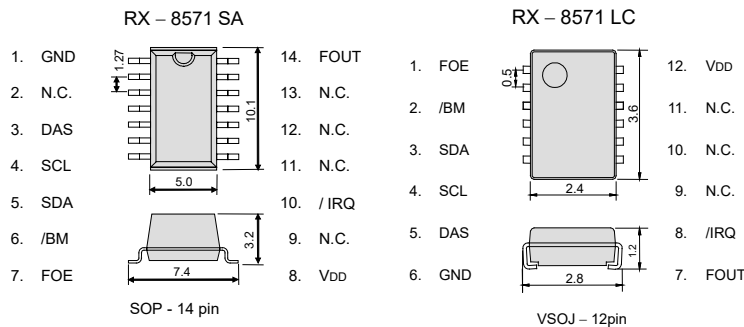


Overview

- 32.768 kHz frequency output function
 - FOE pin enable output on/off control.
 - Output frequency can be selected as 32.768 kHz, 1024 Hz, 1 Hz.
- Timer Function
 - Timer function can be set up between 1/4096 second and 65535 hours.
 - Timing period are 1 h, 1 min, 64 Hz, 4096 Hz.
 - It is recorded automatically to TF-bit at the time of event occurs, and possible to output with /IRQ pin output.
- Alarm function
 - Alarm function can be set to day of week, day, hour, or minute.
 - It is recorded automatically to AF-bit at the time of event occurs, and possible to output with /IRQ pin output.
- User RAM
 - 128 bit (8 bit x 16, SRAM)

Terminal connection / External dimensions

(Unit:mm)



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Prohibition of use of glue after a mount of a product

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 When such a processing is necessary, please examine a CE package product.

Specifications (characteristics)

* Refer to application manual for details.

Recommended Operating Conditions

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Power voltage	VDD	—	1.6	3.0	5.5	V
Clock voltage	VCLK	—	1.3	3.0	5.5	V
Operating temperature	TOPR	—	-40	+25	+85	°C

Frequency characteristics

Item	Symbol	Conditions	Rating	Unit
Frequency tolerance	$\Delta f/f$	T _a = +25 °C VDD = 3.0 V	B: 5 ± 23 *	× 10 ⁻⁶
Oscillation start up time	t _{STA}	T _a = +25 °C VDD = 1.6 V	1 Max.	s

* Please ask for tighter tolerance. (Equivalent to ±1 minute of monthly deviation)

Current consumption characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit	
Current Consumption	I _{DD}	FOE=/BM="L" FOUT= OFF /IRQ = OFF VDD =3.0V T _a = +25 °C	LC type	-	220	400	nA
		SA type	-	200	400		
		FOE=/BM="L" FOUT= OFF /IRQ = OFF VDD =3.0V T _a = -40 °C to +85 °C	-	-	550	nA	

**REAL TIME CLOCK MODULE (SPI-Bus)
LOW BACKUP VOLTAGE**



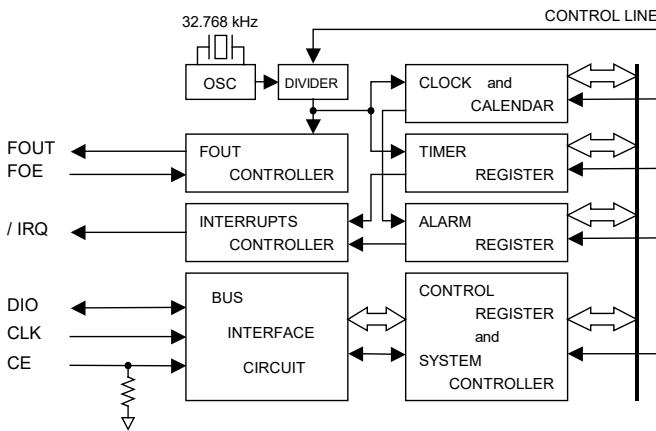
Product Number
RX-4571LC : Q414571C2000100
RX-4571SA : Q41457152000100

RX-4571LC / SA

- Built in frequency adjusted 32.768 kHz crystal unit.
- Interface Type : 3-wire serial interface
- Operating voltage range : 1.6 V to 5.5 V
- The wide voltage for time keeping. : 1.0 V to 5.5 V / T_a = +25 °C
- Low backup current : 0.32 μA (Typ.) / 3 V
- 32.768 kHz frequency output function : C-MOS output With OE pin.
- Real-time clock function
 Clock/calendar function, auto leap year correction function, alarm and Timer interrupt function, etc.



Block diagram



Overview

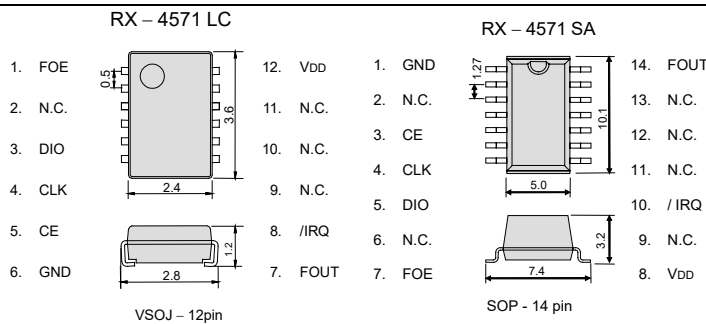
- **32.768 kHz frequency output function**
 - FOUT pin output (C-MOS output), CL=30 pF
- **Timer function**
 - Timer function which can be set up between 1/4096 second and 4095 minutes.
- **Alarm function**
 - Alarm function can be set to any combination of day, day of week, hour, or minute.

• **Pin Function**

Signal Name	Input / Output	Function
CE	Input	The chip enabled input pin 0. (It has a built -in pull-down resistance)
CLK	Input	The shift clock input pin for serial data transfer.
DIO	Bi-directional	The data input / output pin for serial data transfer.
FOUT	Output	32.768 kHz clock output pin with the output control function. (C-MOS)
FOE	Input	FOE pin control the condition of FOUT with FSEL1-bit, FSEL0-bit, etc.
/ IRQ	Output	Interrupt output (N-ch open drain)
VDD	—	Connected to a positive power supply.
GND	—	Connected to a ground.

Terminal connection / External dimensions

(Unit:mm)



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 An LC package product cannot use glue and resin coating.
 When such a processing is necessary, please examine a CE package product.

The metal case inside of the molding compound may be exposed on the top or bottom of this product.
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Specifications (characteristics)

* Refer to application manual for details.

■ Recommended Operating Conditions

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Power voltage	VDD	—	1.6	3.0	5.5	V
Clock voltage	VCLK	T _a = +25 °C	1.0	3.0	5.5	V
		T _a = -40 to +85 °C	1.1	3.0	5.5	V
Operating temperature	T _{OPR}	—	-40	+25	+85	°C

■ Frequency characteristics

Item	Symbol	Conditions	Rating	Unit
Frequency tolerance	Δ f / f	T _a = +25 °C VDD = 3.0 V	B: 5 ± 23 *	× 10 ⁻⁶
Oscillation start-up time	t _{STA}	T _a = +25 °C VDD = 1.6 V	1 Max.	s

* Please ask for tighter tolerance. (Equivalent to ±1 minute of monthly deviation)

■ Current consumption characteristics

T_a = -40 °C to +85 °C

Symbol	Conditions	VDD	Min.	Typ.	Max.	Unit
I _{BK}	CE = GND /IRQ = OFF FOUT ; output OFF (Hi - z)	VDD = 5 V	-	0.40	1.00	μA
		VDD = 3 V	-	0.32	0.95	
I _{32k}	CE = GND /IRQ = OFF FOUT ; 32.768 kHz output ON CL = 30 pF	VDD = 5 V	-	8.0	14.0	μA
		VDD = 3 V	-	5.0	8.5	

REAL TIME CLOCK MODULE (I²C-Bus)

Low current consumption



Product Number
RX-8564LC : Q418564C2000100

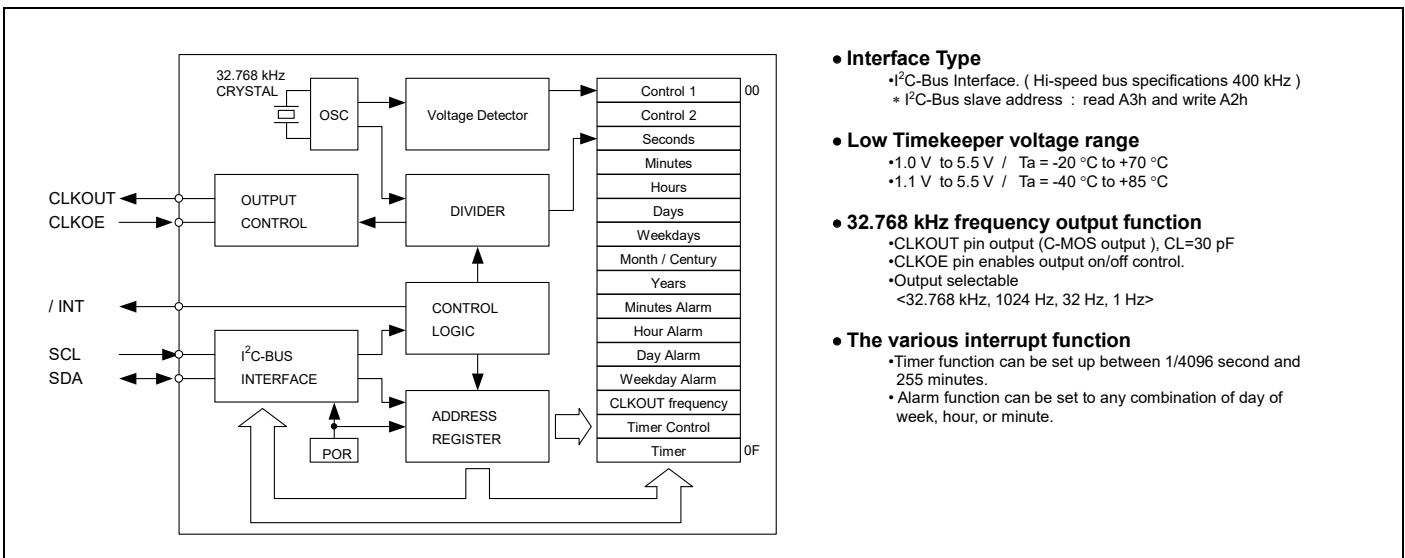
RX-8564LC

- Built in frequency adjusted 32.768 kHz crystal unit.
- Interface Type : I²C-Bus Interface (400 kHz)
- Operating voltage range : 1.8V to 5.5 V
- Timekeeper voltage range : 1.0V to 5.5 V / -20 °C to +70 °C
- Low backup current : 275nA / 3.0 V(Typ.)
- 32.768 kHz frequency output function : C-MOS output With Control Pin
- The various functions include full calendar, alarm, timer, and power supply voltage monitoring function



Block diagram

Overview



Interface Type

- I²C-Bus Interface. (Hi-speed bus specifications 400 kHz)
- * I²C-Bus slave address : read A3h and write A2h

Low Timekeeper voltage range

- 1.0 V to 5.5 V / Ta = -20 °C to +70 °C
- 1.1 V to 5.5 V / Ta = -40 °C to +85 °C

32.768 kHz frequency output function

- CLKOUT pin output (C-MOS output), CL=30 pF
- CLKOE pin enables output on/off control.
- Output selectable
- <32.768 kHz, 1024 Hz, 32 Hz, 1 Hz>

The various interrupt function

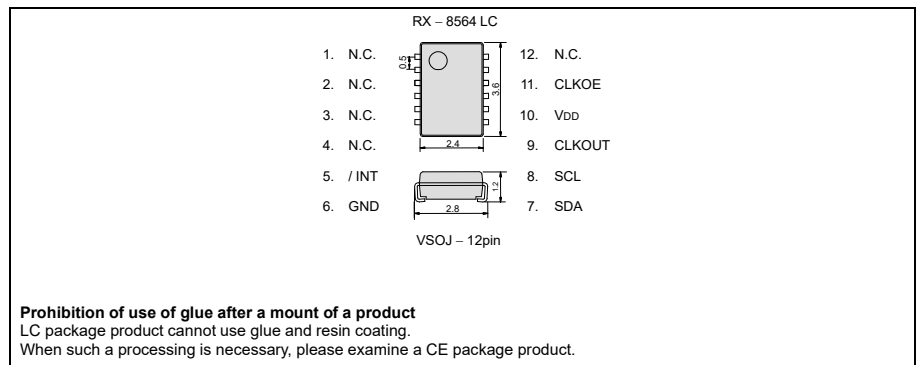
- Timer function can be set up between 1/4096 second and 255 minutes.
- Alarm function can be set to any combination of day of week, hour, or minute.

Pin Function

Terminal connection / External dimensions

(Unit:mm)

Signal Name	Input/Output	Function														
SCL	Input	Serial clock input pin.														
SDA	Bi-directional	Data input and output pin.														
CLKOUT	Output	32.768 kHz clock output pin with the output control function. (C-MOS) CLKOE pin control the condition of CLKOUT with FE-bit, etc.														
CLKOE	Input	<table border="1"> <thead> <tr> <th>CLKOE pin input</th> <th>FE bit</th> <th>CLKOUT pin output</th> </tr> </thead> <tbody> <tr> <td>HIGH</td> <td>1</td> <td>Output (C-MOS)</td> </tr> <tr> <td rowspan="2">LOW</td> <td>0</td> <td>OFF (LOW)</td> </tr> <tr> <td>1</td> <td>OFF (LOW)</td> </tr> <tr> <td></td> <td>0</td> <td>OFF (LOW)</td> </tr> </tbody> </table>	CLKOE pin input	FE bit	CLKOUT pin output	HIGH	1	Output (C-MOS)	LOW	0	OFF (LOW)	1	OFF (LOW)		0	OFF (LOW)
CLKOE pin input	FE bit	CLKOUT pin output														
HIGH	1	Output (C-MOS)														
LOW	0	OFF (LOW)														
	1	OFF (LOW)														
	0	OFF (LOW)														
/INT	Output	Interrupt output (N-ch open drain)														
VDD	—	Connected to a positive power supply.														
GND	—	Connected to a ground.														



Specifications (characteristics)

* Refer to application manual for details.

Recommended Operating Conditions

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Power voltage	VDD	—	1.8	3.0	5.5	V
Clock voltage	VCLK	—	VLOW	3.0	5.5	V
Operating temperature	TOPR	—	-40	+25	+85	°C

Low voltage detection

Item	Symbol	Conditions	Typ.	Max.	Unit
Low voltage detection	VLOW	LC Ta = -20 °C ~ +70 °C	0.9	1.2	V
		Ta = -40 °C ~ +85 °C	0.9	1.3	V

Frequency characteristics

Item	Symbol	Conditions	Rating	Unit
Frequency tolerance	$\Delta f/f$	Ta = +25 °C VDD = 3.0 V	B: 5 ± 23 *	× 10 ⁻⁶

* Please ask for tighter tolerance. (Equivalent to ±1 minute of monthly deviation)

Current consumption characteristics

Ta = -40 °C to +85 °C

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit	
Current Consumption	I _{BK}	f _{SCL} = 0 Hz CLKOE = GND CLKOUT ; output OFF (LOW)	VDD = 5 V	-	330	800	nA
			VDD = 3 V	-	275	700	
Current Consumption	I _{S2K}	f _{SCL} = 0 Hz CLKOE = VDD CLKOUT ; 32.768 kHz output ON (Output=OPEN ; CL = 0 pF)	VDD = 5 V	-	2.5	3.4	μA
			VDD = 3 V	-	1.5	2.2	

REAL TIME CLOCK MODULE (I²C-Bus)

Low current consumption

RX8010SJ



Product Number
RX8010SJ : X1B000242000100

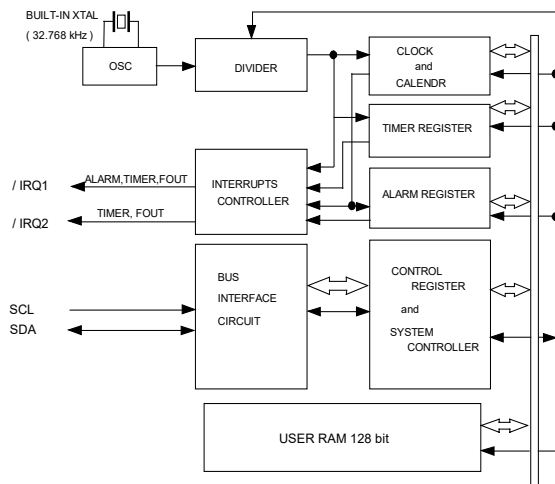


- Built in frequency adjusted 32.768 kHz crystal unit.
- Interface Type : I²C-Bus interface (400 kHz)
- Operating voltage range : 1.6 V to 5.5 V
- Wide voltage for time keeping. : 1.1 V to 5.5 V
- Low backup current : 160 nA / 3 V (Typ.)
- Frequency output function : C-MOS or Open-Drain output
- Built-in user RAM : 128 bit (8 bit x 16, SRAM)
- The various functions include full calendar, alarm, timer, etc.
- This product is conform to industrial standard SOP8 package, and it can be mounted to the common land pattern.

Epson prepared Linux driver for development.
(http://www5.epsondevice.com/en/information/support/linux_rtc/)

The registered trademark Linux® is used pursuant to a sublicense from LMI(Linux Mark Institute)

Block diagram



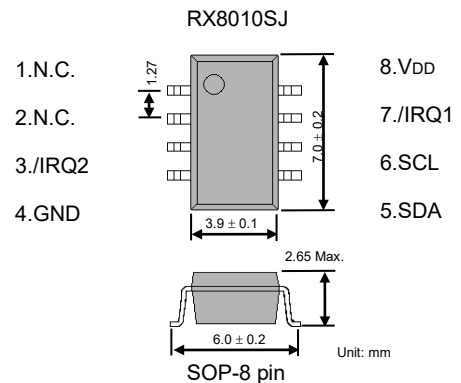
Overview

- **Interface Type**
 - I²C-Bus high-speed bus specifications. (400 kHz)
- **Frequency output function**
 - It may select a CMOS or open drain output
 - Output frequency can be selected as 32.768kHz, 1024Hz, 1Hz.
- **Timer function**
 - Timer function can be set up between 1/4096 second and 65535 hours.
 - Timing period are 1hour, 1min, 64Hz, 4096Hz.
 - It is recorded automatic to TF-bit at the time of event occurrence, and possible to output with /IRQ1 or /IRQ2 pin.
- **Alarm function**
 - Alarm function can be set to day of week, day, hour, and minute.
 - It is recorded automatic to AF-bit at the time of event occurrence, and possible to output with /IRQ1 pin.
- **User RAM**
 - 128 bit (8 bit x 16, SRAM)

Pin Function

Signal Name	Input / Output	Function
SCL	Input	This is a shift clock input pin for serial data transmission.
SDA	Input/Output	This is the data input/output pin for serial data transfer.
/IRQ1	Output	This pin outputs interrupt signals ("L" level) for alarm, timer, time update, and 32.768kHz. This is an N-ch open-drain output.
/IRQ2	Output	This pin outputs interrupt signals ("L" level) for timer and FOUT. This is a C-MOS output.
VDD	Supply	This is a power-supply pin.
GND	Supply	This pin is connected to a ground.

Terminal connection / External dimensions (Unit:mm)



The metal case inside of the molding compound may be exposed on the top or bottom of this product. This purely cosmetic and does not have any effect on quality, reliability or electrical specs.

Specifications (characteristics)

* Refer to application manual for details.

Recommended Operating Conditions

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Power voltage	VDD	—	1.6	3.0	5.5	V
Clock voltage	VCLK	—	1.1	3.0	5.5	V
Operating temperature	TOPR	—	-40	+25	+85	°C

Frequency characteristics

Item	Symbol	Conditions	Rating	Unit
Frequency tolerance	$\Delta f / f$	T _a = +25 °C VDD = 3.0 V	B: 5 ± 23 *	× 10 ⁻⁶
Oscillation Start-up time	t _{STA}	T _a = +25 °C VDD = 1.6 V ~ 5.5 V T _a = -40 °C to +85 °C VDD = 1.6V ~ 5.5V	1 Max. 3 Max.	s

*Equivalent to ±1 minute of monthly deviation

Current consumption characteristics

T_a = -40 °C to +85 °C

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit	
Current Consumption	I _{BK}	Input pins are "L" fCLK = 0 Hz, /IRQ1,2 = OFF TSEL2="1"	VDD = 5 V	-	-	350	nA
			VDD = 3 V	-	160	320	
	I _{32K}	fCLK = 0 Hz, /IRQ1: 32.768 kHz ON, /IRQ2: OFF	VDD = 5 V	-	0.60	1.10	µA
			VDD = 3 V	-	0.52	0.90	

REAL TIME CLOCK MODULE (SPI & I²C-Bus)

Power Switching and Low current consumption



Product Number
RX6110SA B : X1B000232000100

RX6110SA

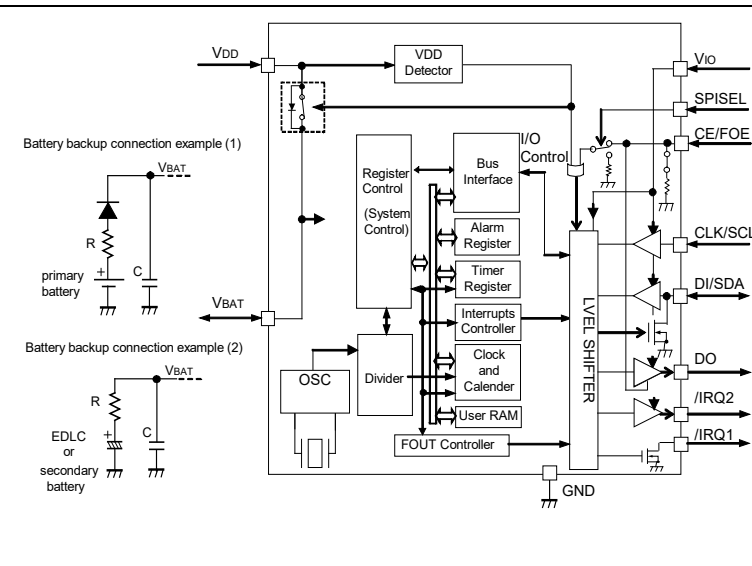
- Built in frequency adjusted 32.768 kHz crystal unit.
- Interface Type : SPI & I²C -Bus
- Operating voltage range : 1.6 V to 5.5 V
- The wide voltage for time keeping : 1.1 V to 5.5 V
- Low backup current : 130 nA / 3 V (Typ.)
- Built-in user RAM : 128 bit (8 bit × 16, SRAM)
- Auto power switching functions : When VDD deteriorates than 1.6V, internal source is switched to VBAT.

•The various functions include full calendar, alarm, timer.
Epson prepared Linux driver for development.
(http://www5.epsondevice.com/en/information/support/linux_rtc/)

The registered trademark Linux® is used pursuant to a sublicense from LMI(Linux Mark Institute)



Block diagram



Overview

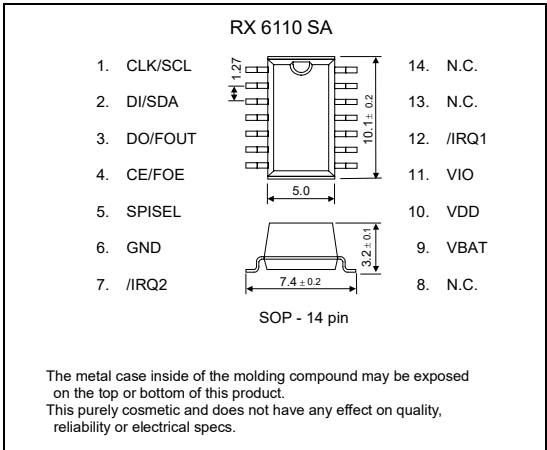
- Interface type.
 - SPI-Bus and I²C-Bus interface.
 - By a terminal, a switchover of the interface is possible.
- Built-in auto power switching function
 - To efficiently charge from VDD to backup battery (Secondary battery, Large capacitor) connected to VBAT is possible. Detects VDD voltage drop(VDET-) and automatically switches to the backup battery.
- Frequency output function
 - Output frequency is selectable from 32.768kHz, 1024Hz, 1Hz.
- Timer function
 - Timer function is selectable in 1/4096 second from 65535 hours.
- Timer source clock are 1hour, 1min, 64Hz, 4096Hz.
 - It is recorded automatic to TF-bit at the time of event occurrence, and possible to output with /IRQ1 or /IRQ2 pin.
- Alarm function
 - Alarm function can be set to day of week, day, hour, and minute.
 - It is recorded automatic to AF-bit at the alarm occurrence, and possible to output with /IRQ1 pin output.
- User RAM
 - 128 bit (8 bit x 16, SRAM)

Pin Function

Signal Name	Input/Output	Function
SPISEL	Input	The interface select pin. SPI is chosen at a "H" level (V _{IO} voltage) / I ² C is chosen at a "L" level (GND voltage).
CE/FOE	Input	SPI: Should be held high to allow access to the CPU. Incorporates a pull-down resistor. I ² C: It is an input pin for controlling the DO/FOUT output. When the frequency output from a DO/FOUT pin does not need, CE/FOE pin must be connected to GND.
CLK/SCL	Input	This is a shift clock input pin for serial data transmission.
DI/SDA	Input / Output	SPI: This is the data input pin for serial data transfer. I ² C: This is the data input/output pin for serial data transfer.
DO/FOUT	Output	SPI: This is the data output pin for serial data transfer. I ² C: This is the C-MOS output pin with output control provided via the CE/FOE pin. (frequency selection: 32.768 kHz / 1024 Hz / 1Hz / Hi-z)
/IRQ1	Output	This pin outputs interrupt signals ("L" level) for alarm, timer, time update, and FOUT. This is an N-ch open-drain output. This pin can output even a backup mode.
/IRQ2	Output	This pin outputs interrupt signals ("L" level) for timer and FOUT. This is an C-MOS output. This pin becomes Hi-z in less than V _{DD} =1.6V.
VDD	-	This is a power-supply pin. It can impress the voltage unlike V _{IO} .
V _{IO}	-	This pin is a power supply for input and the output and input / output pins. Connected to a positive power supply.
V _{BAT}	-	Connect a secondary battery or capacitor for backup power supply. If a backup power supply is not present, this pin connect to V _{DD} .
GND	-	Connected to a ground.

Terminal connection / External dimensions

(Unit:mm)



The metal case inside of the molding compound may be exposed on the top or bottom of this product. This purely cosmetic and does not have any effect on quality, reliability or electrical specs.

Specifications (characteristics)

* Refer to application manual for details.

Recommended Operating Conditions

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Power voltage	V _{DD}	—	1.6	3.0	5.5	V
Clock voltage	V _{CLK}	—	1.1	3.0	5.5	V
Operating temperature	T _{OPR}	—	-40	+25	+85	°C

Frequency characteristics

Item	Symbol	Conditions	Rating	Unit
Frequency tolerance	Δf/f	T _A = +25 °C V _{DD} = 3.0 V	B: 5 ± 23 *1	× 10 ⁻⁶
Oscillation start-up time	t _{STA}	T _A = +25 °C V _{DD} = 1.6 V	1 Max.	s

*1) Equivalent to ±1 minute of monthly deviation (excluding offset.) / Standard product

Current consumption characteristics

T_A = -40 °C to +85 °C

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Current Consumption	I _{BK}	V _{BAT} = 3.0 V Input pins are "L", V _{DD} = 0 V DO/FOUT=OFF, f _{CLK} = 0 Hz, /IRQ1,2 = OFF, TSEL2="1" It include an OFF leak current of SW between the power supply (V _{BAT} -V _{DD})	-	130	250	nA
	I _{32k}	V _{DD} = 3.0 V f _{CLK} = 0 Hz, /IRQ1,2 = OFF, CE/FOE = V _{IO} , DO/FOUT : 32.768 kHz ON, CL = 0 pF	-	1.5	2.1	μA

REAL TIME CLOCK MODULE (I²C-Bus)

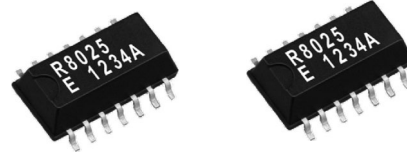
High-Stability



Product Number
 RX-8025SA AA : Q41802552000100
 RX-8025SA AC : Q41802551000200

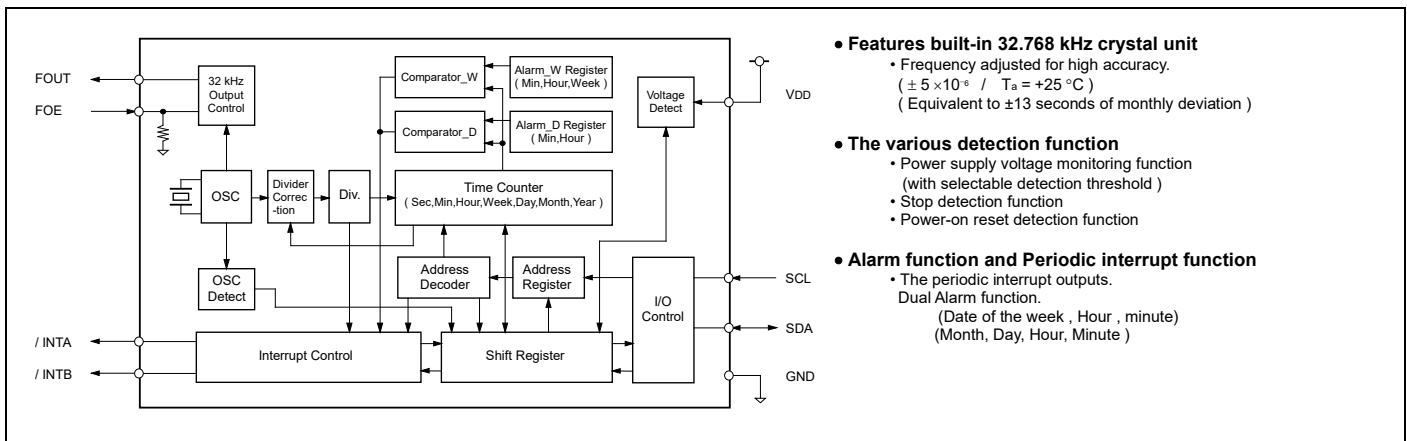
RX-8025SA

- Built-in 32.768 kHz crystal unit : Frequency adjusted for high accuracy ($\pm 5 \times 10^{-6}$ / $T_a = +25^\circ\text{C}$)
- Interface Type : I²C-Bus Interface (400 kHz)
- Operating voltage range : 1.70 V to 5.5 V
- Wide voltage for timekeeping : 1.15 V to 5.5 V
- Various detection Functions : Ex. Oscillation stop detection function
- Low backup current : 0.48 μA / 3 V (Typ.)
- 32.768 kHz frequency output function : C-MOS output with OE pin.
- The various functions include full calendar, Dual alarm, Periodic interruption.



Block diagram

Overview

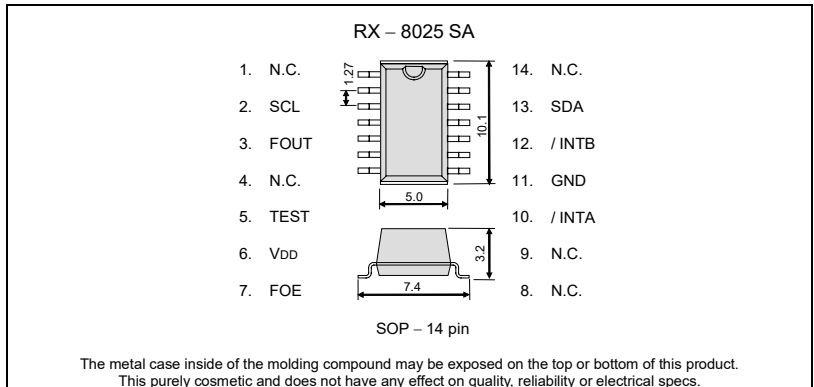


- **Features built-in 32.768 kHz crystal unit**
 - Frequency adjusted for high accuracy. ($\pm 5 \times 10^{-6}$ / $T_a = +25^\circ\text{C}$) (Equivalent to ± 13 seconds of monthly deviation)
- **The various detection function**
 - Power supply voltage monitoring function (with selectable detection threshold)
 - Stop detection function
 - Power-on reset detection function
- **Alarm function and Periodic interrupt function**
 - The periodic interrupt outputs. Dual Alarm function. (Date of the week, Hour, minute) (Month, Day, Hour, Minute)

Pin Function

Terminal connection / External dimensions (Unit:mm)

Signal Name	Input / output	Function																					
SCL	Input	Serial clock input pin																					
SDA	Bi-directional	Data input and output pin																					
FOUT	Output	32.768 kHz clock output pin with the output control function. (C-MOS)																					
FOE	Input	<table border="1"> <thead> <tr> <th>FOE input</th> <th>/CLEN1 bit</th> <th>/CLEN2 bit</th> <th>FOUT output</th> </tr> </thead> <tbody> <tr> <td>L</td> <td>X</td> <td>X</td> <td>OFF (LOW)</td> </tr> <tr> <td rowspan="3">H</td> <td>0</td> <td>0</td> <td>32.768 kHz</td> </tr> <tr> <td>0</td> <td>1</td> <td>32.768 kHz</td> </tr> <tr> <td>1</td> <td>0</td> <td>32.768 kHz</td> </tr> <tr> <td>1</td> <td>1</td> <td>OFF (LOW)</td> </tr> </tbody> </table>	FOE input	/CLEN1 bit	/CLEN2 bit	FOUT output	L	X	X	OFF (LOW)	H	0	0	32.768 kHz	0	1	32.768 kHz	1	0	32.768 kHz	1	1	OFF (LOW)
		FOE input	/CLEN1 bit	/CLEN2 bit	FOUT output																		
		L	X	X	OFF (LOW)																		
		H	0	0	32.768 kHz																		
0	1		32.768 kHz																				
1	0		32.768 kHz																				
1	1	OFF (LOW)																					
/INTA	Output	Interrupt output A pin (N-ch open drain)																					
/INTB	Output	Interrupt output B pin (N-ch open drain)																					
TEST	—	* Used by the manufacture for testing. (Do not connect externally.)																					
VDD	—	Connected to a positive power supply.																					
GND	—	Connected to a ground.																					



Specifications (characteristics)

* Refer to application manual for details.

Recommended Operating Conditions

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Power voltage	VDD	—	1.7	3.0	5.5	V
Clock voltage	VCLK	—	1.15	3.0	5.5	V
Operating temperature	TOPR	—	-40	+25	+85	°C

Frequency characteristics

Item	Symbol	Conditions	Range	Unit
Frequency tolerance	$\Delta f/f$	$T_a = +25^\circ\text{C}$ VDD = 3.0 V	AA: 5 ± 5 ^{*1)} AC: 0 ± 5 ^{*2)}	$\times 10^{-6}$
Oscillation start-up time	t _{STA}	$T_a = +25^\circ\text{C}$ VDD = 2.0 V	1 Max.	s
Frequency voltage characteristics	f/V	$T_a = +25^\circ\text{C}$ VDD = 2.0 V to 5.5 V	± 1 Max.	$\times 10^{-6}$

*1) *2) Equivalent to ± 13 seconds of monthly deviation (excluding offset).

Current consumption characteristics

Item	Symbol	Conditions	$T_a = -40^\circ\text{C}$ to $+85^\circ\text{C}$				
			Min.	Typ.	Max.	Unit	
Current Consumption	I _{BK}	f _{SCL} = 0Hz FOE = GND FOUT ; output OFF (LOW)	VDD = 5 V	-	0.60	1.80	μA
			VDD = 3 V	-	0.48	1.20	
	I _{32k}	f _{SCL} = 0Hz VDD, FOE = 5.5 V FOUT ; output ON (Output=OPEN; CL = 0 pF)	VDD = 5.5 V	-	3.0	6.5	μA

Power supply detection voltage

Item	Symbol	Conditions	$T_a = -30^\circ\text{C}$ to $+70^\circ\text{C}$			
			Min.	Typ.	Max.	Unit
High-voltage mode	VDETH	VDD pin	1.90	2.10	2.30	V
Low-voltage mode	VDETL	VDD pin	1.15	1.30	1.45	V

REAL TIME CLOCK MODULE (SPI-Bus)

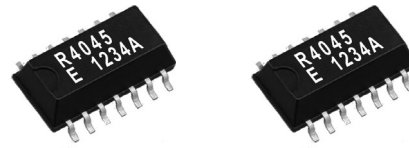
High-Stability Frequency



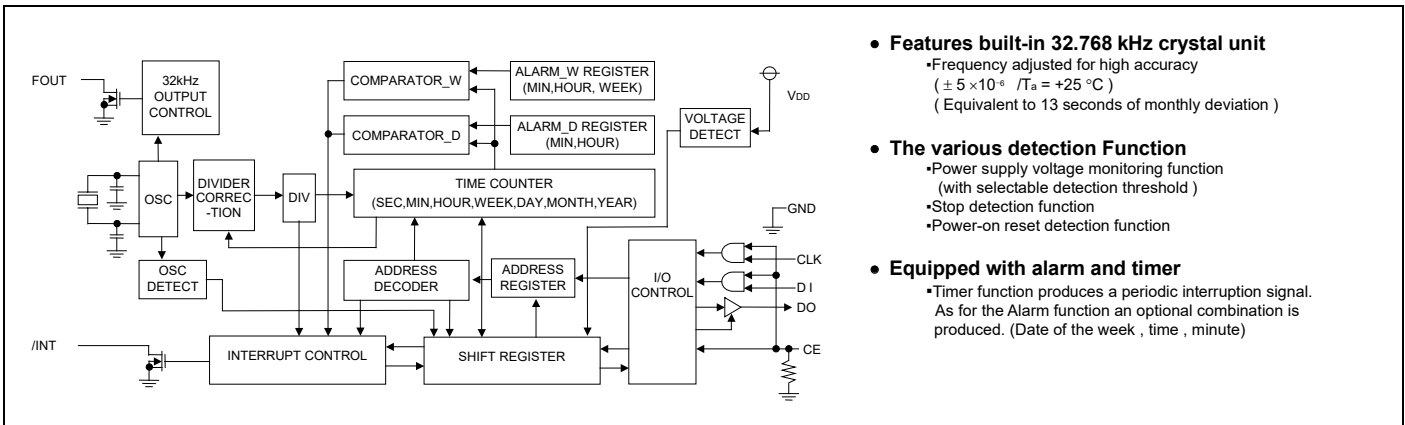
Product Number
 RX-4045SA AA: Q41404552000100
 RX-4045SA AC: Q41404551000200

RX-4045SA

- Built-in 32.768 kHz crystal unit: Frequency adjusted for high accuracy. ($\pm 5 \times 10^{-6} / T_a = +25^\circ\text{C}$)
- Interface Type : 4-wire serial interface
- Operating voltage range : 1.7 V to 5.5 V
- Wide Timekeeper voltage range : 1.15 V to 5.5 V
- Various detection Functions : Oscillation stop detection function etc.
- Low backup current : 0.48 μA / 3 V (Typ.)
- 32.768 kHz clock frequency output : N-ch open drain output
- Function of time and calendar, the various detection function, and interrupt function etc.



Block diagram



Overview

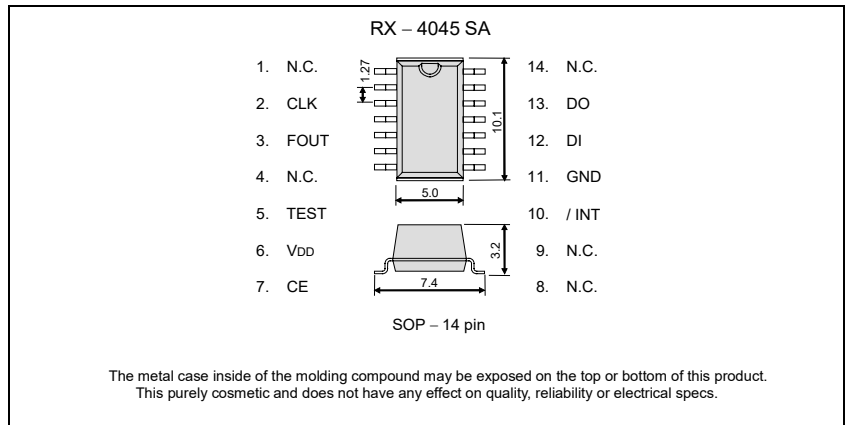
- **Features built-in 32.768 kHz crystal unit**
 - Frequency adjusted for high accuracy ($\pm 5 \times 10^{-6} / T_a = +25^\circ\text{C}$) (Equivalent to 13 seconds of monthly deviation)
- **The various detection Function**
 - Power supply voltage monitoring function (with selectable detection threshold)
 - Stop detection function
 - Power-on reset detection function
- **Equipped with alarm and timer**
 - Timer function produces a periodic interruption signal. As for the Alarm function an optional combination is produced. (Date of the week, time, minute)

Pin function

Signal Name	Input / Output	Function
CE	Input	The chip enabled input pin. (built-in pull-down resistance) At the "H" level, access becomes possible.
CLK	Input	The shift clock input pin for serial data transfer.
DI	Input	The data input pin for serial data transfer.
DO	Output	The data output pin for serial data transfer.
FOUT	Output	32.768 kHz clock output pin with the output control function (N-ch open drain) High impedance at the time of output off.
/INT	Output	Interrupt output (N-ch open drain)
TEST	—	* Used by the manufacturer for testing. (Do not connect externally.)
VDD	—	Connected to a positive power supply.
GND	—	Connected to a ground.

Terminal connection / External dimensions

(Unit:mm)



Specifications (characteristics)

* Refer to application manual for details.

Recommended Operating Conditions

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Power voltage	VDD	—	1.7	3.0	5.5	V
Clock voltage	VCLK	—	1.15	3.0	5.5	V
Operating temperature	TOPR	—	-40	+25	+85	°C

Frequency characteristics

Item	Symbol	Conditions	Rating	Unit
Frequency tolerance	$\Delta f / f$	$T_a = +25^\circ\text{C}$ $V_{DD} = 3.0\text{ V}$	AA: 5 ± 5 ^{*1)} AC: 0 ± 5 ^{*2)}	$\times 10^{-6}$
Oscillation start-up time	t_{STA}	$T_a = +25^\circ\text{C}$ $V_{DD} = 2.0\text{ V}$	1 Max.	s
Frequency / voltage characteristics	f / V	$T_a = +25^\circ\text{C}$ $V_{DD} = 2.0\text{ V to } 5.5\text{ V}$	± 1 Max.	$\times 10^{-6}$

*1) *2) Equivalent to ± 13 seconds of monthly deviation (excluding offset.)

Current consumption characteristics

$T_a = -40^\circ\text{C to } +85^\circ\text{C}$

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit	
Current Consumption	I _{bk}	CE = GND FOUT ; output OFF (Hi-Z)	$V_{DD} = 5\text{ V}$	-	0.60	1.80	μA
			$V_{DD} = 3\text{ V}$	-	0.48	1.20	
	I _{32k}	CE = GND FOUT ; 32.768 kHz output ON	$V_{DD} = 3\text{ V}$	-	0.65	2.00	μA

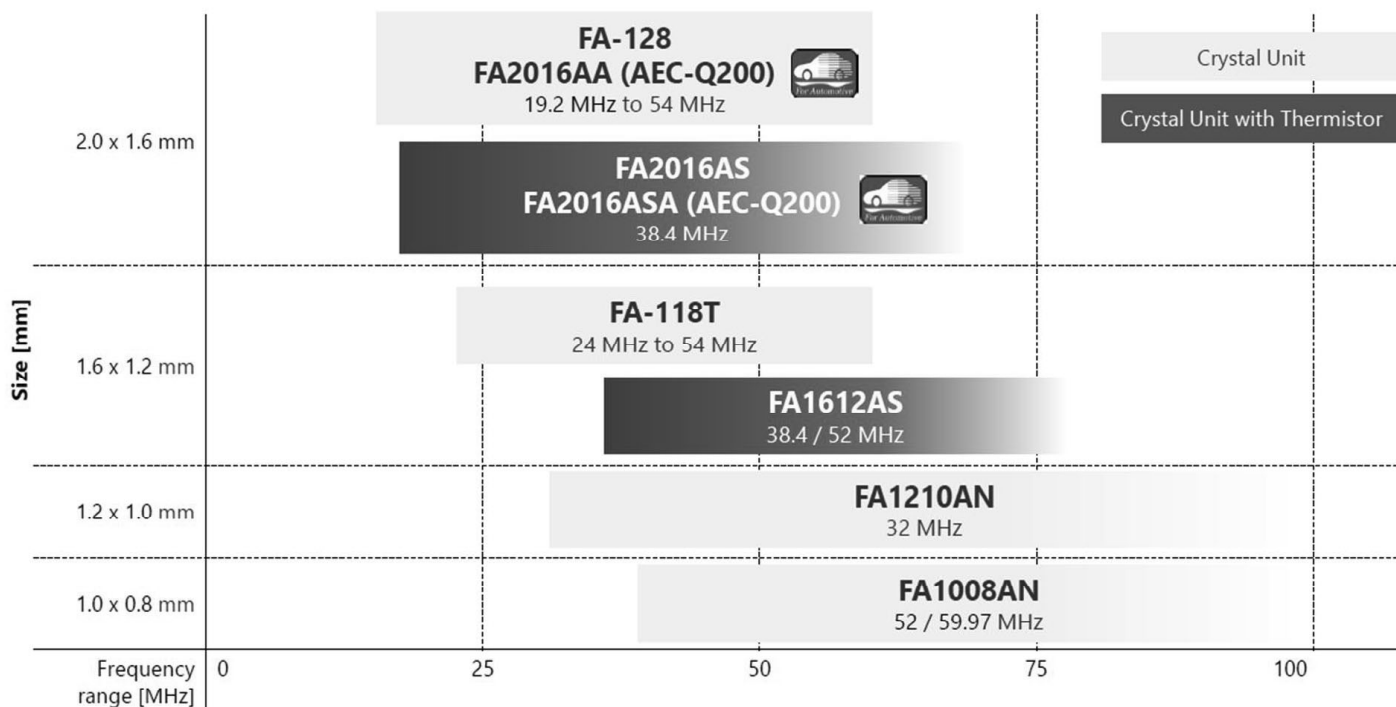
Power supply detection voltage

$T_a = -30^\circ\text{C to } +70^\circ\text{C}$

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
High-voltage mode	V _{DETH}	VDD pin	1.90	2.10	2.30	V
Low-voltage mode	V _{DETL}	VDD pin	1.15	1.30	1.45	V

MHz Crystal Units

MHz Crvstal Unit. Crvstal Unit with Thermistor Recommendation Products



► MHz Crystal Unit

P	Model	Size [mm]	Nominal frequency range	Frequency tolerance (+25 °C) [x 10 ⁻⁶]	Frequency vs. temperature characteristics [x10 ⁻⁶] / Operating temperature [°C]	Motional resistance Max. [Ω]	Load Capacitance [pF]	Operating temperature [°C]
43	FA1008AN	1.0 × 0.8 × 0.3t	40 MHz - 100 MHz	±10	±10 / -20 to +75 ±15 / -30 to +85 ±20 / -40 to +85	60	6 to ∞	-40 to +85 (-40 to +105)
44	FA1210AN	1.2 × 1.0 × 0.3t	32 MHz - 100 MHz	±10	±10 / -20 to +75 ±15 / -30 to +85 ±20 / -40 to +85	100 (32 ≤ f < 50 MHz) 60 (50 ≤ f ≤ 100 MHz)	6 to ∞	-40 to +85 (-40 to +105)
45	FA-118T	1.6 × 1.2 × 0.35t	24 MHz - 54 MHz	±30	±30 / -20 to +75	200 (24 ≤ f < 32 MHz) 100 (32 ≤ f < 36 MHz) 80 (36 ≤ f ≤ 54 MHz)		-40 to +85
46	FA-128	2.0 × 1.6 × 0.5t	19.2 MHz - 54 MHz	±10 ±30	±10 / -20 to +75 ±30 / -20 to +75	150 (19.2 ≤ f < 20 MHz) 100 (20 ≤ f < 24 MHz) 80 (24 ≤ f < 26 MHz) 60 (26 ≤ f ≤ 54 MHz)	6 to ∞	
47	FA-20H	2.5 × 2.0 × 0.55t	12 MHz - 54 MHz	±10 ±30	±10 / -20 to +75 ±30 / -20 to +75	150 (12 ≤ f < 16 MHz) 80 (16 ≤ f ≤ 25 MHz) 60 (25 < f ≤ 30 MHz) 50 (30 < f ≤ 35 MHz) 40 (30 < f ≤ 35 MHz)		-40 to +85 (-40 to +105)
48	FA-238V	3.2 × 2.5 × 0.7t	12 MHz - 15.999 MHz	±15 ±50	±30 / -20 to +70	100 (12 ≤ f ≤ 13 MHz) 80 (13 < f < 20 MHz) 60 (20 ≤ f < 25 MHz) 50 (25 ≤ f < 30 MHz) 40 (30 ≤ f ≤ 50 MHz)	7 to ∞	
	FA-238	3.2 × 2.5 × 0.7t	16 MHz - 50 MHz					
	TSX-3225	3.2 × 2.5 × 0.6t	16 MHz - 48 MHz					
123	FA2016AA (AEC-Q200)	2.0 × 1.6 × 0.5t	19.2 MHz - 54 MHz	±10	±20 / -40 to +85 ±50 / -40 to +125	150 (19.2 ≤ f < 20 MHz) 100 (20 ≤ f < 24 MHz) 80 (24 ≤ f < 26 MHz) 60 (26 ≤ f ≤ 54 MHz)	6 to ∞	-40 to +125
124	FA-238A (AEC-Q200)	3.2 × 2.5 × 0.7t	12 MHz - 50 MHz	±15	±30 / -40 to +85 ±50 / -40 to +125	120 (12 ≤ f ≤ 13 MHz) 80 (13 < f < 20 MHz) 60 (20 ≤ f < 25 MHz) 50 (25 ≤ f ≤ 50 MHz)	7 to ∞	-40 to +125

► MHz Crystal Unit (Built-in Thermistor)

P	Model	Size [mm]	Nominal frequency range	Frequency tolerance (+25 °C) [x 10 ⁻⁶]	Frequency vs. temperature characteristics [x10 ⁻⁶] / Operating temperature [°C]	Motional resistance Max. [Ω]	Load Capacitance [pF]	Operating temperature [°C]
49	FA1612AS	1.6 × 1.2 × 0.65t	38.4 MHz - 52 MHz	±10	±12 / -30 to +85	80	6 to ∞	-40 to +85
50	FA2016AS	2.0 × 1.6 × 0.65t	38.4 MHz	±10	±12 / -30 to +85	60	6 to ∞	-40 to +85
Web site	FA-20HS	2.5 × 2.0 × 1.0t	19.2 MHz - 26 MHz	±10	±12 / -30 to +85	80 (19.2 MHz) 60 (26.0 MHz)	6 to ∞	-40 to +85
125	FA2016ASA (AEC-Q200)	2.0 × 1.6 × 0.68t	38.4 MHz	±10	±12 / -30 to +85 ±30 / -40 to +105	50	6 to ∞	-40 to +105

*1 Please contact us about reference temperature.

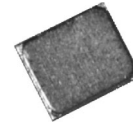


MHz RANGE CRYSTAL UNIT

FA1008AN



Product Number
FA1008AN: X1E000451xxx26



FA1008AN

(1.0 x 0.8 mm, t = 0.3 mm Max.)

- Nominal frequency range : 40 MHz to 100 MHz
- External dimensions : 1.0 x 0.8 mm, t = 0.3 mm Max.
- Overtone order : Fundamental
- Applications : Small wireless device
Small wireless module
Wearable device
Health care equipment
Small consumer equipment etc.

Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
Nominal frequency range	f_nom	40 MHz to 100 MHz	Please contact us for requirements not listed in this specification.
		52 MHz, 59.97 MHz	Standard frequency
Storage temperature range	T_stg	-40 °C to +125 °C	Storage as single product.
Operating temperature range	T_use	-40 °C to +85 °C (+105 °C)	Please contact us about +85 °C < T_use
Level of drive	DL	100 μW Max.	Recommended: 10 μW
Frequency tolerance (standard)	f_tol	±10 x 10 ⁻⁶	+25 °C, Please contact us for requirements not listed in this specification.
Frequency versus temperature characteristics. (standard)	f_tem	±10 x 10 ⁻⁶ / -20 °C to +75 °C	Specify from the specifications on the left Please contact us for requirements not listed in this specification.
		±15 x 10 ⁻⁶ / -30 °C to +85 °C	
		±20 x 10 ⁻⁶ / -40 °C to +85 °C	
Load capacitance	CL	6 pF to ∞	Please specify.
Motional resistance (ESR)	R1	60 Ω Max. (52 MHz, 59.97MHz)	-40 °C to +85 °C, DL = 10 μW
Frequency aging	f_age	±1 x 10 ⁻⁶ / year Max. (52 MHz, 59.97 MHz)	+25 °C, First year

Product name

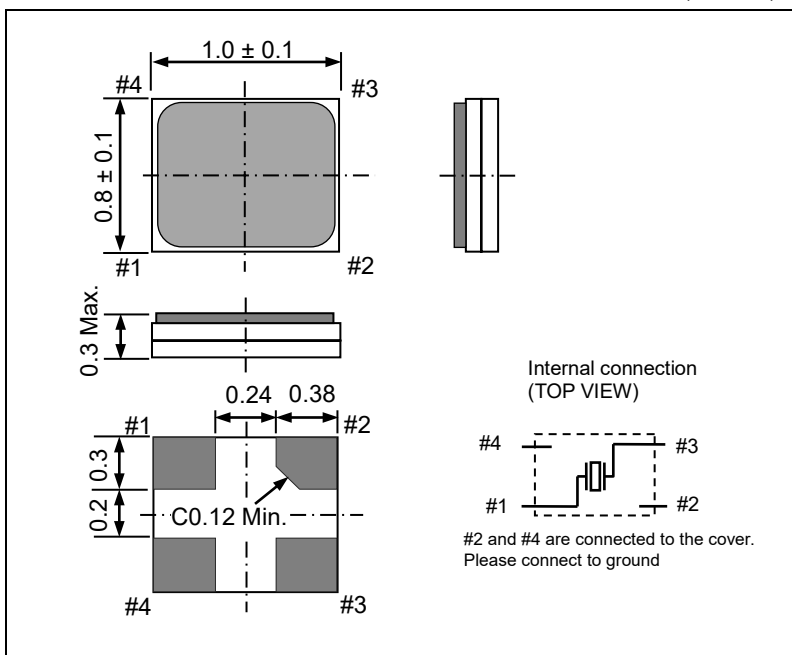
Product name FA1008AN 59.970000MHz 12.0 +10.0+10.0
 (Standard form) ① ② ③ ④

①Model ②Frequency ③Load capacitance (pF) ④Frequency tolerance (× 10⁻⁶, +25 °C)

In addition to the mentioned above specification item, please specify frequency temperature characteristics and operating temperature range in case of inquiry.

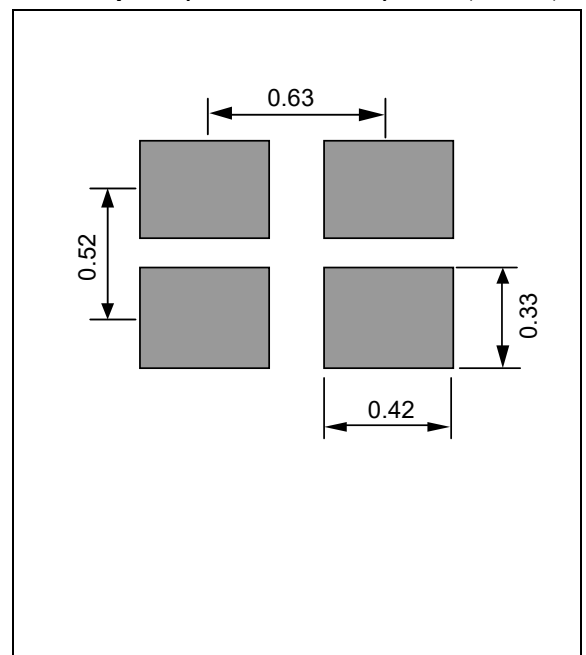
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



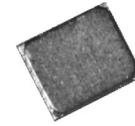


MHz RANGE CRYSTAL UNIT

FA1210AN

Product Number
FA1210AN: X1E000411xxxx26

- Nominal frequency range : 32 MHz to 100 MHz
- External dimensions : 1.2 × 1.0 mm , t = 0.3 mm Max.
- Overtone order : Fundamental
- Applications : Small wireless device
Small wireless module
Wearable device
Health care equipment
Small consumer equipment etc.



FA1210AN

(1.2 x 1.0 mm t = 0.3 mm Max.)

Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
Nominal frequency range	f_nom	32 MHz to 100 MHz	Please contact us for requirements not listed in this specification.
		32 MHz	Standard frequency
Storage temperature range	T_stg	-40 °C to +125 °C	Storage as single product.
Operating temperature range	T_use	-40 °C to +85 °C (+105 °C)	Please contact us about +85 °C < T_use
Level of drive	DL	100 μW Max.	Recommended: 10 μW
Frequency tolerance (standard)	f_tol	±10 × 10 ⁻⁶	+25 °C, Please contact us for requirements not listed in this specification.
Frequency versus temperature characteristics. (standard)	f_tem	±10 × 10 ⁻⁶ / -20 °C to +75 °C	Specify from the specifications on the left Please contact us for requirements not listed in this specification.
		±15 × 10 ⁻⁶ / -30 °C to +85 °C	
		±20 × 10 ⁻⁶ / -40 °C to +85 °C	
Load capacitance	CL	6 pF to ∞	Please specify.
Motional resistance (ESR)	R1	100 Ω Max. (32 MHz)	-40 °C to +85 °C, DL = 10 μW
Frequency aging	f_age	±1 × 10 ⁻⁶ / year Max. (32 MHz)	+25 °C, First year

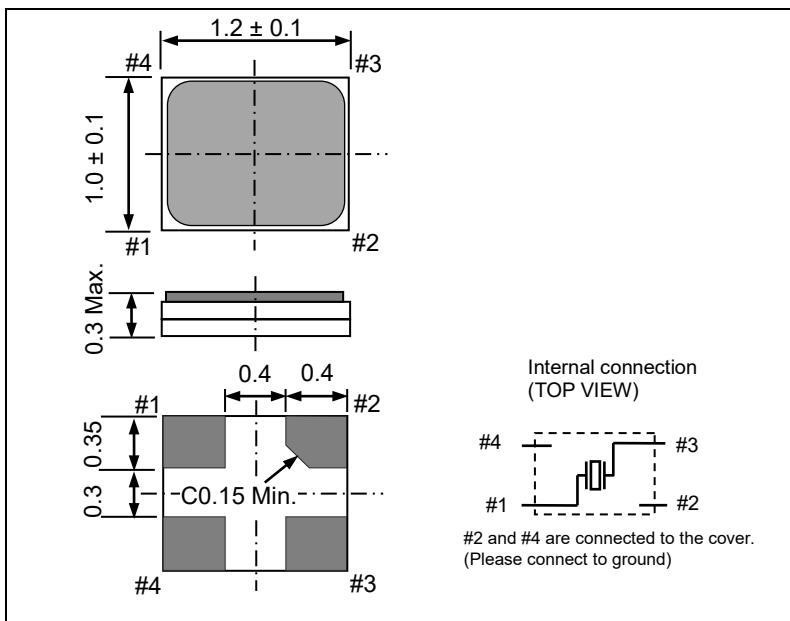
Product name

Product name FA1210AN 32.000000MHz 12.0 +10.0-10.0
(Standard form) ① ② ③ ④

①Model ②Frequency ③Load capacitance(pF) ④Frequency tolerance(× 10⁻⁶, +25 °C)
In addition to the mentioned above specification item, please specify frequency temperature characteristics and operating temperature range in case of inquiry.

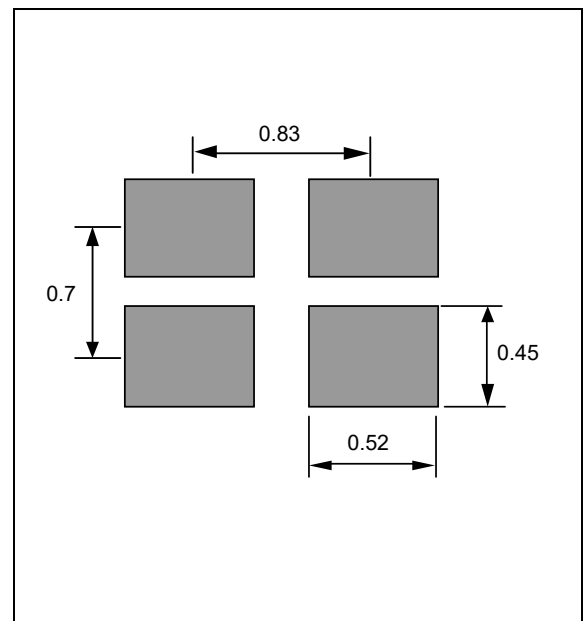
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)





MHz RANGE CRYSTAL UNIT

FA-118T

Product Number
FA-118T : X1E000251xxxx26

- Nominal frequency range : 24 MHz to 54 MHz
- External dimensions : 1.6 × 1.2 × 0.35 mm
- Overtone order : Fundamental
- Applications : Mobile phone, Bluetooth, W-LAN
ISM band radio, Clock for MPU



Specifications (characteristics)

Item	Symbol	Specifications		Conditions / Remarks
		For RF Reference	For Clock	
Nominal frequency range	f_nom	24 MHz to 54 MHz		Fundamental Please contact us about available frequencies.
Storage temperature range	T_stg	-40 °C to +125 °C		Storage as single product.
Operating temperature range	T_use	-40 °C to +85 °C (+105 °C)		Please contact us about +85 °C < T_use
Level of drive	DL	100 μW Max.	200 μW Max.	Recommended: 10 μW
Frequency tolerance (standard)	f_tol	±10 × 10 ⁻⁶ *1	±30 × 10 ⁻⁶	+25 °C, please contact us for requirements not listed in this specification.
Frequency versus temperature characteristics. (standard)	f_tem	±12 × 10 ⁻⁶ *1	±30 × 10 ⁻⁶	-20 °C to +75 °C, please contact us for requirements not listed in this specification.
Load capacitance	CL	6 pF to ∞		Please specify.
Motional resistance (ESR)	R1	As per below table1.		-20 °C to +75 °C
Frequency aging	f_age	±1 × 10 ⁻⁶ / year Max.	±5 × 10 ⁻⁶ / year Max.	+25 °C, First year

*1 Please contact us for available frequency tolerances as they are dependent upon the nominal frequency.

Table 1. Motional resistance(ESR) R1

Frequency	Motional resistance
24 MHz ≤ f_nom < 32 MHz	200 Ω Max.
32 MHz ≤ f_nom < 36 MHz	100 Ω Max.
36 MHz ≤ f_nom ≤ 54 MHz	80 Ω Max.

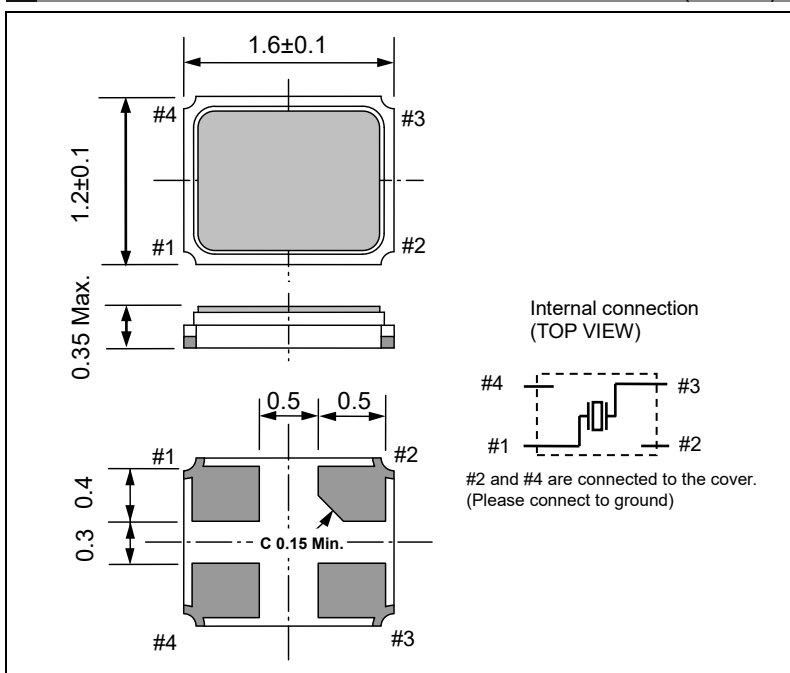
Product name

Product name FA-118T 24.000000MHz 12.0 +10.0-10.0
(Standard form) ① ② ③ ④①Model ②Frequency ③Load capacitance(pF) ④Frequency tolerance(× 10⁻⁶, +25 °C)

In addition to the above mentioned specification item, please specify frequency temperature characteristics and operating temperature range in case of inquiry.

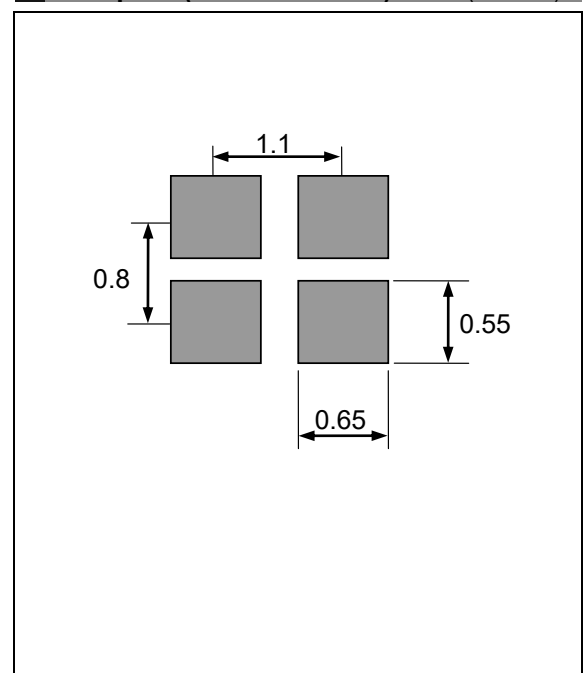
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)





MHz RANGE CRYSTAL UNIT

FA-128

Product Number
Q22FA1280xxxx18

- Nominal frequency range : 19.2 MHz to 54 MHz
- External dimensions : 2.0 × 1.6 × 0.5 mm
- Overtone order : Fundamental
- Applications : Mobile phone, Bluetooth, W-LAN
ISM band radio, Clock for MPU



Specifications (characteristics)

Item	Symbol	Specifications		Conditions / Remarks
		For RF Reference	For Clock	
Nominal frequency range	f_nom	19.2 MHz to 54 MHz		Fundamental Please contact us about available frequencies.
Storage temperature range	T_stg	-40 °C to +125 °C		Storage as single product.
Operating temperature range	T_use	-40 °C to +85 °C (+105 °C)		Please contact us about +85 °C < T_use
Level of drive	DL	100 μW Max.	200 μW Max.	Recommended: 10 μW
Frequency tolerance (standard)	f_tol	±10 × 10 ⁻⁶ *1	±30 × 10 ⁻⁶	+25 °C, please contact us for requirements not listed in this specification.
Frequency versus temperature characteristics. (standard)	f_tem	±10 × 10 ⁻⁶ *1	±30 × 10 ⁻⁶	-20 °C to +75 °C, please contact us for requirements not listed in this specification.
Load capacitance	CL	6 pF to ∞		Please specify.
Motional resistance (ESR)	R1	As per below Table 1.		-20 °C to +75 °C
Frequency aging	f_age	±1 × 10 ⁻⁶ / year Max.	±5 × 10 ⁻⁶ / year Max.	+25 °C, First year

*1 Please contact us for available frequency tolerances as they are dependent upon the nominal frequency.

Table 1. Motional resistance (ESR) R1

Frequency	Motional resistance
19.2 MHz ≤ f_nom < 20 MHz	150 Ω Max.
20 MHz ≤ f_nom < 24 MHz	100 Ω Max.
24 MHz ≤ f_nom < 26 MHz	80 Ω Max.
26 MHz ≤ f_nom ≤ 54 MHz	60 Ω Max.

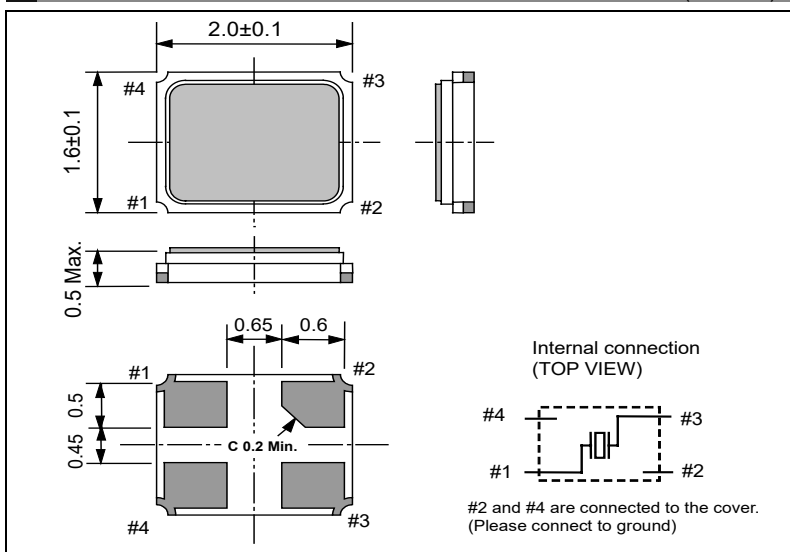
Product name

Product name FA-128 24.000000MHz 12.0 +10.0-10.0
(Standard form) ① ② ③ ④①Model ②Frequency ③Load capacitance(pF) ④Frequency tolerance(× 10⁻⁶, +25 °C)

In addition to the above mentioned specification item, please specify frequency temperature characteristics and operating temperature range in case of inquiry.

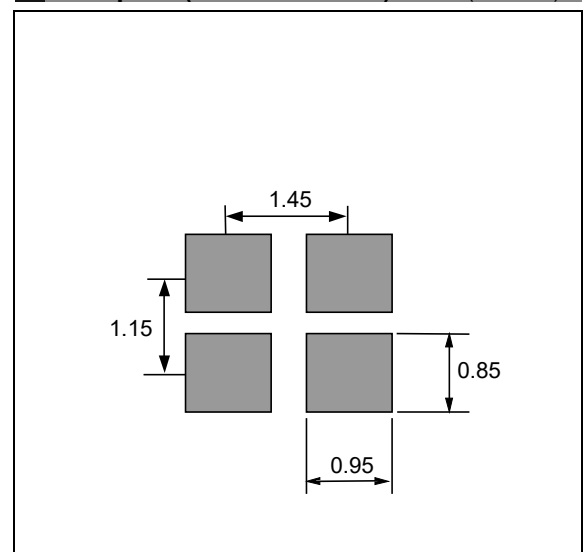
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



MHz RANGE CRYSTAL UNIT

FA-20H



Product Number
Q24FA20H0xxxx18

- Nominal frequency range : 12 MHz to 54 MHz
- External dimensions : 2.5× 2.0×0.55 mm
- Overtone order : Fundamental
- Applications : Mobile phone, Bluetooth, W-LAN
ISM band radio, Clock for MPU



Specifications (characteristics)

Item	Symbol	Specifications		Conditions / Remarks
		For RF Reference	For Clock	
Nominal frequency range	f _{nom}	12 MHz to 54 MHz		Fundamental Please contact us about available frequencies.
Storage temperature range	T _{stg}	-40 °C to +125 °C		Storage as single product.
Operating temperature range	T _{use}	-40 °C to +85 °C (+105 °C)		Please contact us about +85 °C < T _{use}
Level of drive	DL	100 μW Max.	200 μW Max.	Recommended: 10 μW
Frequency tolerance	f _{tol}	±10 × 10 ⁻⁶	±30 × 10 ⁻⁶	+25 °C Please contact us for inquiries.
Frequency versus temperature characteristics	f _{tem}	±10 × 10 ⁻⁶	±30 × 10 ⁻⁶	-20 °C to +75 °C Please contact us for inquiries.
Load capacitance	CL	6 pF to ∞		Please specify.
Motional resistance (ESR)	R1	As per below table1.		-20 °C to +75 °C
Frequency aging	f _{age}	±1 × 10 ⁻⁶ to ±3 × 10 ⁻⁶ / year Max. *1		+25 °C, First year

*1 Please contact us for available frequency tolerances as they are dependent upon the nominal frequency.

Table 1. Motional resistance (ESR) R1

Frequency	Motional resistance
12 MHz ≤ f _{nom} < 16 MHz	150 Ω Max.
16 MHz ≤ f _{nom} ≤ 25 MHz	80 Ω Max.
25 MHz < f _{nom} ≤ 30 MHz	60 Ω Max.
30 MHz < f _{nom} ≤ 35 MHz	50 Ω Max.
35 MHz < f _{nom} ≤ 54 MHz	40 Ω Max.

Product name

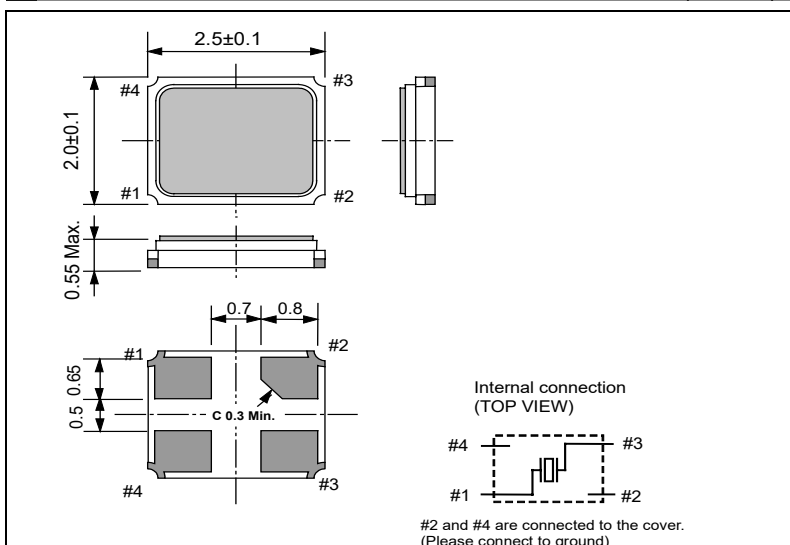
Product name FA-20H 24.000000MHz 12.0 +10.0-10.0
(Standard form) ① ② ③ ④

①Model ②Frequency ③Load capacitance(pF) ④Frequency tolerance(× 10⁻⁶, +25 °C)

In addition to the above mentioned specification item, please specify frequency temperature characteristics and operating temperature range in case of inquiry.

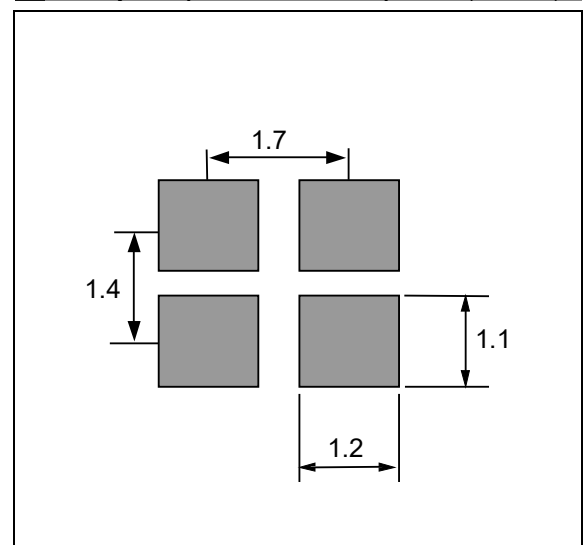
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



MHz RANGE CRYSTAL UNIT

FA-238V

FA-238

TSX-3225



Product Number
 FA-238V : Q22FA23V0xxxx17
 FA-238 : Q22FA2380xxxx17
 TSX-3225 : X1E000021xxxx17



FA-238V/FA-238



TSX-3225

- Frequency range : 12 MHz to 50 MHz (FA-238 / FA-238V)
- External dimensions : 3.2 × 2.5 × 0.6 mm ...TSX-3225
: 3.2 × 2.5 × 0.7 mm ...FA-238V / FA-238
- Overtone order : Fundamental
- Applications : Mobile phone, Bluetooth, W-LAN
ISM band radio, Clock for MPU

Specifications (characteristics)

Item	Symbol	For Clock		For RF Reference	Conditions / Remarks
		FA-238V	FA-238	TSX-3225	
Nominal frequency range	f_nom	12 MHz to 15.999 MHz	16 MHz to 50 MHz	16 MHz to 48 MHz	Fundamental *1 Please contact us about available frequencies.
Storage temperature range	T_stg	-40 °C to +125 °C			Storage as single product.
Operating temperature range	T_use	-40 °C to +85 °C (+105 °C)			Please contact us about +85 °C < T_use
Level of drive	DL	200 μW Max.			Recommended: 10 μW
Frequency tolerance	f_tol	±50 × 10 ⁻⁶ (standard), (±15 × 10 ⁻⁶ to ±50 × 10 ⁻⁶ is available)		±10 × 10 ⁻⁶	+25 °C Please contact us for requirements not listed in this specifications. *1
Frequency versus temperature characteristics	f_tem	±30 × 10 ⁻⁶ /20 °C to +70 °C		±10 × 10 ⁻⁶ /20 °C to +75 °C	Please contact us for requirements not listed in this specifications. *1
Load capacitance	CL	7 pF to ∞			Please specify.
Motional resistance (ESR)	R1	As per below table1.		As per below table2.	-40 °C to +85 °C, DL = 100 μW
Frequency aging	f_age	±5 × 10 ⁻⁶ / year Max.		±1 × 10 ⁻⁶ / year Max.*2	+25 °C, First year

*1 FA-238: For over 40 MHz, only the standard specification applies. *2 40 MHz ≤ f_nom : ±2 × 10⁻⁶ / year Max.

Table 1. FA-238V / FA-238 Motional resistance (ESR) R1

(FA-238V / FA-238) Frequency	Motional resistance
12 MHz ≤ f_nom ≤ 13 MHz	100 Ω Max.
13 MHz < f_nom < 20 MHz	80 Ω Max.
20 MHz ≤ f_nom < 25 MHz	60 Ω Max.
25 MHz ≤ f_nom < 30 MHz	50 Ω Max.
30 MHz ≤ f_nom ≤ 50 MHz	40 Ω Max.

Table 2. TSX-3225 Motional resistance (ESR) R1

(TSX-3225) Frequency	Motional resistance
16 MHz ≤ f_nom < 21 MHz	60 Ω Max.
21 MHz ≤ f_nom ≤ 48 MHz	40 Ω Max.

Product name

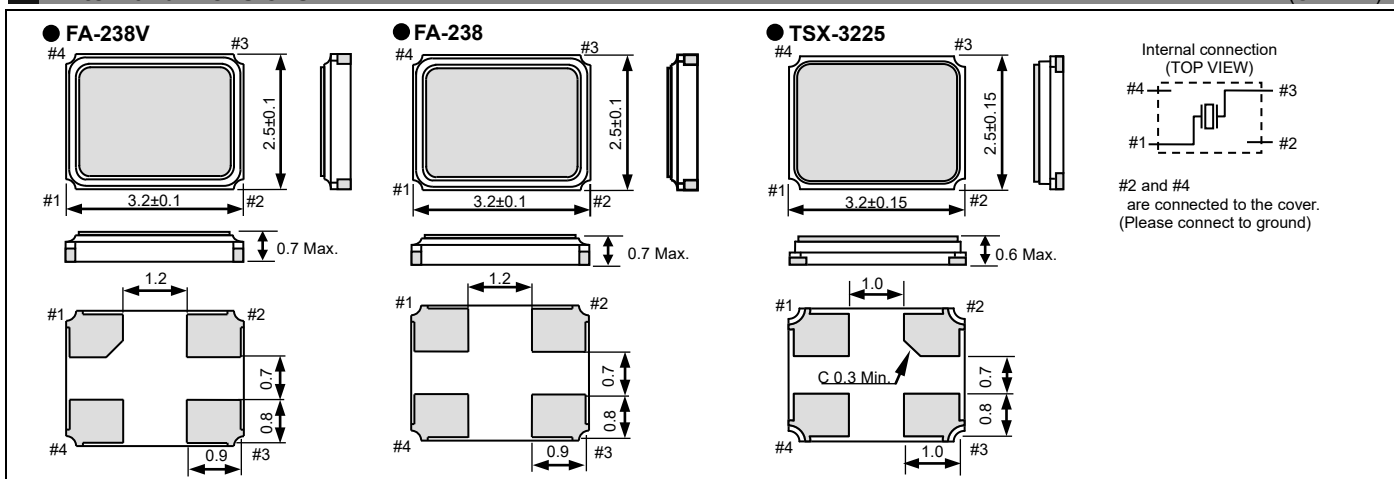
Product name FA-238V 12.000000MHz 12.0 +15.0-15.0
 (Standard form) ① ② ③ ④

① Model ② Frequency ③ Load capacitance(pF) ④ Frequency tolerance(× 10⁻⁶, +25 °C)

In addition to the above mentioned specification item, please specify frequency temperature characteristics and operating temperature range in case of inquiry.

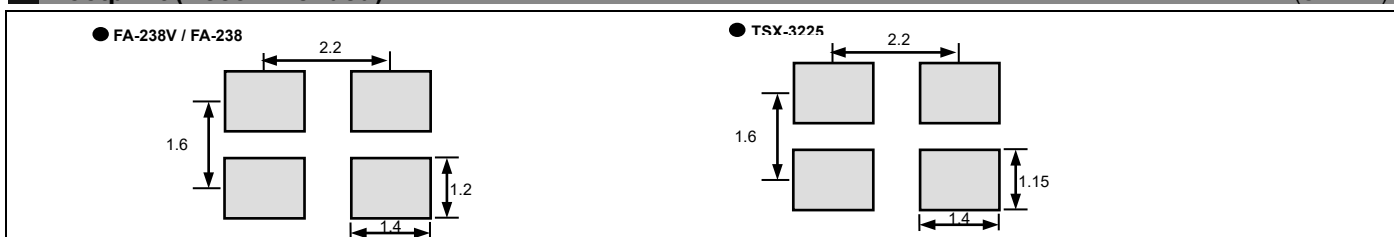
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



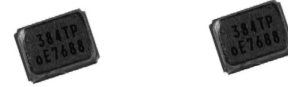
MHz RANGE CRYSTAL UNIT

Built-in thermistor


 Product Number
 FA1612AS: X1E000401xxxx18

FA1612AS

- Nominal frequency range : 38.4 MHz, 52MHz
- External dimensions : 1.6 × 1.2 × 0.65 mm
- Overtone order : Fundamental
- Applications : Mobile phone, Bluetooth, W-LAN
ISM band radio, Clock for MPU
GPS, Smart phone



Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
Nominal frequency range	f_nom	38.4 MHz, 52 MHz	Fundamental Please contact us about available frequencies.
Storage temperature range	T_stg	-40 °C to +125 °C	Storage as single product.
Operating temperature range	T_use	-40 °C to +85 °C	
Level of drive	DL	100 μW Max.	Recommended: 10 μW
Frequency tolerance	f_tol	±10 × 10 ⁻⁶ *1	Please contact us about reference temperature. Please contact us for inquiries.
Frequency versus temperature characteristics	f_tem	±12 × 10 ⁻⁶ *1	-30 °C to +85 °C Please contact us for inquiries.
Load capacitance	CL	6 pF to ∞	Please specify.
Motional resistance (ESR)	R1	50 Ω Max. (38.4 MHz) 30 Ω Max. (52 MHz)	-30 °C to +85 °C
Frequency aging	f_age	±1 × 10 ⁻⁶ / year Max.	+25 °C, First year
Thermistor resistance	—	Please contact us about specifications.	
Thermistor B constant	—		

*1 Please contact us for available frequency tolerances as they are dependent upon the nominal frequency.

Product name

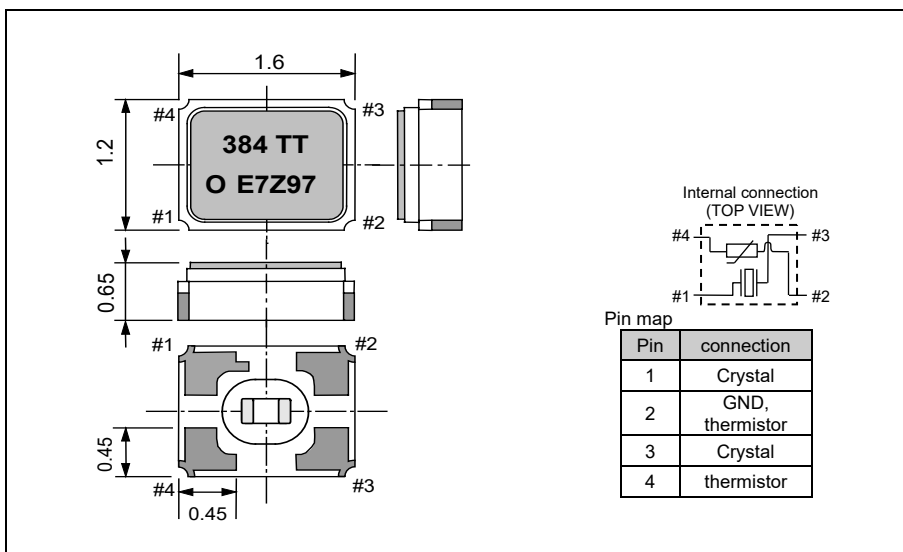
 Product name FA1612AS 38.400000MHz 7.0 +10.0-10.0
 (Standard form) ① ② ③ ④

 ①Model ②Frequency ③Load capacitance(pF) ④Frequency tolerance(× 10⁻⁶, +25 °C)

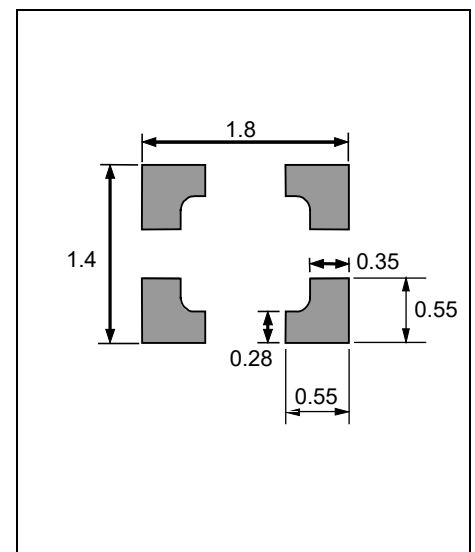
In addition to the above mentioned specification item, please specify frequency temperature characteristics and operating temperature range in case of inquiry.

External dimensions

(Unit:mm)



Footprint (Recommended) (Unit:mm)





MHz RANGE CRYSTAL UNIT

Built-in thermistor



Product Number
FA2016AS: X1E000371xxxx18

FA2016AS

- Nominal frequency range : 38.4 MHz
- External dimensions : 2.0 × 1.6 × 0.65 mm
- Overtone order : Fundamental
- Applications : Mobile phone, Bluetooth, W-LAN
ISM band radio, Clock for MPU
GPS, Smart phone



Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
Nominal frequency range	f_nom	38.4 MHz	Fundamental Please contact us about available frequencies.
Storage temperature range	T_stg	-40 °C to +125 °C	Storage as single product.
Operating temperature range	T_use	-40 °C to +85 °C	
Level of drive	DL	100 μW Max.	Recommended: 10 μW
Frequency tolerance	f_tol	±10 × 10 ⁻⁶ *1	Please contact us about reference temperature. Please contact us for inquiries.
Frequency versus temperature characteristics	f_tem	±12 × 10 ⁻⁶ *1	-30 °C to +85 °C Please contact us for inquiries.
Load capacitance	CL	6 pF to ∞	Please specify.
Motional resistance (ESR)	R1	50 Ω Max.	-30 °C to +85 °C
Frequency aging	f_age	±1 × 10 ⁻⁶ / year Max.	+25 °C, First year
Thermistor resistance	—	Please contact us about specifications.	
Thermistor B constant	—		

*1 Please contact us for available frequency tolerances as they are dependent upon the nominal frequency.

Product name

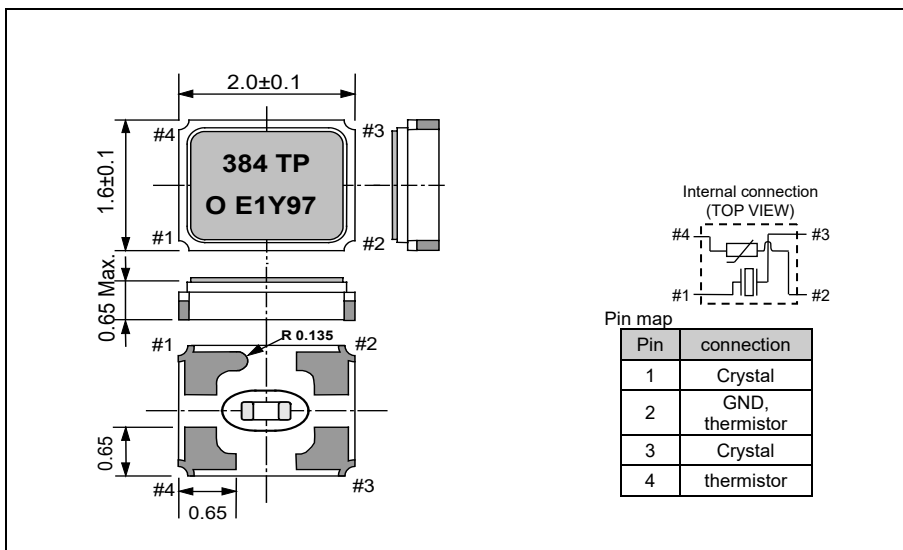
Product name (Standard form) **FA2016AS** 38.400000MHz 7.0 +10.0-10.0

① Model ② Frequency ③ Load capacitance(pF) ④ Frequency tolerance(× 10⁻⁶, +25 °C)

In addition to the above mentioned specification item, please specify frequency temperature characteristics and operating temperature range in case of inquiry.

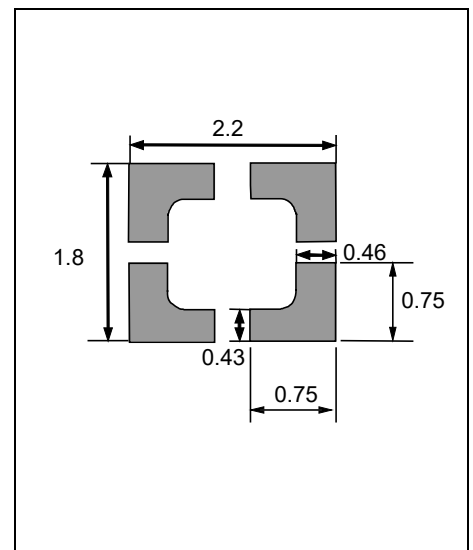
External dimensions

(Unit:mm)

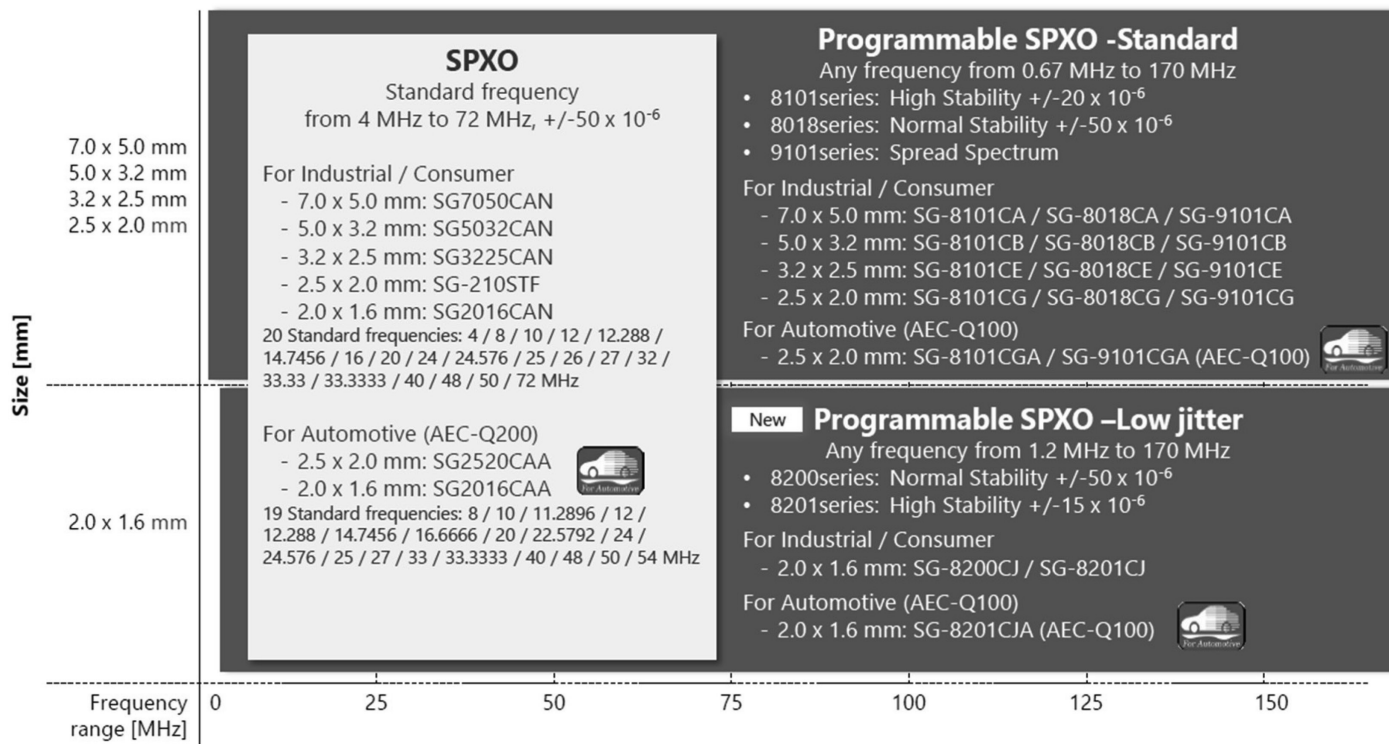


Footprint (Recommended)




(Unit:mm)







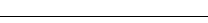

Crystal Oscillator (SPXO / SPSO) CMOS Output





► Fixed-Frequency SPXO

P	Model	Size [mm]	Nominal frequency range	Frequency Tolerance [$\times 10^{-6}$]	Operating temperature [°C]	Supply voltage [V]	Current consumption Max. [mA]	Output load condition [pF]	Output control
53	SG2016CAN SG-210STF SG3225CAN SG5032CAN SG7050CAN	2.0 x 1.6 x 0.7t 2.5 x 2.0 x 0.8t 3.2 x 2.5 x 1.05t 5.0 x 3.2 x 1.1t 7.0 x 5.0 x 1.3t	 4 MHz 72 MHz 20 standard frequencies	± 25 ± 50 $\pm 50, \pm 100$	-20 to +70 -40 to +85 -40 to +105	1.6 to 2.2	2.4	15	$\overline{\text{ST}}$
						2.2 to 2.7	2.8		
						2.7 to 3.6	3.0		
Web Site	SG5032CCN SG7050CCN	5.0 x 3.2 x 1.1t 7.0 x 5.0 x 1.3t	 2.5 MHz 50 MHz	± 50	-40 to +85	4.5 to 5.5	20.0	50	OE
124	SG2016CAA (AEC-Q200) SG2520CAA (AEC-Q200)	2.0 x 1.6 x 0.7t 2.5 x 2.0 x 0.8t	 8 MHz 54 MHz 19 standard frequencies	$\pm 50, \pm 100$ $\pm 50, \pm 100$ $\pm 100, \pm 150$	-40 to +85 -40 to +105 -40 to +125	1.6 to 2.2	2.9	15	$\overline{\text{ST}}$
						2.2 to 2.7	3.3		
						2.7 to 3.6	3.5		

► Programmable SPXO

P	Model	Size [mm]	Nominal frequency range	Frequency Tolerance [$\times 10^{-6}$]	Operating temperature [°C]	Supply voltage [V]	Current consumption Max. [mA]	Output load condition [pF]	Output control
55	SG-8200CJ SG-8200CG	2.0 x 1.6 x 0.6t 2.5 x 2.0 x 0.74t	1.2 MHz  170 MHz	±50	-40 to +125	1.62 to 1.98	10.4	15	OE or ST
						2.25 to 2.75	12.4		
						2.97 to 3.63	15.0		
57	SG-8201CJ SG-8201CG	2.0 x 1.6 x 0.6t 2.5 x 2.0 x 0.74t	1.2 MHz  170 MHz	±15 ±25	-40 to +105 -40 to +125	1.62 to 1.98	10.4	15	OE or ST
						2.25 to 2.75	12.4		
						2.97 to 3.63	15.0		
125	SG-8201CJA (AEC-Q100)	2.0 x 1.6 x 0.6t	1.2 MHz  170 MHz	±15 ±25 / ±50	-40 to +105 -40 to +125	1.62 to 1.98	10.4	15	OE or ST
						2.25 to 2.75	12.4		
						2.97 to 3.63	15.0		
59	SG-8018CG SG-8018CE SG-8018CB SG-8018CA	2.5 x 2.0 x 0.7t 3.2 x 2.5 x 1.05t 5.0 x 3.2 x 1.1t 7.0 x 5.0 x 1.3t	0.67 MHz  170 MHz	±50	-40 to +105	1.62 to 1.98	5.5	15	OE or ST
						1.98 to 2.2	5.8		
						2.2 to 2.8	6.7		
						2.7 to 3.63	8.1		
61	SG-8101CG SG-8101CE SG-8101CB SG-8101CA	2.5 x 2.0 x 0.7t 3.2 x 2.5 x 1.05t 5.0 x 3.2 x 1.1t 7.0 x 5.0 x 1.3t	0.67 MHz  170 MHz	±15 ±20 / ±50	-40 to +85 -40 to +105	1.62 to 1.98	5.5	15	OE or ST
						1.98 to 2.2	5.8		
						2.20 to 2.80	6.7		
						2.70 to 3.63	8.1		
127	SG-8101CGA (AEC-Q100)	2.5 x 2.0 x 0.7t	0.67 MHz  170 MHz	±15 ±20 ±50 / ±100	-40 to +85 -40 to +105 -40 to +125	1.62 to 1.98	5.5	15	OE or ST
						1.98 to 2.2	5.8		
						2.20 to 2.80	6.7		
						2.70 to 3.63	8.1		

► Programmable SPXO, Spread Spectrum

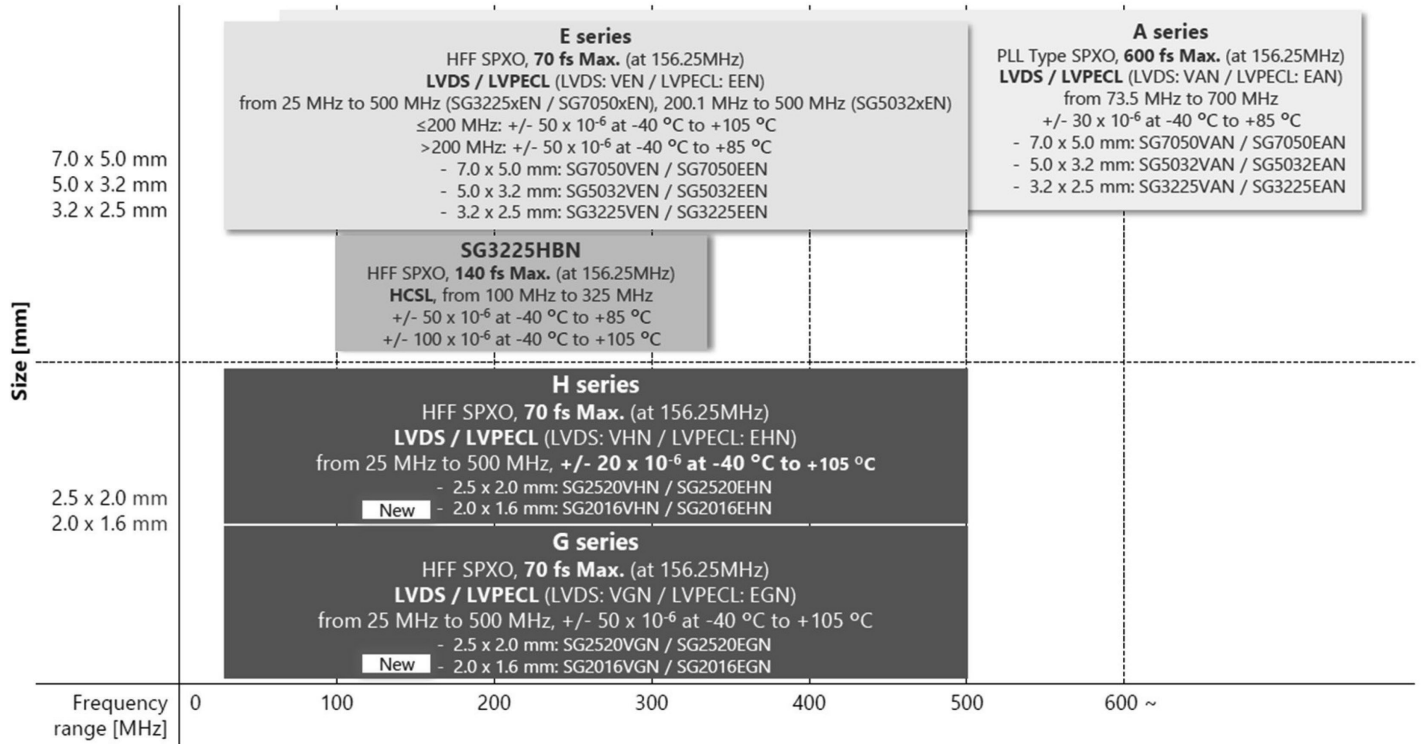
P	Model	Size [mm]	Nominal frequency range	Frequency Tolerance [$\times 10^{-6}$]	Operating temperature [°C]	Supply voltage [V]	Current consumption Max. [mA]	Output load condition [pF]	Output control
63	SG-9101CG SG-9101CE SG-9101CB SG-9101CA	2.5 x 2.0 x 0.7t 3.2 x 2.5 x 1.05t 5.0 x 3.2 x 1.1t 7.0 x 5.0 x 1.3t	0.67 MHz  170 MHz	±50	-40 to +85 -40 to +105	1.62 to 1.98	5.7	15	OE or ST
						1.98 to 2.20	6.0		
						2.20 to 2.80	6.9		
						2.70 to 3.63	8.3		
129	SG-9101CGA (AEC-Q100)	2.5 x 2.0 x 0.7t	0.67 MHz  170 MHz	±100	-40 to +125	1.62 to 1.98	5.8	15	OE or ST
						1.98 to 2.20	6.1		
						2.20 to 2.80	7.0		
						2.70 to 3.63	8.4		

Spread Spectrum Configuration Center [%]	±0.25	±0.5	±0.75	±1.0	±1.5	±2.0
Spread Spectrum Configuration Down [%]	-0.5	-1.0	-1.5	-2.0	-3.0	-4.0

► Programming tool for Programmable SPXO

P	Model
65	SG-8000 series Programming Tool (SG-Writer II)

Crystal Oscillator (SPXO / SPSO) Differential Output



► SPXO

P	Model	Size [mm]	Nominal frequency range	Frequency Tolerance [x 10 ⁻⁶]	Operating temperature [°C]	Supply voltage [V]	Current consumption Max. [mA]	Output load condition [Ω]	Phase Jitter Max. (Bandwidth: 12 kHz to 20 MHz) [fs]
66	SG2016EHN SG2520EHN	LV-PECL	25 MHz — 500 MHz	±20	-40 to +85 -40 to +105	2.5 ± 0.125	60	50	90 (fo = 100.00 MHz)
	SG2016VHN SG2520VHN	LVDS				1.8 ± 0.090			25
xx	SG2016HHN SG2520HHN	HCSL				2.5 ± 0.125	40	50	60 (fo = 212.50 MHz)
67	SG2016EGN SG2520EGN	LV-PECL	25 MHz — 500 MHz	±25 ±50	-40 to +85 -40 to +105	2.5 ± 0.125	60	50	50 (fo = 491.52 MHz)
	SG2016VGN SG2520VGN	LVDS				1.8 ± 0.090			25
xx	SG2016HGN SG2520HGN	HCSL				2.5 ± 0.125	40	50	60 (fo = 212.50 MHz)
68	SG3225EEN SG5032EEN SG7050EEN	LV-PECL	25 MHz — 500 MHz (SG3225xEN, SG7050xEN)	±25 ±50 ±100	-40 to +85 -40 to +105	2.5 ± 0.125	60	50	100 (fo = 100.00 MHz)
	SG3225VEN SG5032VEN SG7050VEN	LVDS	200.1 MHz — 500 MHz (SG5032xEN)			3.3 ± 0.165			25
69	SG3225EAN SG5032EAN SG7050EAN	LV-PECL	73.5 MHz — 700 MHz	±20 ±30 ±50	-20 to +70 -40 to +85	2.375 to 3.63	65	50	600 (none of the following fo)
	SG3225VAN SG5032VAN SG7050VAN	LVDS							
70	SG3225HBN	HCSL	100 MHz — 325 MHz	±50 ±100	-40 to +85 -40 to +105	3.3 ± 0.33	35	50	180 (fo = 100.00 MHz)

► SPSO

P	Model	Size [mm]	Nominal frequency range	Frequency Tolerance [$\times 10^{-6}$]	Operating temperature [°C]	Supply voltage [V]	Current consumption Max. [mA]	Output load condition [Ω]	Phase Jitter Max. (Bandwidth: 12 kHz to 20 MHz) [fs]		
Web Site	EG-2121CB	LV-PECL	100 MHz 700 MHz	± 50 ± 100	0 to +70 -5 to +85 -20 to +70	2.5 \pm 0.125	60	50	LV-PECL / LVDS 230 / 270 (fo = 100 MHz to 150 MHz) 220 / 240 (fo = 150 MHz to 200 MHz) 210 / 230 (fo = 200 MHz to 300 MHz) 180 / 190 (fo = 300 MHz to 400 MHz) 160 / 160 (fo = 400 MHz to 500 MHz) 140 / 140 (fo = 500 MHz to 600 MHz) 100 / 100 (fo = 600 MHz to 700 MHz)		
										230 / 270 (fo = 100 MHz to 150 MHz) 220 / 240 (fo = 150 MHz to 200 MHz) 210 / 230 (fo = 200 MHz to 300 MHz) 180 / 190 (fo = 300 MHz to 400 MHz) 160 / 160 (fo = 400 MHz to 500 MHz) 140 / 140 (fo = 500 MHz to 600 MHz) 100 / 100 (fo = 600 MHz to 700 MHz)	
	EG-2102CB	LVDS	100 MHz 700 MHz	± 50 ± 100	0 to +70 -5 to +85 -20 to +70	3.3 \pm 0.33	30	100			
											EG-2102CB
	XG5032HAN	HCSL	100 MHz 200 MHz	± 50 ± 100	0 to +70 -5 to +85 -20 to +70	3.3 \pm 0.33	30	100			
											XG-2121CA
	XG-2102CA	LVDS	100 MHz 700 MHz	± 50 ± 100	0 to +70 -5 to +85 -20 to +70	3.3 \pm 0.33	60	50			
											XG-2103CA
	XG-2103CA	LVDS	100 MHz 700 MHz	± 50 ± 100	0 to +70 -5 to +85 -20 to +70	3.3 \pm 0.33	30	100			
											MG7050EAN (Multi output)
	MG7050VAN (Multi output)	LVDS	7.0 \times 5.0 \times 1.6t	100 MHz 700 MHz	± 50 ± 100	0 to +70 -5 to +85 -20 to +70	3.3 \pm 0.33	102 (2out) 184 (4out)			
											MG7050HAN (Multi output)
MG7050HAN (Multi output)	HCSL	7.0 \times 5.0 \times 1.6t	100 MHz 200 MHz	± 50 ± 100	0 to +70 -5 to +85 -20 to +70	3.3 \pm 0.33	72 (4out) 84 (2out)	42.2			
									MG7050HAN (Multi output)	HCSL	7.0 \times 5.0 \times 1.6t
MG7050HAN (Multi output)	HCSL	7.0 \times 5.0 \times 1.6t	100 MHz 200 MHz	± 50 ± 100	0 to +70 -5 to +85 -20 to +70	3.3 \pm 0.33	136 (4out)	42.2			

► Programmable SPXO (Output: LV-PECL)

P	Model	Size [mm]	Nominal frequency range	Frequency Tolerance [$\times 10^{-6}$]	Operating temperature [°C]	Supply voltage [V]	Current consumption Max. [mA]	Output load condition [Ω]	Phase Jitter Max. (Bandwidth: 12 kHz to 20 MHz) [fs]
71	SG-8506CA (I2C-Bus, Programmable, 8 pin)	7.0 \times 5.0 \times 1.5t	50 MHz 800 MHz	± 31.5 ± 50	-40 to +85	2.5 \pm 0.125	90	50	300
73	SG-8503CA (Dual Selectable, 6 pin)					3.3 \pm 0.33			
						2.5 \pm 0.125			
						3.3 \pm 0.33			
	SG-8504CA (Quad Selectable, 8 pin)	2.5 \pm 0.125		3.3 \pm 0.33					

CRYSTAL OSCILLATOR (SPXO)

OUTPUT : CMOS



Product Number (please contact us)
 SG2016CAN: X1G004801xxxx00
 SG-210STF: X1G004171xxxx00
 SG3225CAN: X1G005961xxxx15
 SG5032CAN: X1G004451xxxx00
 SG7050CAN: X1G004481xxxx00

SG2016 / 3225 / 5032 / 7050CAN
 SG-210STF

- Frequency : 20 standard frequencies
- Supply voltage : 1.8 V to 3.3 V Typ.
- Function : Standby(\overline{ST})
- Operating temperature : -40 °C to +105 °C



Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks																								
Output frequency	f_o	4 MHz 8 MHz 10 MHz 12 MHz 12.288 MHz 14.7456 MHz 16 MHz 20 MHz 24 MHz 24.576 MHz 25 MHz 26 MHz 27 MHz 32 MHz 33.33 MHz 33.3333 MHz 40 MHz 48 MHz 50 MHz 72 MHz																									
Supply voltage	V_{CC}	1.60 V to 3.63 V 1.71 V to 3.63 V 2.25 V to 3.63 V	4 MHz $\leq f_o \leq$ 50 MHz, $T_{use} = +105^\circ\text{C}$ Max. fo = 72 MHz, $T_{use} = +85^\circ\text{C}$ Max. fo = 72 MHz, $T_{use} = +105^\circ\text{C}$ Max. Refer to Figure 1																								
Storage temperature	T_{stg}	-55 °C to +125 °C -40 °C to +125 °C	SG2016CAN, SG3225CAN All others																								
Operating temperature	T_{use}	-20 °C to +70 °C, -40 °C to +85 °C, -40 °C to +105 °C	See of figure *1																								
Frequency tolerance	f_{tol}	$\pm 25 \times 10^{-6}$ $\pm 50 \times 10^{-6}$	-20 °C to +70 °C -40 °C to +85 °C, -40 °C to +105 °C																								
Current consumption	I_{CC}	$V_{CC} = 1.8\text{ V} \pm 10\%$ $V_{CC} = 2.5\text{ V} \pm 10\%$ $V_{CC} = 3.3\text{ V} \pm 10\%$ 1.5 mA Max. 1.6 mA Max. 1.8 mA Max. 1.8 mA Max. 2.0 mA Max. 2.2 mA Max. 2.1 mA Max. 2.4 mA Max. 2.6 mA Max. 2.4 mA Max. 2.8 mA Max. 3.0 mA Max.	No load condition, 4 MHz $\leq f_o \leq$ 20 MHz No load condition, 20 MHz $< f_o \leq$ 40 MHz No load condition, 40 MHz $< f_o \leq$ 50 MHz No load condition, fo = 72 MHz																								
Stand-by current	I_{std}	2.1 μA Max. 2.5 μA Max. 2.7 μA Max.	$\overline{ST} = \text{GND}$																								
Symmetry	SYM	45 % to 55 %	50 % V_{CC} level, $L_{CMOS} \leq 15\text{ pF}$																								
Output voltage	V_{OH} V_{OL} V_{OH-2} V_{OL-2}	90 % V_{CC} Min. 10 % V_{CC} Max. $V_{CC} - 0.4\text{ V}$ Min. 0.4 V Max.	<table border="1"> <tr> <td></td> <td>1.8 V $\pm 10\%$</td> <td>2.5 V $\pm 10\%$</td> <td>3.3 V $\pm 10\%$</td> </tr> <tr> <td>I_{OH}</td> <td>-1.5 mA</td> <td>-3 mA</td> <td>-4 mA</td> </tr> <tr> <td>I_{OL}</td> <td>1.5 mA</td> <td>3 mA</td> <td>4 mA</td> </tr> </table> <table border="1"> <tr> <td></td> <td>1.8 V $\pm 10\%$</td> <td>2.5 V $\pm 10\%$</td> <td>3.3 V $\pm 10\%$</td> </tr> <tr> <td>I_{OH}</td> <td>-3 mA</td> <td>-4 mA</td> <td>-6 mA</td> </tr> <tr> <td>I_{OL}</td> <td>3 mA</td> <td>4 mA</td> <td>6 mA</td> </tr> </table>		1.8 V $\pm 10\%$	2.5 V $\pm 10\%$	3.3 V $\pm 10\%$	I_{OH}	-1.5 mA	-3 mA	-4 mA	I_{OL}	1.5 mA	3 mA	4 mA		1.8 V $\pm 10\%$	2.5 V $\pm 10\%$	3.3 V $\pm 10\%$	I_{OH}	-3 mA	-4 mA	-6 mA	I_{OL}	3 mA	4 mA	6 mA
	1.8 V $\pm 10\%$	2.5 V $\pm 10\%$	3.3 V $\pm 10\%$																								
I_{OH}	-1.5 mA	-3 mA	-4 mA																								
I_{OL}	1.5 mA	3 mA	4 mA																								
	1.8 V $\pm 10\%$	2.5 V $\pm 10\%$	3.3 V $\pm 10\%$																								
I_{OH}	-3 mA	-4 mA	-6 mA																								
I_{OL}	3 mA	4 mA	6 mA																								
Output load condition (CMOS)	L_{CMOS}	15 pF Max.																									
Input voltage	V_{IH} V_{IL}	80 % V_{CC} Min. 20 % V_{CC} Max.	\overline{ST} terminal																								
Rise time and Fall time	t_r / t_f	3 ns Max. 3.5 ns Max. (@1.8 V $\pm 10\%$)	20 % V_{CC} to 80 % V_{CC} level, $L_{CMOS} = 15\text{ pF}$																								
Start-up time	t_{str}	3 ms Max.	$T = 0$ at 90 % V_{CC}																								
Frequency aging	f_{age}	$\pm 3 \times 10^{-6}$ / year Max.	+25 °C, First year																								

[Model : SG2016 / 3225 / 5032 / 7050CAN]

Product name SG2016CAN25.000000MHzTJHA

(Standard form) ① ② ③ ④⑤⑥⑦

- ① Model ② Output(C: CMOS) ③ Frequency ④ Supply voltage
 ⑤ Frequency tolerance ⑥ Operating temperature range
 ⑦ Internal identification code("A" is default)

④ Supply voltage *See Figure 1		⑤ Frequency tolerance / ⑥ Operating temperature range	
T	1.8 V to 3.3 V Typ.	DB*	$\pm 25 \times 10^{-6}$ / -20 °C to +70 °C
K	2.5 V to 3.3 V Typ.	JG	$\pm 50 \times 10^{-6}$ / -40 °C to +85 °C
		JH	$\pm 50 \times 10^{-6}$ / -40 °C to +105 °C

* Please refer to Product number list on Full Data Sheet for available frequencies

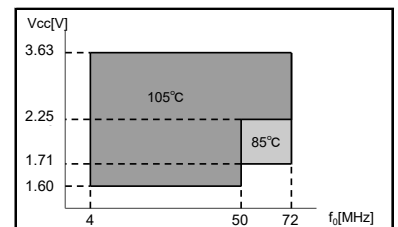


Figure 1 : The upper limit of Operating temperature and the related conditions

Please note that Supply voltage range (V_{CC}) depends on Output frequency (f_o) and upper limit of Operating temperature (T_{use} Max.).

[Model : SG-210STF]

Product name SG-210STF25.000000MHzY

(Standard form) ① ②③ ④ ⑤

- ① Model ② Function(S: Standby) ③ Supply voltage
 ④ Frequency ⑤ Frequency tolerance

③ Supply voltage *See Figure 1		⑤ Frequency tolerance	
T	1.8 V to 3.3 V Typ.	S*	$\pm 25 \times 10^{-6}$ / -20 °C to +70 °C
		L	$\pm 50 \times 10^{-6}$ / -40 °C to +85 °C
		Y	$\pm 50 \times 10^{-6}$ / -40 °C to +105 °C

* Please refer to Product number list on Full Data Sheet for available frequencies



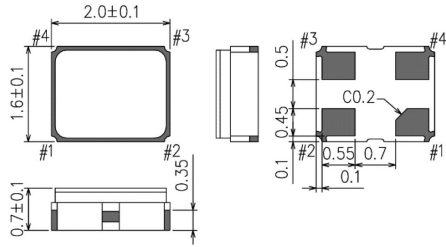
External dimensions

(Unit:mm)

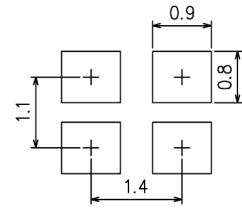
Footprint (Recommended)

(Unit:mm)

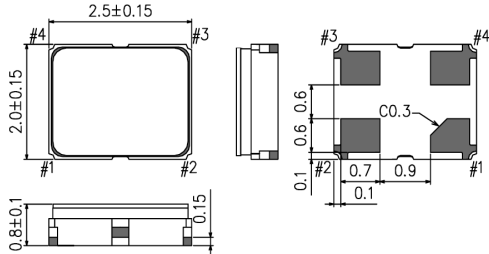
SG2016CAN



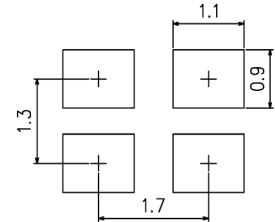
SG2016CAN



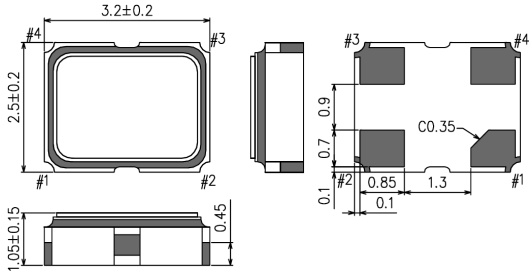
SG-210STF



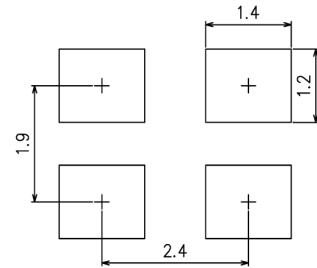
SG-210STF



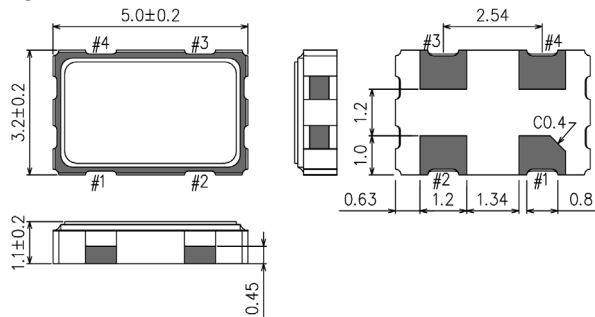
SG3225CAN



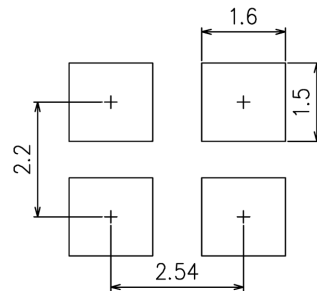
SG3225CAN



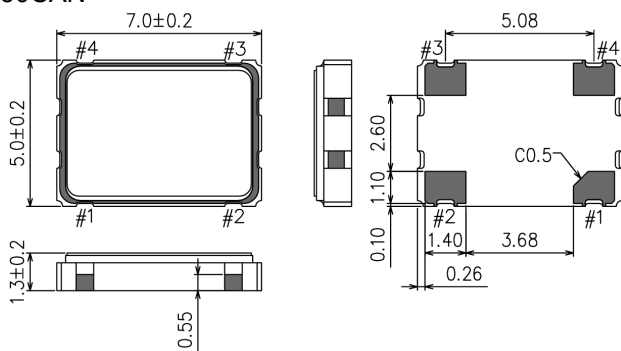
SG5032CAN



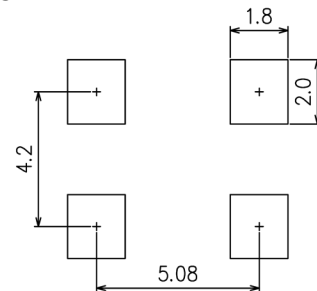
SG5032CAN



SG7050CAN



SG7050CAN



Pin Map

Pin	Connection	Function		
		ST terminal	Oscillator circuit	Output
1	ST	ST function	Oscillation	Specified frequency: Enable
		HIGH or "open"	Oscillation stop	High impedance: Disable
		LOW		
2	GND	Ground		
3	OUT	Clock output		
4	V _{CC}	Power supply		

■Notes: To maintain stable operation, provide a 0.01uF to 0.1uF by-pass capacitor at a location as near as possible to the power source terminal of the crystal product (between V_{CC} - GND).



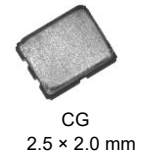
CRYSTAL OSCILLATOR (Programmable)
OUTPUT: CMOS

SG-8200 series

- Frequency range : 1.2 MHz to 170 MHz
- Supply voltage : 1.62 V to 3.63 V
- Function : Output enable (OE/ \overline{OE}) or Standby (\overline{ST} / \overline{ST})
- Frequency tolerance, operating temperature:
±50 × 10⁻⁶ (-40 °C to +125 °C)
- PLL technology to enable setting any output frequency



Product Number
SG-8200CJ: X1G006211xxxx16
SG-8200CG: X1G006201xxxx16



Specifications (characteristics)

Item	Symbol	Specifications			Conditions/Remarks				
		1.80 V Typ. 1.62 V to 1.98 V	2.50 V Typ. 2.25 V to 2.75 V	3.30 V Typ. 2.97 V to 3.63 V					
Supply voltage	V _{CC}								
Output frequency range	f _o	1.2 MHz to 170 MHz							
Storage temperature range	T _{stg}	-55 °C to +125 °C			Storage as single product.				
Operating temperature range	T _{use}	J: -40 °C to +125 °C							
Frequency tolerance ^{*1}	f _{tol}	J: ±50 × 10 ⁻⁶			T _{use} = -40 °C to +125 °C				
Current consumption	I _{CC}	5.2 mA Typ. 7.0 mA Max.	5.4 mA Typ. 7.2 mA Max.	5.6 mA Typ. 7.5 mA Max.	1.2 MHz ≤ f _o ≤ 25 MHz	No load, Rise/Fall time: Default			
		5.4 mA Typ. 7.3 mA Max.	5.7 mA Typ. 7.6 mA Max.	6.1 mA Typ. 8.1 mA Max.	25 MHz < f _o ≤ 50 MHz				
		5.7 mA Typ. 7.7 mA Max.	6.3 mA Typ. 8.2 mA Max.	7.0 mA Typ. 9.1 mA Max.	50 MHz < f _o ≤ 75 MHz				
		6.2 mA Typ. 8.2 mA Max.	6.9 mA Typ. 9.1 mA Max.	7.9 mA Typ. 10.4 mA Max.	75 MHz < f _o ≤ 100 MHz				
		6.9 mA Typ. 9.4 mA Max.	7.9 mA Typ. 10.7 mA Max.	9.1 mA Typ. 12.4 mA Max.	100 MHz < f _o ≤ 125 MHz				
		7.8 mA Typ. 10.4 mA Max.	9.2 mA Typ. 12.4 mA Max.	11.2 mA Typ. 15.0 mA Max.	125 MHz < f _o ≤ 170 MHz				
		Output disable current	I _{dis}	5.0 mA Typ. 7.2 mA Max.	5.0 mA Typ. 7.3 mA Max.		5.1 mA Typ. 7.4 mA Max.	OE = GND, \overline{OE} = V _{CC}	
		Standby current	I _{std}	0.3 μA Typ. 15.0 μA Max.	0.3 μA Typ. 15.0 μA Max.		0.5 μA Typ. 15.0 μA Max.	ST = GND, \overline{ST} = V _{CC}	
				45 % to 55 %			50 % V _{CC} Level, L _{CMOS} ≤ 15 pF		
		Output voltage (DC characteristics)	V _{OH} V _{OL}	90 % V _{CC} Min.			Rise/Fall time		
				Default 'A' Option ^{*2}	Other Options	I _{OH}	I _{OL}		
				f _o > 125 MHz	B: Faster	-2.0 mA	2.0 mA		
				75 MHz < f _o ≤ 125 MHz	C: Fast	-1.0 mA	1.0 mA		
				50 MHz < f _o ≤ 75 MHz	D: Slow	-0.5 mA	0.5 mA		
			f _o ≤ 50 MHz	E: Slower	-0.2 mA	0.2 mA			
Output load condition	L _{CMOS}	15 pF Max.							
Input voltage	V _{IH}	70 % V _{CC} Min.			Pin 1				
	V _{IL}	30 % V _{CC} Max.							
Rise/Fall time	tr/tf	-			Default 'A' Option ^{*2}	20 % - 80 % V _{CC} L _{CMOS} = 15 pF			
		2.0 ns Max.			f _o > 125 MHz			B: Faster	
		2.5 ns Max.			75 MHz < f _o ≤ 125 MHz			C: Fast	
		4.0 ns Max.			50 MHz < f _o ≤ 75 MHz			D: Slow	
6.0 ns Max.			f _o ≤ 50 MHz	E: Slower					
Output disable time (OE) Output disable time (ST)	tstp_oe tstp_st	1 μs Max.			Measured from the time OE or \overline{ST} pin crosses 30 % V _{CC} or measured from the time \overline{OE} or ST pin crosses 70 % V _{CC}				
Output enable time (OE)	tsta_oe	100 ns + 2 clock cycle Max.			Measured from the time OE pin crosses 70 % V _{CC} or measured from the time \overline{OE} pin crosses 30 % V _{CC}				
Output enable time (ST)	tsta_st	3 ms Max.			Measured from the time \overline{ST} pin crosses 70 % V _{CC} or measured from the time ST pin crosses 30 % V _{CC}				
Start-up time	t _{str}	3 ms Max.			Measured from the time V _{CC} reaches its rated minimum value, 1.62 V				
Phase Jitter	t _{pj}	1.2 ps Typ.			f _o = 25 MHz, Offset frequency: 12 kHz to 5 MHz				
		1.2 ps Typ.			f _o = 50 MHz, Offset frequency: 12 kHz to 20 MHz				
		1.2 ps Typ.			f _o = 75 MHz, Offset frequency: 12 kHz to 20 MHz				
		1.2 ps Typ.			f _o = 100 MHz, Offset frequency: 12 kHz to 20 MHz				
		1.1 ps Typ.			f _o = 125 MHz, Offset frequency: 12 kHz to 20 MHz				
		1.4 ps Typ. 1.5 ps Typ.			f _o = 150 MHz, Offset frequency: 12 kHz to 20 MHz f _o = 170 MHz, Offset frequency: 12 kHz to 20 MHz				
Frequency aging	f _{age}	This is included in frequency tolerance specification.			+25 °C, first year				

*1 Frequency tolerance includes initial frequency tolerance, temperature variation, supply voltage variation, reflow drift, load drift and aging (+25 °C, 1 year).

*2 Default 'A' Rise/Fall time and I_{OH}/I_{OL} are dependent on programmed frequency.

Pin description

Pin	Name	I/O type	Function	
1	OE	Input	Output Enable	High ^{*1} or Open: Specified frequency output from OUT pin Low: OUT pin is low (pull down with 500 kΩ), only output driver is disabled.
	$\overline{\text{OE}}$	Input	Output Enable	Low ^{*2} or Open: Specified frequency output from OUT pin High: OUT pin is low (pull down with 500 kΩ), only output driver is disabled.
	$\overline{\text{ST}}$	Input	Standby	High ^{*1*3} : Specified frequency output from OUT pin Low: OUT pin is low (pull down with 500 kΩ), Device goes to standby mode. Supply current reduces to the least as I _{std} .
	ST	Input	Standby	Low ^{*2*3} : Specified frequency output from OUT pin High: OUT pin is low (pull down with 500 kΩ), Device goes to standby mode. Supply current reduces to the least as I _{std} .
2	GND	Power	Ground	
3	OUT	Output	Clock output	
4	V _{CC}	Power	Power supply	

*1 If fixing it at High, please connect to V_{CC} directly.

*2 If fixing it at Low, please connect to GND directly.

*3 If necessary to use Open, please select Output Enable function.

Product Name

SG-8200CJ 170.000000MHz T J J P A
a b c d e f g h

b: Package type

CJ	2.0 mm × 1.6 mm
CG	2.5 mm × 2.0 mm

e: Frequency tolerance / f: Operating temperature

JJ	±50 × 10 ⁻⁶ / -40 °C to +125 °C
----	--

a: Model b: Package type

c: Frequency d: Supply voltage (T: 1.8 V to 3.3 V Typ.)

e: Frequency tolerance f: Operating temperature

g: Function h: Rise/Fall time

g: Function

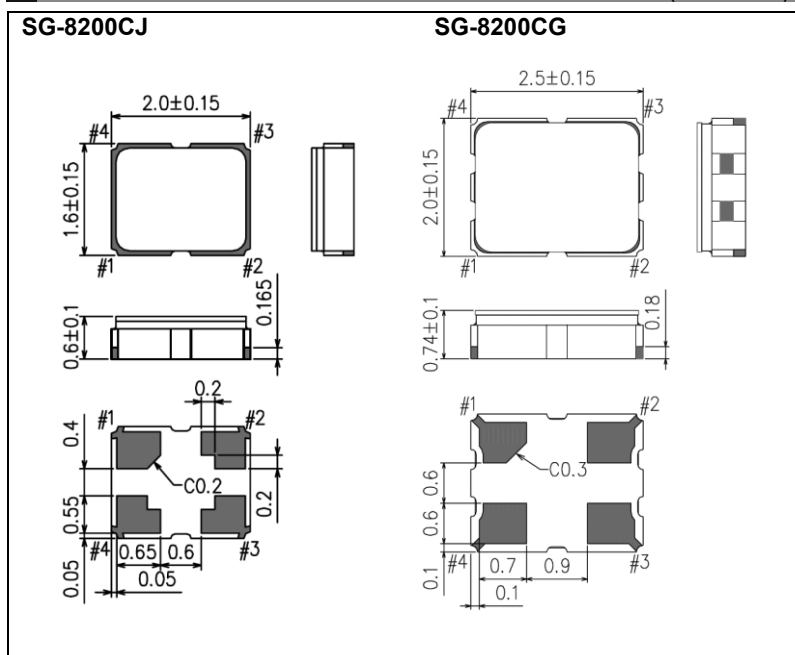
P	Output Enable (OE)
Q	Output Enable ($\overline{\text{OE}}$)
S	Standby ($\overline{\text{ST}}$)
T	Standby (ST)

h: Rise/Fall time

A	Default
B	Faster
C	Fast
D	Slow
E	Slower

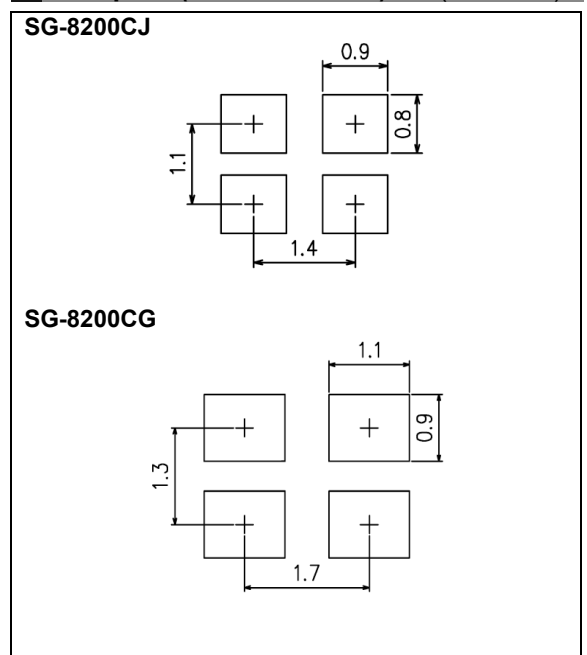
External dimensions

(Unit: mm)



Footprint (Recommended)

(Unit: mm)



Notes:

In order to achieve optimum jitter performance, the 0.01 μF to 0.1 μF capacitor between V_{CC} and GND should be placed. It is also recommended that the capacitors are placed on the device side of the PCB, as close to the device as possible and connected together with short wiring pattern.



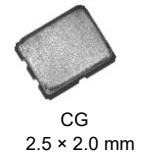
CRYSTAL OSCILLATOR (Programmable)
OUTPUT: CMOS

SG-8201 series

- Frequency range : 1.2 MHz to 170 MHz
- Supply voltage : 1.62 V to 3.63 V
- Function : Output enable (OE/ \overline{OE}) or Standby (\overline{ST} / \overline{ST})
- Frequency tolerance, operating temperature:
 - $\pm 15 \times 10^{-6}$ (-40 °C to +105 °C)
 - $\pm 25 \times 10^{-6}$ (-40 °C to +125 °C)
- PLL technology to enable setting any output frequency



Product Number
SG-8201CJ: X1G005981xxxx16
SG-8201CG: X1G006191xxxx16



Specifications (characteristics)

Item	Symbol	Specifications			Conditions/Remarks			
		1.80 V Typ. 1.62 V to 1.98 V	2.50 V Typ. 2.25 V to 2.75 V	3.30 V Typ. 2.97 V to 3.63 V				
Supply voltage	V_{CC}							
Output frequency range	f_o	1.2 MHz to 170 MHz						
Storage temperature range	T_{stg}	-55 °C to +125 °C			Storage as single product.			
Operating temperature range	T_{use}	H: -40 °C to +105 °C J: -40 °C to +125 °C						
Frequency tolerance*1	f_{tol}	B: $\pm 15 \times 10^{-6}$			$T_{use} = -40 \text{ °C to } +105 \text{ °C}$			
		D: $\pm 25 \times 10^{-6}$			$T_{use} = -40 \text{ °C to } +125 \text{ °C}$			
Current consumption	I_{CC}	5.2 mA Typ.	5.4 mA Typ.	5.6 mA Typ.	1.2 MHz $\leq f_o \leq$ 25 MHz	No load, Rise/Fall time: Default		
		7.0 mA Max.	7.2 mA Max.	7.5 mA Max.	25 MHz $< f_o \leq$ 50 MHz			
		5.4 mA Typ.	5.7 mA Typ.	6.1 mA Typ.			50 MHz $< f_o \leq$ 75 MHz	
		7.3 mA Max.	7.6 mA Max.	8.1 mA Max.	75 MHz $< f_o \leq$ 100 MHz			
		5.7 mA Typ.	6.3 mA Typ.	7.0 mA Typ.			100 MHz $< f_o \leq$ 125 MHz	
		7.7 mA Max.	8.2 mA Max.	9.1 mA Max.	125 MHz $< f_o \leq$ 170 MHz			
		6.2 mA Typ.	6.9 mA Typ.	7.9 mA Typ.				
		8.2 mA Max.	9.1 mA Max.	10.4 mA Max.				
		6.9 mA Typ.	7.9 mA Typ.	9.1 mA Typ.				
		9.4 mA Max.	10.7 mA Max.	12.4 mA Max.				
Output disable current	I_{dis}	5.0 mA Typ.	5.0 mA Typ.	5.1 mA Typ.	OE = GND, $\overline{OE} = V_{CC}$			
		7.2 mA Max.	7.3 mA Max.	7.4 mA Max.				
Standby current	I_{std}	0.3 μ A Typ.	0.3 μ A Typ.	0.5 μ A Typ.	ST = GND, ST = V_{CC}			
		15.0 μ A Max.	15.0 μ A Max.	15.0 μ A Max.				
Symmetry	SYM	45 % to 55 %			50 % V_{CC} Level, $L_{CMOS} \leq 15$ pF			
Output voltage (DC characteristics)	V_{OH}	90 % V_{CC} Min.			Rise/Fall time			
					Default 'A' Option*2	Other Options	I_{OH}	I_{OL}
	V_{OL}	10 % V_{CC} Max.			fo > 125 MHz	B: Faster	-2.0 mA	2.0 mA
					75 MHz $< f_o \leq$ 125 MHz	C: Fast	-1.0 mA	1.0 mA
					50 MHz $< f_o \leq$ 75 MHz	D: Slow	-0.5 mA	0.5 mA
			fo \leq 50 MHz	E: Slower	-0.2 mA	0.2 mA		
Output load condition	L_{CMOS}	15 pF Max.						
Input voltage	V_{IH}	70 % V_{CC} Min.			Pin 1			
	V_{IL}	30 % V_{CC} Max.						
Rise/Fall time	tr/ff	-			Default 'A' Option*2	Other Options	20 % - 80 % V_{CC} , $L_{CMOS} = 15$ pF	
		2.0 ns Max.			fo > 125 MHz	B: Faster		
		2.5 ns Max.			75 MHz $< f_o \leq$ 125 MHz	C: Fast		
		4.0 ns Max.			50 MHz $< f_o \leq$ 75 MHz	D: Slow		
6.0 ns Max.			fo \leq 50 MHz	E: Slower				
Output disable time (OE)	tstp_oe	1 μ s Max.			Measured from the time OE or \overline{ST} pin crosses 30 % V_{CC} or measured from the time \overline{OE} or ST pin crosses 70 % V_{CC}			
Output disable time (ST)	tstp_st							
Output enable time (OE)	tsta_oe	100 ns + 2 clock cycle Max.			Measured from the time OE pin crosses 70 % V_{CC} or measured from the time \overline{OE} pin crosses 30 % V_{CC}			
Output enable time (ST)	tsta_st	3 ms Max.			Measured from the time \overline{ST} pin crosses 70 % V_{CC} or measured from the time ST pin crosses 30 % V_{CC}			
Start-up time	t_str	3 ms Max.			Measured from the time V_{CC} reaches its rated minimum value, 1.62 V			
Phase Jitter	t_{PJ}	1.2 ps Typ.			fo = 25 MHz, Offset frequency: 12 kHz to 5 MHz			
		1.2 ps Typ.			fo = 50 MHz, Offset frequency: 12 kHz to 20 MHz			
		1.2 ps Typ.			fo = 75 MHz, Offset frequency: 12 kHz to 20 MHz			
		1.2 ps Typ.			fo = 100 MHz, Offset frequency: 12 kHz to 20 MHz			
		1.1 ps Typ.			fo = 125 MHz, Offset frequency: 12 kHz to 20 MHz			
		1.4 ps Typ.			fo = 150 MHz, Offset frequency: 12 kHz to 20 MHz			
1.5 ps Typ.			fo = 170 MHz, Offset frequency: 12 kHz to 20 MHz					
Frequency aging	f_{age}	This is included in frequency tolerance specification.			+25 °C, first year			

*1 Frequency tolerance includes initial frequency tolerance, temperature variation, supply voltage variation, reflow drift, load drift and aging (+25 °C, 1 year).

*2 Default 'A' Rise/Fall time and I_{OH}/I_{OL} are dependent on programmed frequency.

Pin description

Pin	Name	I/O type	Function
1	OE	Input	Output Enable High ^{*1} or Open: Specified frequency output from OUT pin Low: OUT pin is low (pull down with 500 kΩ), only output driver is disabled.
	$\overline{\text{OE}}$	Input	Output Enable Low ^{*2} or Open: Specified frequency output from OUT pin High: OUT pin is low (pull down with 500 kΩ), only output driver is disabled.
	$\overline{\text{ST}}$	Input	Standby High ^{*1*3} : Specified frequency output from OUT pin Low: OUT pin is low (pull down with 500 kΩ), Device goes to standby mode. Supply current reduces to the least as I _{std} .
	ST	Input	Standby Low ^{*2*3} : Specified frequency output from OUT pin High: OUT pin is low (pull down with 500 kΩ), Device goes to standby mode. Supply current reduces to the least as I _{std} .
2	GND	Power	Ground
3	OUT	Output	Clock output
4	V _{CC}	Power	Power supply

*1 If fixing it at High, please connect to V_{CC} directly.

*2 If fixing it at Low, please connect to GND directly.

*3 If necessary to use Open, please select Output Enable function.

Product Name

SG-8201CJ 170.000000MHz T D J P A
 a b c d e f g h

b: Package type	
CJ	2.0 mm × 1.6 mm
CG	2.5 mm × 2.0 mm

e: Frequency tolerance / f: Operating temperature	
BH	±15 × 10 ⁻⁶ / -40 °C to +105 °C
DJ	±25 × 10 ⁻⁶ / -40 °C to +125 °C

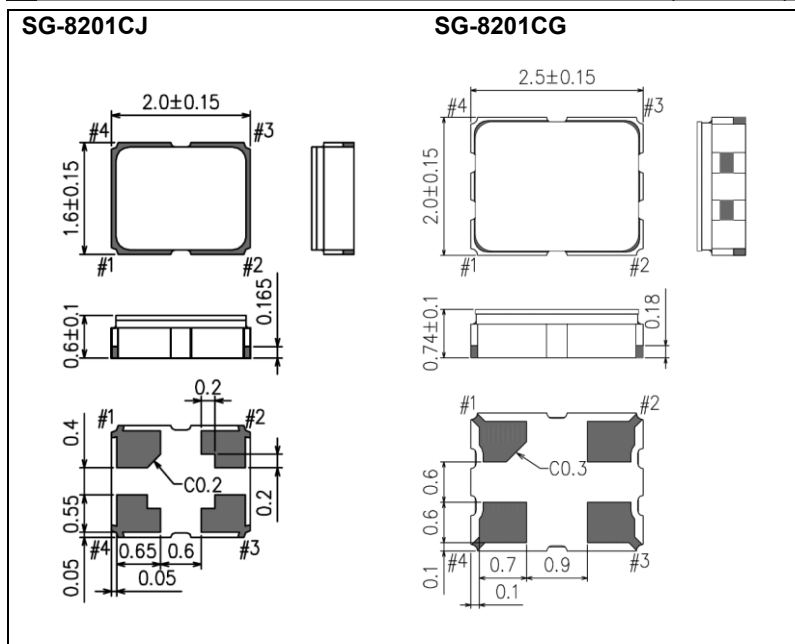
a: Model b: Package type
 c: Frequency d: Supply voltage (T: 1.8 V to 3.3 V Typ.)
 e: Frequency tolerance f: Operating temperature
 g: Function h: Rise/Fall time

g: Function	
P	Output Enable (OE)
Q	Output Enable ($\overline{\text{OE}}$)
S	Standby ($\overline{\text{ST}}$)
T	Standby (ST)

h: Rise/Fall time	
A	Default
B	Faster
C	Fast
D	Slow
E	Slower

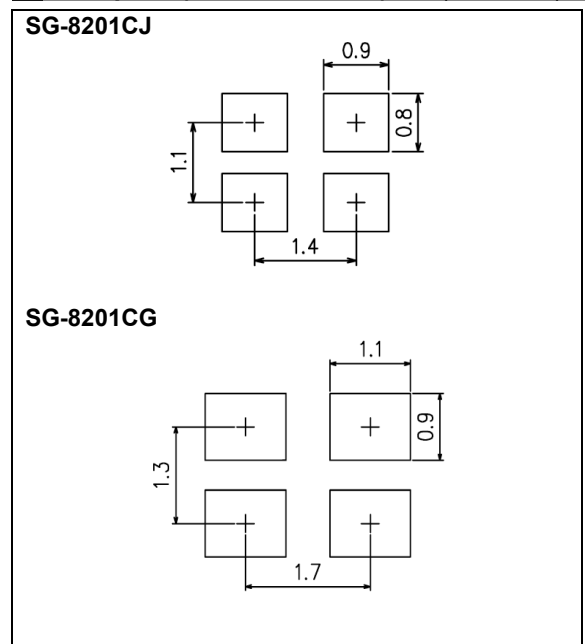
External dimensions

(Unit: mm)



Footprint (Recommended)

(Unit: mm)



Notes:

In order to achieve optimum jitter performance, the 0.1 μF capacitor between V_{CC} and GND should be placed. It is also recommended that the capacitors are placed on the device side of the PCB, as close to the device as possible and connected together with short wiring pattern.

CRYSTAL OSCILLATOR (Programmable)

OUTPUT: CMOS

SG-8018 series

- Frequency range : 0.67 MHz to 170 MHz (1 ppm Step)
- Supply voltage : 1.62 V to 3.63 V
- Function : Output enable (OE) or Standby (\overline{ST})
- Frequency tolerance : ± 50 ppm (-40 °C to +105 °C)
Including frequency aging(+25 °C, 10 years)
- PLL technology to enable short lead time
- Available field oscillator programmer "SG-Writer II"



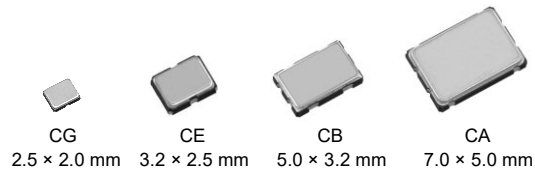
Product Number

SG-8018CG: X1G005601xxxx00

SG-8018CE: X1G005591xxxx00

SG-8018CB: X1G005581xxxx00

SG-8018CA: X1G005571xxxx00



Specifications (characteristics)

Item	Symbol	Specifications				Conditions/Remarks					
Supply voltage	V_{CC}	1.80 V Typ.		2.50 V Typ.	3.30 V Typ.	-					
		1.62 V to 1.98 V	1.98 V to 2.20 V	2.20 V to 2.80 V	2.70 V to 3.63 V						
Output frequency range	f_o	0.67 MHz to 170 MHz									
Storage temperature range	T_{stg}	-40 °C to +125 °C				Storage as single product.					
Operating temperature range	T_{use}	-40 °C to +105 °C				-					
Frequency tolerance ^{*1}	f_{tol}	J: $\pm 50 \times 10^{-6}$				$T_{use} = -40$ °C to +105 °C					
Current consumption	I_{CC}	3.2 mA Max.	3.3 mA Max.	3.4 mA Max.	3.5 mA Max.	$T_{use} = +105$ °C	No load, $f_o = 20$ MHz				
		2.7 mA Typ.		2.9 mA Typ.	3.0 mA Typ.	$T_{use} = +25$ °C					
		5.5 mA Max.	5.8 mA Max.	6.7 mA Max.	8.1 mA Max.	$T_{use} = +105$ °C	No load, $f_o = 170$ MHz				
		4.7 mA Typ.		5.7 mA Typ.	6.8 mA Typ.	$T_{use} = +25$ °C					
Output disable current	I_{dis}	3.2 mA Max.	3.2 mA Max.	3.3 mA Max.	3.5 mA Max.	OE = GND, $f_o = 170$ MHz					
Standby current	I_{std}	0.9 μ A Max.	1.0 μ A Max.	1.5 μ A Max.	2.5 μ A Max.	$T_{use} = +105$ °C	ST = GND				
		0.3 μ A Typ.	0.4 μ A Typ.	0.5 μ A Typ.	1.1 μ A Typ.	$T_{use} = +25$ °C					
Symmetry	SYM	45 % to 55 %				50 % V_{CC} Level					
Output voltage (DC characteristics)	V_{OH}	90 % V_{CC} Min.				IOH/IOL Conditions [mA]					
						Rise/Fall time	V_{CC}	*A	*B	*C	*D
	V_{OL}	10 % V_{CC} Max.				Default ($f_o > 40$ MHz),	IOH	-2.5	-3.5	-4.0	-5.0
						Fast	IOH	2.5	3.5	4.0	5.0
Slow	Default ($f_o \leq 40$ MHz)	IOH	-1.5	-2.0	-2.5	-3.0					
		IOH	1.5	2.0	2.5	3.0					
			IOH	-1.0	-1.5	-2.0	-2.5				
			IOH	1.0	1.5	2.0	2.5				
						*A: 1.62 V to 1.98 V, *B: 1.98 V to 2.20 V, *C: 2.20 V to 2.80 V, *D: 2.70 V to 3.63 V					
Output load condition	L_{CMOS}	15 pF Max.				-					
Input voltage	V_{IH}	70 % V_{CC} Min.				OE or \overline{ST}					
	V_{IL}	30 % V_{CC} Max.									
Rise time /Fall time	Default	t_r/t_f	3.0 ns Max.			$f_o > 40$ MHz		20 % - 80 % V_{CC} , $L_{CMOS} = 15$ pF			
			6.0 ns Max.			$f_o \leq 40$ MHz					
			3.0 ns Max.			$f_o = 0.67$ MHz to 170 MHz					
			10.0 ns Max.			$f_o = 0.67$ MHz to 20 MHz					
Output disable time (OE)	t_{stp_oe}	1 μ s Max.				Measured from the time OE or \overline{ST} pin crosses 30 % V_{CC}					
Output disable time (ST)	t_{stp_st}	1 μ s Max.				Measured from the time OE pin crosses 70 % V_{CC}					
Output enable time (OE)	t_{sta_oe}	1 μ s Max.				Measured from the time OE pin crosses 70 % V_{CC}					
Output enable time (ST)	t_{sta_st}	3 ms Max.				Measured from the time \overline{ST} pin crosses 70 % V_{CC}					
Start-up time	t_{str}	3 ms Max.				Measured from the time V_{CC} reaches its rated minimum value, 1.62 V					
Frequency aging	f_{age}	This is included in frequency tolerance specification.				+25 °C, 10 years					

*1 Frequency tolerance includes initial frequency tolerance, frequency / temperature characteristics, frequency / voltage coefficient, frequency / load coefficient and frequency aging (+25 °C, 10 years).

Pin description

Pin	Name	I/O type	Function	
1	OE	Input	Output enable	High ^{*2} : Specified frequency output from OUT pin Low: Out pin is low (weak pull down), only output driver is disabled.
	\overline{ST}	Input	Standby	High ^{*2} : Specified frequency output from OUT pin Low: Out pin is low (weak pull down), Device goes to standby mode. Supply current reduces to the least as I_{std} .
2	GND	Power	Ground	
3	OUT	Output	Clock output	
4	V_{CC}	Power	Power supply	

*2 Please do not use the OE/ \overline{ST} terminal in the open state.



Product Name

SG-8018CG 25.000000MHz T J H P A
① ② ③ ④⑤⑥⑦⑧

- ① Model ② Package type ③ Frequency
- ④ Supply voltage (T: 1.8 V to 3.3 V Typ.)
- ⑤ Frequency tolerance (J: $\pm 50 \times 10^{-6}$)
- ⑥ Operating temperature (H: -40 °C to +105 °C)
- ⑦ Function ⑧ Rise/Fall time

② Package type	
CG	2.5 mm × 2.0 mm
CE	3.2 mm × 2.5 mm
CB	5.0 mm × 3.2 mm
CA	7.0 mm × 5.0 mm

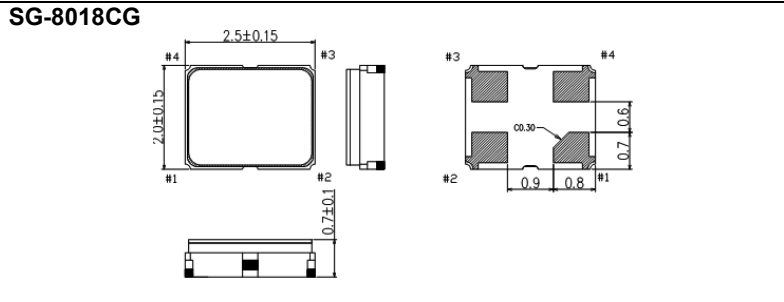
⑦ Function	
P	Output enable
S	Standby

⑧ Rise time/Fall time	
A	Default
B	Fast
C*	Slow

* Available only when fo ≤ 20 MHz

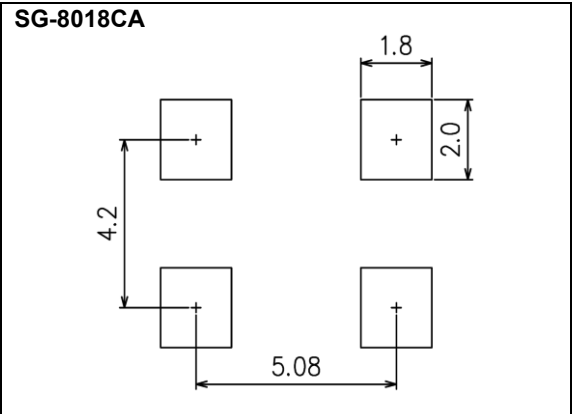
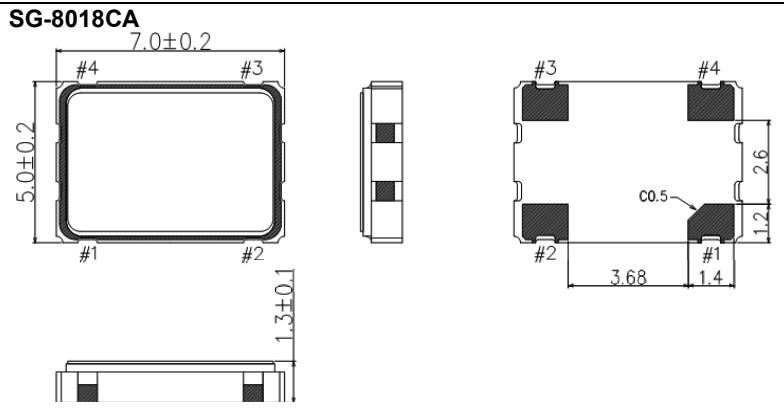
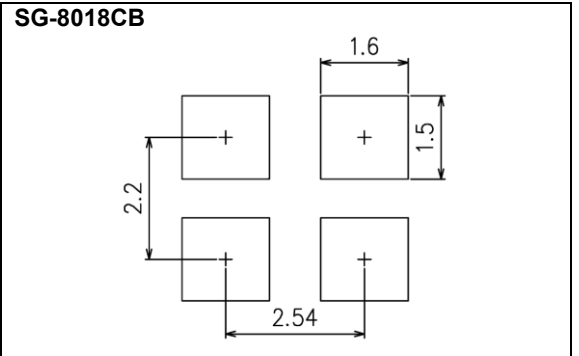
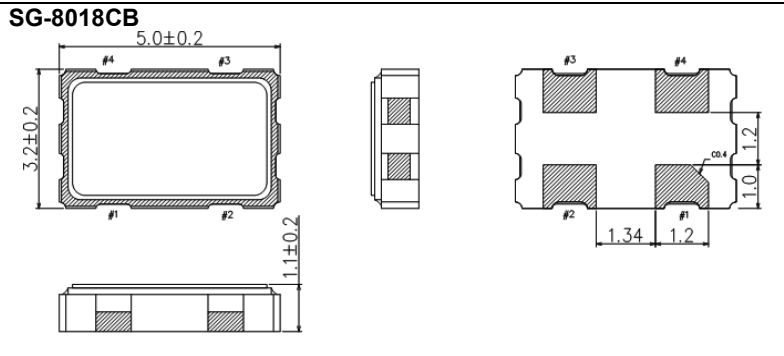
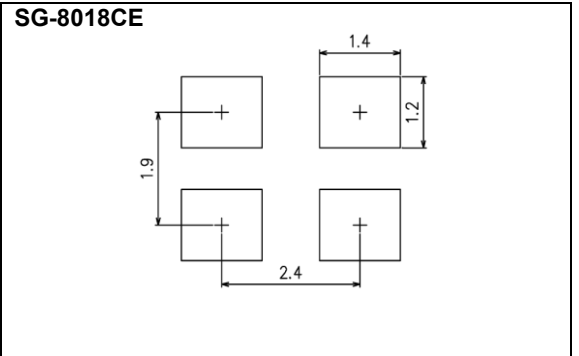
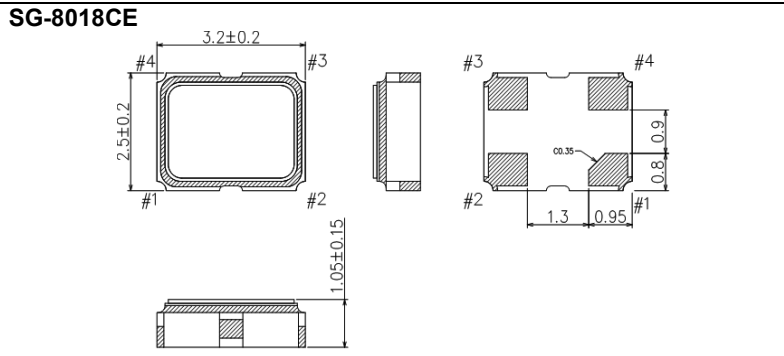
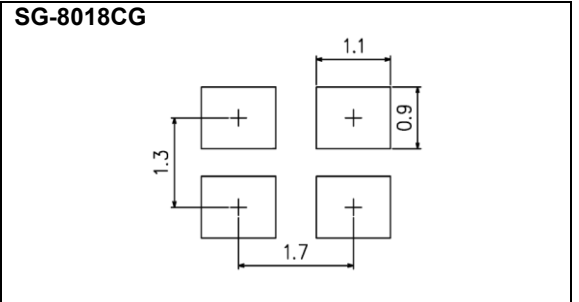
External dimensions

(Unit: mm)



Footprint (Recommended)

(Unit: mm)



Notes:

In order to achieve optimum jitter performance, the 0.1 μF capacitor between V_{CC} and GND should be placed. It is also recommended that the capacitors are placed on the device side of the PCB, as close to the device as possible and connected together with short wiring pattern.

CRYSTAL OSCILLATOR (Programmable)

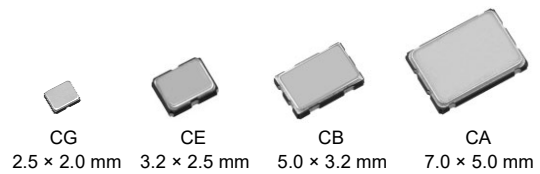
OUTPUT: CMOS

SG-8101 series

- Frequency range : 0.67 MHz to 170 MHz (1 × 10⁻⁶ Step)
- Supply voltage : 1.62 V to 3.63 V
- Function : Output enable (OE) or Standby (\overline{ST})
- Frequency tolerance : $\pm 15 \times 10^{-6}$ (-40 °C to +85 °C)
 $\pm 20 \times 10^{-6}$, $\pm 50 \times 10^{-6}$ (-40 °C to +105 °C)
- PLL technology to enable short lead time
- Available field oscillator programmer "SG-Writer II"



Product Number
 SG-8101CG: X1G005181xxxx00
 SG-8101CE: X1G005211xxxx00
 SG-8101CB: X1G005201xxxx00
 SG-8101CA: X1G005191xxxx00



Specifications (characteristics)

Item	Symbol	Specifications				Conditions/Remarks																																											
Supply voltage	V _{CC}	1.80 V Typ.		2.50 V Typ.	3.30 V Typ.		-																																										
		1.62 V to 1.98 V	1.98 V to 2.20 V	2.20 V to 2.80 V	2.70 V to 3.63 V																																												
Output frequency range	f _o	0.67 MHz to 170 MHz																																															
Storage temperature range	T _{stg}	-40 °C to +125 °C				Storage as single product.																																											
Operating temperature range	T _{use}	-40 °C to +85 °C				-																																											
		-40 °C to +105 °C																																															
Frequency tolerance ^{*1}	f _{tol}	B: $\pm 15 \times 10^{-6}$				T _{use} = -40 °C to +85 °C																																											
		C: $\pm 20 \times 10^{-6}$				T _{use} = -40 °C to +105 °C																																											
		J: $\pm 50 \times 10^{-6}$				T _{use} = -40 °C to +105 °C																																											
Current consumption	I _{CC}	3.2 mA Max.	3.3 mA Max.	3.4 mA Max.	3.5 mA Max.	T _{use} = +105 °C	No load, f _o = 20 MHz																																										
		2.7 mA Typ.		2.9 mA Typ.	3.0 mA Typ.	T _{use} = +25 °C																																											
		5.5 mA Max.	5.8 mA Max.	6.7 mA Max.	8.1 mA Max.	T _{use} = +105 °C	No load, f _o = 170 MHz																																										
		4.7 mA Typ.		5.7 mA Typ.	6.8 mA Typ.	T _{use} = +25 °C																																											
Output disable current	I _{dis}	3.2 mA Max.	3.2 mA Max.	3.3 mA Max.	3.5 mA Max.	OE = GND, f _o = 170 MHz																																											
Standby current	I _{std}	0.9 μ A Max.	1.0 μ A Max.	1.5 μ A Max.	2.5 μ A Max.	T _{use} = +105 °C	ST = GND																																										
		0.3 μ A Typ.	0.4 μ A Typ.	0.5 μ A Typ.	1.1 μ A Typ.	T _{use} = +25 °C																																											
Symmetry	SYM	45 % to 55 %				50 % V _{CC} Level																																											
Output voltage (DC characteristics)	V _{OH}	90 % V _{CC} Min.				<table border="1"> <thead> <tr> <th></th> <th></th> <th>V_{CC}</th> <th>*A</th> <th>*B</th> <th>*C</th> <th>*D</th> </tr> </thead> <tbody> <tr> <td rowspan="2">Rise/Fall time</td> <td>Default (f_o > 40 MHz)</td> <td>I_{OH}</td> <td>-2.5</td> <td>-3.5</td> <td>-4.0</td> <td>-5.0</td> </tr> <tr> <td>Fast</td> <td>I_{OL}</td> <td>2.5</td> <td>3.5</td> <td>4.0</td> <td>5.0</td> </tr> <tr> <td rowspan="2">Default (f_o ≤ 40 MHz)</td> <td>I_{OH}</td> <td>-1.5</td> <td>-2.0</td> <td>-2.5</td> <td>-3.0</td> </tr> <tr> <td>I_{OL}</td> <td>1.5</td> <td>2.0</td> <td>2.5</td> <td>3.0</td> </tr> <tr> <td rowspan="2">Slow</td> <td>I_{OH}</td> <td>-1.0</td> <td>-1.5</td> <td>-2.0</td> <td>-2.5</td> </tr> <tr> <td>I_{OL}</td> <td>1.0</td> <td>1.5</td> <td>2.0</td> <td>2.5</td> </tr> </tbody> </table>				V _{CC}	*A	*B	*C	*D	Rise/Fall time	Default (f _o > 40 MHz)	I _{OH}	-2.5	-3.5	-4.0	-5.0	Fast	I _{OL}	2.5	3.5	4.0	5.0	Default (f _o ≤ 40 MHz)	I _{OH}	-1.5	-2.0	-2.5	-3.0	I _{OL}	1.5	2.0	2.5	3.0	Slow	I _{OH}	-1.0	-1.5	-2.0	-2.5	I _{OL}	1.0	1.5	2.0	2.5
			V _{CC}	*A	*B			*C	*D																																								
Rise/Fall time	Default (f _o > 40 MHz)	I _{OH}	-2.5	-3.5	-4.0	-5.0																																											
	Fast	I _{OL}	2.5	3.5	4.0	5.0																																											
Default (f _o ≤ 40 MHz)	I _{OH}	-1.5	-2.0	-2.5	-3.0																																												
	I _{OL}	1.5	2.0	2.5	3.0																																												
Slow	I _{OH}	-1.0	-1.5	-2.0	-2.5																																												
	I _{OL}	1.0	1.5	2.0	2.5																																												
V _{OL}	10 % V _{CC} Max.																																																
Output load condition	L _{CMOS}	15 pF Max.				-																																											
Input voltage	V _{IH}	70 % V _{CC} Min.				OE or \overline{ST}																																											
	V _{IL}	30 % V _{CC} Max.																																															
Rise time / Fall time	Default	tr/tf	3.0 ns Max.			f _o > 40 MHz																																											
			6.0 ns Max.			f _o ≤ 40 MHz																																											
			3.0 ns Max.			f _o = 0.67 MHz to 170 MHz																																											
			10.0 ns Max.			f _o = 0.67 MHz to 20 MHz																																											
Output disable time (OE)	tstp_oe	1 μ s Max.				Measured from the time OE or \overline{ST} pin crosses 30 % V _{CC}																																											
Output disable time (ST)	tstp_st	1 μ s Max.				Measured from the time OE pin crosses 70 % V _{CC}																																											
Output enable time (OE)	tsta_oe	1 μ s Max.				Measured from the time OE pin crosses 70 % V _{CC}																																											
Output enable time (ST)	tsta_st	3 ms Max.				Measured from the time \overline{ST} pin crosses 70 % V _{CC}																																											
Start-up time	t _{str}	3 ms Max.				Measured from the time V _{CC} reaches its rated minimum value, 1.62 V																																											
Frequency aging	f _{age}	This is included in frequency tolerance specification.				+25 °C, first year																																											

*1 Frequency tolerance includes initial frequency tolerance, frequency / temperature characteristics, frequency / voltage coefficient, frequency / load coefficient and frequency aging (+25 °C, 1 year).

Pin description

Pin	Name	I/O type	Function	
1	OE	Input	Output enable	High ^{*2} : Specified frequency output from OUT pin Low: Out pin is low (weak pull down), only output driver is disabled.
	\overline{ST}	Input	Standby	High ^{*2} : Specified frequency output from OUT pin Low: Out pin is low (weak pull down), Device goes to standby mode. Supply current reduces to the least as I _{std} .
2	GND	Power	Ground	
3	OUT	Output	Clock output	
4	V _{CC}	Power	Power supply	

*2 Please do not use the OE/ \overline{ST} terminal in the open state.



Product Name

SG-8101CG 25.000000MHz TCHPA
① ② ③ ④⑤⑥⑦⑧

- ① Model ② Package type
- ③ Frequency ④ Supply voltage (T: 1.8 V to 3.3 V Typ.)
- ⑤ Frequency tolerance ⑥ Operating temperature
- ⑦ Function ⑧ Rise/Fall time

② Package type	
CG	2.5 mm × 2.0 mm
CE	3.2 mm × 2.5 mm
CB	5.0 mm × 3.2 mm
CA	7.0 mm × 5.0 mm

⑤ Frequency tolerance / ⑥ Operating temperature	
BG	±15 × 10 ⁻⁶ / -40 °C to +85 °C
CH	±20 × 10 ⁻⁶ / -40 °C to +105 °C
JH	±50 × 10 ⁻⁶ / -40 °C to +105 °C

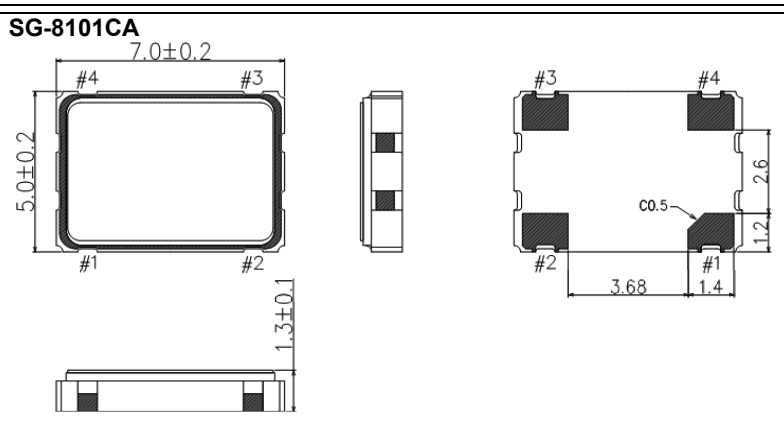
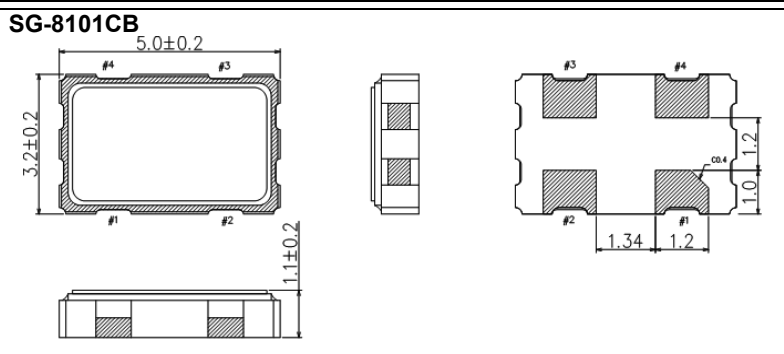
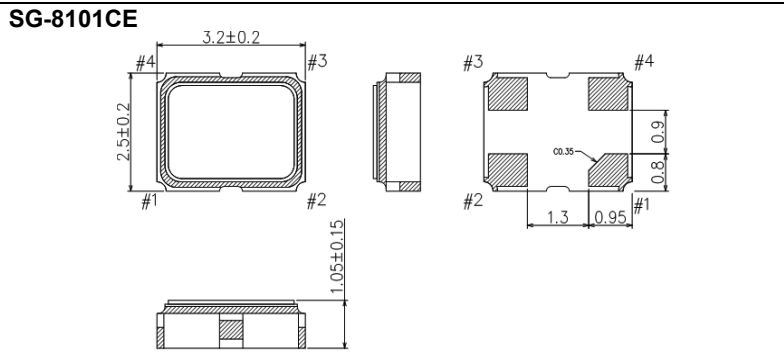
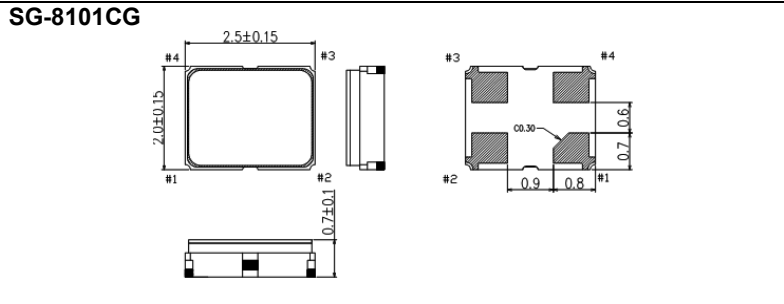
⑦ Function	
P	Output enable
S	Standby

⑧ Rise time/Fall time	
A	Default
B	Fast
C*	Slow

* Available only when fo ≤ 20 MHz

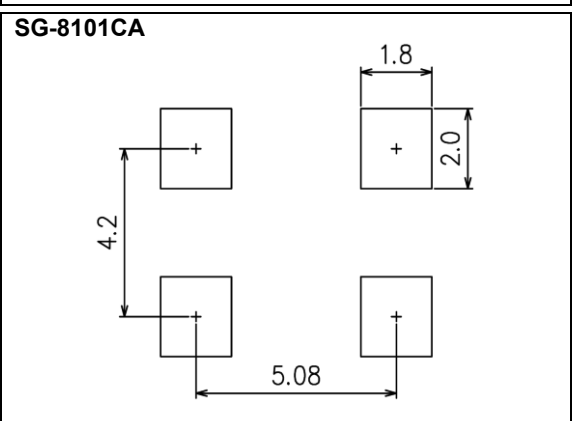
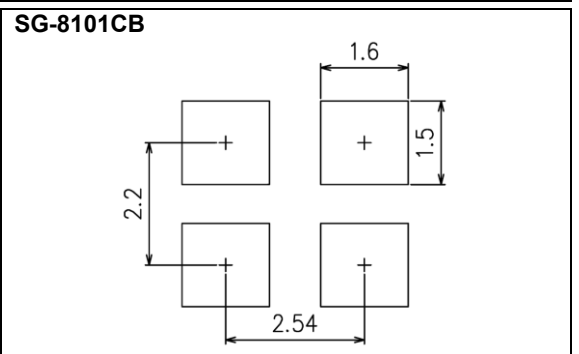
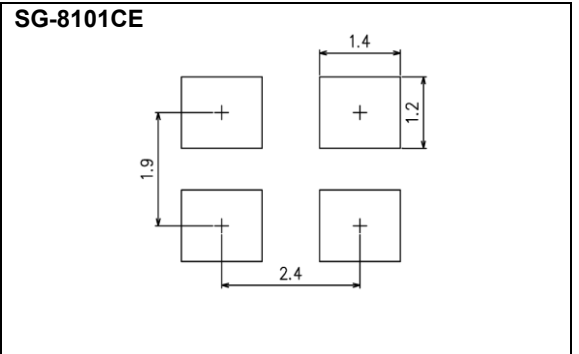
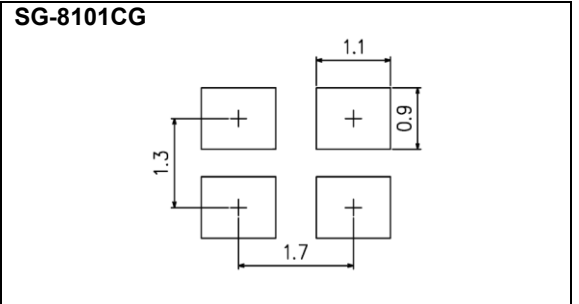
External dimensions

(Unit: mm)



Footprint (Recommended)

(Unit: mm)



Notes:

In order to achieve optimum jitter performance, the 0.1 μF capacitor between V_{CC} and GND should be placed. It is also recommended that the capacitors are placed on the device side of the PCB, as close to the device as possible and connected together with short wiring pattern.

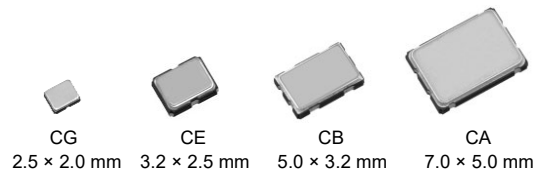
CRYSTAL OSCILLATOR (Programmable)
SPREAD SPECTRUM
OUTPUT: CMOS



Product Number
SG-9101CA: X1G005301xxxx00
SG-9101CB: X1G005311xxxx00
SG-9101CE: X1G005321xxxx00
SG-9101CG: X1G005291xxxx00

SG-9101 series

- Frequency range : 0.67 MHz to 170 MHz (1 ppm Step)
- Supply voltage : 1.62 V to 3.63 V
- Function : Output enable (OE) or Standby (\overline{ST})
- Configurable spread spectrum settings:
2 kinds of spread type, 6 kinds of spread width
4 kinds of modulation frequency, 3 kinds of spread profile
- PLL technology to enable short lead time
- Available field oscillator programmer "SG-Writer II"



Specifications (characteristics)

Item	Symbol	Specifications				Conditions/Remarks																																														
Supply voltage	V_{CC}	1.80 V Typ.		2.50 V Typ.	3.30 V Typ.	-																																														
		1.62 V to 1.98 V	1.98 V to 2.20 V	2.20 V to 2.80 V	2.70 V to 3.63 V																																															
Output frequency range	f_o	0.67 MHz to 170 MHz																																																		
Storage temperature range	T_{stg}	-40 °C to +125 °C				Storage as single product.																																														
Operating temperature range	T_{use}	-40 °C to +85 °C																																																		
Frequency tolerance ^{*1}	f_{tol}	$\pm 50 \times 10^{-6}$				Average frequency of 1s gate time.																																														
		3.4 mA Max.		3.5 mA Max.	3.6 mA Max.	3.7 mA Max.	$T_{use} = +105 \text{ °C}$	No load, $f_o = 20 \text{ MHz}$																																												
Current consumption	I_{CC}	2.9 mA Typ.		3.0 mA Typ.	3.2 mA Typ.	$T_{use} = +25 \text{ °C}$	No load, $f_o = 170 \text{ MHz}$																																													
		5.7 mA Max.	6.0 mA Max.	6.9 mA Max.	8.3 mA Max.	$T_{use} = +105 \text{ °C}$																																														
		4.9 mA Typ.		5.9 mA Typ.	7.0 mA Typ.	$T_{use} = +25 \text{ °C}$																																														
		3.4 mA Max.		3.4 mA Max.	3.5 mA Max.	3.7 mA Max.		$OE = GND, f_o = 170 \text{ MHz}$																																												
Output disable current	I_{dis}	0.9 μA Max.	1.0 μA Max.	1.5 μA Max.	2.5 μA Max.	$T_{use} = +105 \text{ °C}$	$\overline{ST} = GND$																																													
Standby current	I_{std}	0.3 μA Typ.	0.4 μA Typ.	0.5 μA Typ.	1.1 μA Typ.	$T_{use} = +25 \text{ °C}$																																														
Symmetry	SYM	45 % to 55 %				50 % V_{CC} Level																																														
Output voltage (DC characteristics)	V_{OH}	90 % V_{CC} Min.				<table border="1"> <thead> <tr> <th colspan="2">I_{OH}/I_{OL} Conditions</th> <th colspan="4">[mA]</th> </tr> <tr> <th>Rise/Fall time</th> <th>V_{CC}</th> <th>*A</th> <th>*B</th> <th>*C</th> <th>*D</th> </tr> </thead> <tbody> <tr> <td rowspan="2">Default ($f_o > 40 \text{ MHz}$), Fast</td> <td>I_{OH}</td> <td>-2.5</td> <td>-3.5</td> <td>-4.0</td> <td>-5.0</td> </tr> <tr> <td>I_{OL}</td> <td>2.5</td> <td>3.5</td> <td>4.0</td> <td>5.0</td> </tr> <tr> <td rowspan="2">Default ($f_o \leq 40 \text{ MHz}$)</td> <td>I_{OH}</td> <td>-1.5</td> <td>-2.0</td> <td>-2.5</td> <td>-3.0</td> </tr> <tr> <td>I_{OL}</td> <td>1.5</td> <td>2.0</td> <td>2.5</td> <td>3.0</td> </tr> <tr> <td rowspan="2">Slow</td> <td>I_{OH}</td> <td>-1.0</td> <td>-1.5</td> <td>-2.0</td> <td>-2.5</td> </tr> <tr> <td>I_{OL}</td> <td>1.0</td> <td>1.5</td> <td>2.0</td> <td>2.5</td> </tr> </tbody> </table>		I _{OH} /I _{OL} Conditions		[mA]				Rise/Fall time	V_{CC}	*A	*B	*C	*D	Default ($f_o > 40 \text{ MHz}$), Fast	I _{OH}	-2.5	-3.5	-4.0	-5.0	I _{OL}	2.5	3.5	4.0	5.0	Default ($f_o \leq 40 \text{ MHz}$)	I _{OH}	-1.5	-2.0	-2.5	-3.0	I _{OL}	1.5	2.0	2.5	3.0	Slow	I _{OH}	-1.0	-1.5	-2.0	-2.5	I _{OL}	1.0	1.5	2.0	2.5
	I _{OH} /I _{OL} Conditions		[mA]																																																	
Rise/Fall time	V_{CC}	*A	*B	*C	*D																																															
Default ($f_o > 40 \text{ MHz}$), Fast	I _{OH}	-2.5	-3.5	-4.0	-5.0																																															
	I _{OL}	2.5	3.5	4.0	5.0																																															
Default ($f_o \leq 40 \text{ MHz}$)	I _{OH}	-1.5	-2.0	-2.5	-3.0																																															
	I _{OL}	1.5	2.0	2.5	3.0																																															
Slow	I _{OH}	-1.0	-1.5	-2.0	-2.5																																															
	I _{OL}	1.0	1.5	2.0	2.5																																															
V_{OL}	10 % V_{CC} Max.																																																			
Output load condition	L_{CMOS}	15 pF Max.				-																																														
Input voltage	V_{IH}	70 % V_{CC} Min.				OE or \overline{ST}																																														
	V_{IL}	30 % V_{CC} Max.																																																		
Rise time / Fall time	Default Fast Slow	t_r/t_f	3.0 ns Max.		$f_o > 40 \text{ MHz}$		20 % - 80 % V_{CC} , $L_{CMOS} = 15 \text{ pF}$																																													
			6.0 ns Max.		$f_o \leq 40 \text{ MHz}$																																															
			3.0 ns Max.		$f_o = 0.67 \text{ MHz to } 170 \text{ MHz}$																																															
			10.0 ns Max.		$f_o = 0.67 \text{ MHz to } 20 \text{ MHz}$																																															
Output disable time (OE)	t_{stp_oe}	1 μs Max.				Measured from the time OE or \overline{ST} pin crosses 30 % V_{CC}																																														
Output disable time (ST)	t_{stp_st}	1 μs Max.				Measured from the time OE pin crosses 70 % V_{CC}																																														
Output enable time (OE)	t_{sta_oe}	3 ms Max.				Measured from the time \overline{ST} pin crosses 70 % V_{CC}																																														
Output enable time (ST)	t_{sta_st}	3 ms Max.				Measured from the time V_{CC} reaches its rated minimum value, 1.62 V																																														
Start-up time	t_{str}	3 ms Max.				Measured from the time V_{CC} reaches its rated minimum value, 1.62 V																																														
Frequency aging	f_{age}	This is included in frequency tolerance specification.				+25 °C, first year																																														

*1 Frequency tolerance includes initial frequency tolerance, frequency / temperature characteristics, frequency / voltage coefficient, frequency / load coefficient and frequency aging (+25 °C, 1 year).

Pin description

Pin	Name	I/O type	Function	
1	OE	Input	Output enable	High ^{*2} : Specified frequency output from OUT pin Low: Out pin is low (weak pull down), only output driver is disabled.
	\overline{ST}	Input	Standby	High ^{*2} : Specified frequency output from OUT pin Low: Out pin is low (weak pull down), Device goes to standby mode. Supply current reduces to the least as I_{std} .
2	GND	Power	Ground	
3	OUT	Output	Clock output	
4	V_{CC}	Power	Power supply	

*2 Please do not use the OE/ST terminal in the open state.



Product Name

SG-9101CG 170.000000MHz C 20 P H A A A
① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩

- ① Model ② Package type ③ Frequency
- ④ Spread type ⑤ Spread width
- ⑥ Function ⑦ Operating temperature
- ⑧ Modulation frequency ⑨ Spread profile
- ⑩ Rise/Fall time

② Package type	
CG	2.5 mm × 2.0 mm
CE	3.2 mm × 2.5 mm
CB	5.0 mm × 3.2 mm
CA	7.0 mm × 5.0 mm

④ Spread type	
C	Center spread
D	Down spread

⑤ Spread width		
	Center spread	Down spread
02	±0.25 %	
05	±0.5 %	-0.5 %
07	±0.75 %	
10	±1.0 %	-1.0 %
15	±1.5 %	-1.5 %
20	±2.0 %	-2.0 %
30		-3.0 %
40		-4.0 %

⑧ Modulation frequency	
A	25.4 kHz (Default)
B	12.7 kHz
C	8.5 kHz
D	6.3 kHz

⑨ Spread profile	
A	Hershey-kiss (Default)
B	Sine-wave
C	Triangle

⑥ Function	
P	Output enable
S	Standby

⑩ Rise/Fall time	
A	Default
B	Fast
C*	Slow

⑦ Operating temperature	
G	-40 °C to +85 °C
H	-40 °C to +105 °C

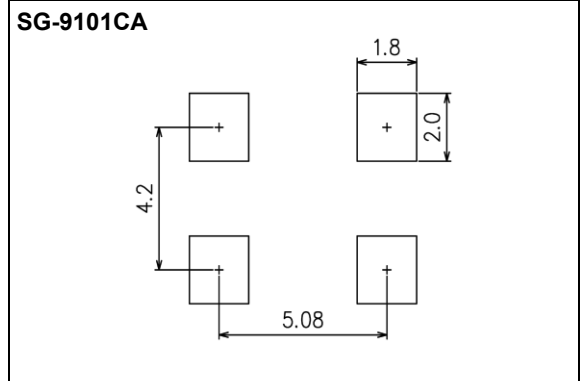
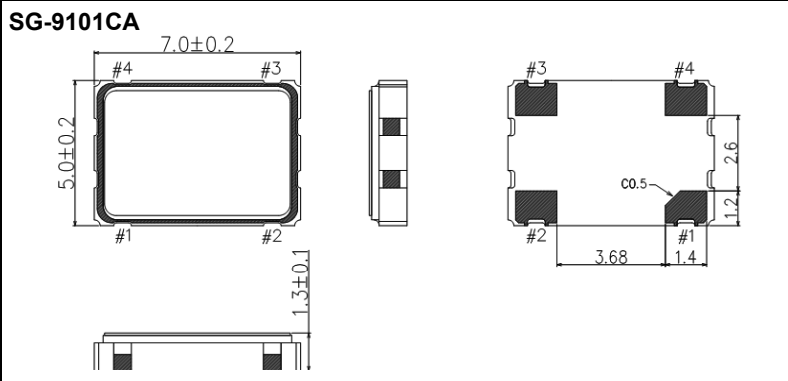
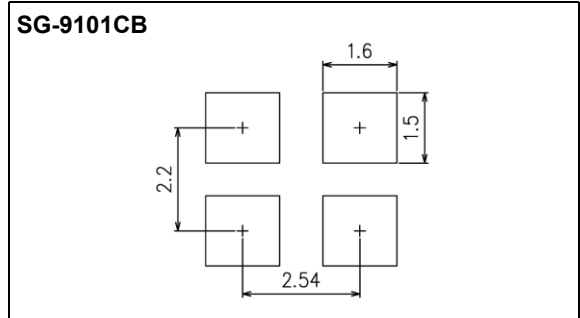
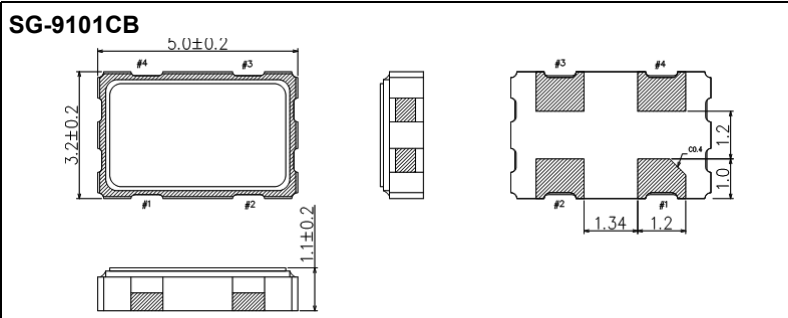
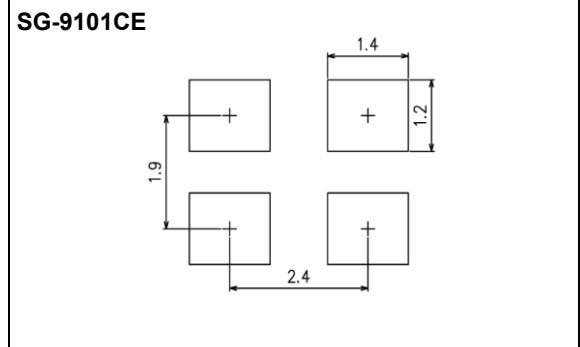
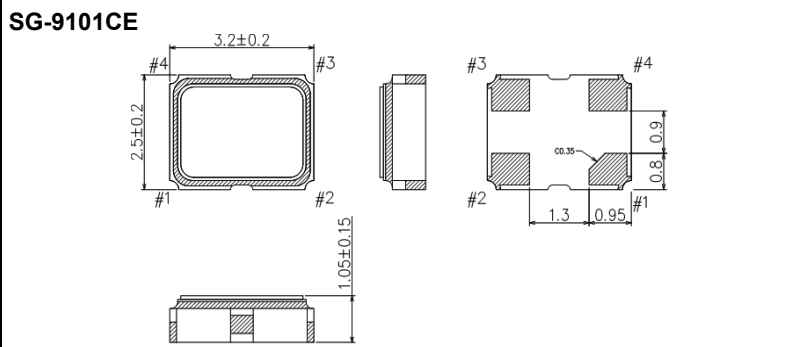
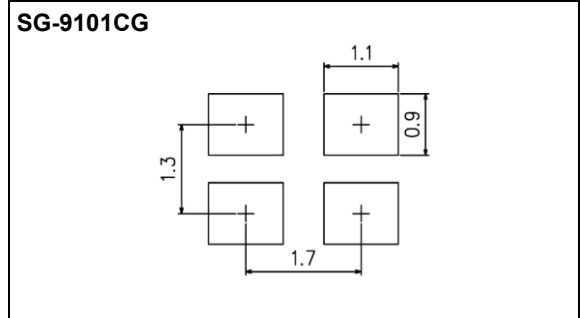
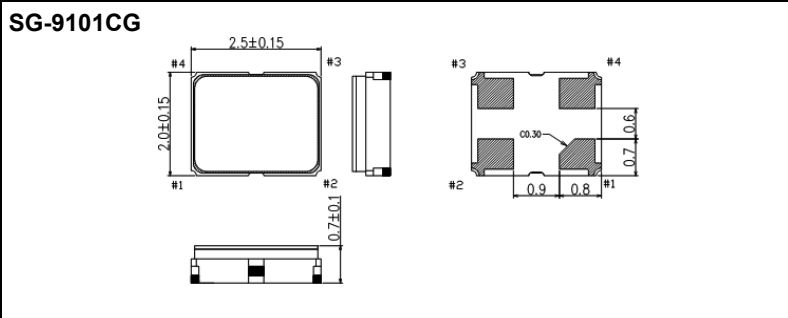
* Available only when fo ≤ 20 MHz

External dimensions

(Unit: mm)

Footprint (Recommended)

(Unit: mm)



Notes:

In order to achieve optimum jitter performance, the 0.1 μF capacitor between Vcc and GND should be placed. It is also recommended that the capacitors are placed on the device side of the PCB, as close to the device as possible and connected together with short wiring pattern.



Programming tool for Programmable Crystal Oscillator SG-Writer II



Product Number
Q91PR20W1102000



- Programming tool for programmable oscillator:
SG-8018, SG-8101, SG-9101, SG-8503, SG-8504 and
SG-8506 series (Blank sample)
- Able to program required frequency at customer side
- External power supply by USB cable
- Available PC OS: Windows 10, etc
- Small body and easy carry

Main Body Specifications

Name	SG-Writer II for programmable oscillator: SG-8018, SG-8101, SG-9101, SG-8503, SG-8504 and SG-8506 series					
Operating Temperature	+10 °C to +40 °C Writing (+25 °C ±5 °C)					
Electric Power Supply	Via USB					
Standard Interface	USB 2.0 (mini-B)					
Accessories	SG-Writer II, Instruction manual (Documents: English, Japanese)					
Software	SG-Writer II					
Option SG-Writer IC socket *1	Model		Product number			
	SG-8018CA, SG-8101CA, SG-9101CA		Q91PR10W00021			
	SG-8018CB, SG-8101CB, SG-9101CB		Q91PR10W00025			
	SG-8018CE, SG-8101CE, SG-9101CE		Q91PR10W00018			
	SG-8018CG, SG-8101CG, SG-9101CG		Q91PR10W00024			
	SG-8503CA		Q91PR10W00026			
	SG-8504CA		Q91PR10W00027			
	SG-8506CA		Q91PR10W00028			
Option Blank sample	Model		Product number	Model		Product number
	SG-8018CA		X1G0055710001	SG-8503CA		X1G0050111001
	SG-8018CB		X1G0055810001	SG-8504CA		X1G0050211001
	SG-8018CE		X1G0055910001	SG-8506CA		X1G0050311001
	SG-8018CG		X1G0056010001			
	SG-8101CB		X1G0052010001			
	SG-8101CE		X1G0052110001			
	SG-8101CG		X1G0051810001			
	SG-9101CA		X1G0053010001			
	SG-9101CB		X1G0053110001			
	SG-9101CE		X1G0053210001			
	SG-9101CG		X1G0052910001			

*1 If you have IC socket of either SG-8002CA/CE it can use for both SG-8018CA/CE, SG-8101CA/CE and SG-9101CA/CE.

Recommend PC Specifications (You need to connect the SG-Writer II to a PC when you are writing.)

Available PC OS	Windows 7 (32 bit, 64 bit), Windows Vista (32 bit), Windows XP (32 bit) Windows 10 (32 bit, 64 bit)
Recommend CPU	Over Pentium 4 1.4 GHz or equivalent Processor
Recommend memory Capacity	Windows XP : Over 512 MB Windows Vista, Windows7 32 bit : Over 1 GB Windows 7 64 bit : Over 2 GB Windows 10 : Over 2 GB
Other	USB cable (Type A ↔ mini-B) Need SMD *2 socket when you write SG-8000 SMD products. (Sold separately)

*2 Conventional IC socket for PROM Writer or SG-Writer (the previous model) can be used for SG-Writer II.

Precautions

- The Blank sample of each series can be identified by marking
- The firmware update is required to program SG-8018, SG-8101, SG-9101, SG-8503, SG-8504 and SG-8506 series.
For details please contact your supplier.

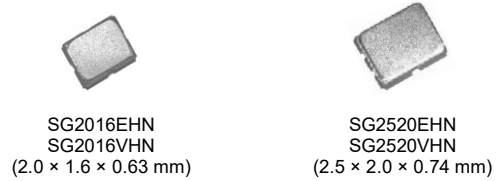
CRYSTAL OSCILLATOR (SPXO)
OUTPUT : LV-PECL, LVDS



Product Number
SG2016EHN: X1G006141xxxx15
SG2016VHN: X1G006121xxxx15
SG2520EHN: X1G005921xxxx15
SG2520VHN: X1G005941xxxx15

SG2016EHN/VHN
SG2520EHN/VHN

- Frequency range : 25 MHz to 500 MHz
- Supply voltage : 1.8 V Typ. (LVDS only) / 2.5 V Typ. / 3.3 V Typ.
- Frequency tolerance : $\pm 20 \times 10^{-6}$
- Operating temperature : -40 °C to +85 °C, -40 °C to +105 °C
- Function : Output enable (OE) or Standby (\overline{ST})
- Phase jitter : 50 fs Max. (391 MHz < fo ≤ 500 MHz, V_{CC} = 2.5 V, 3.3 V)



Specifications (characteristics)

Item	Symbol	Specifications		Conditions / Remarks	
		LV-PECL SG2016EHN / SG2520EHN	LVDS SG2016VHN / SG2520VHN		
Output frequency range	f _o	25 MHz to 500 MHz		Please contact us for available frequencies.	
Supply voltage	V _{CC}	C: 3.3 V ± 5 % D: 2.5 V ± 5 %	E: 1.8 V ± 5 %		
Storage temperature	T _{stg}	-55 °C to +125 °C			
Operating temperature	T _{use}	G: -40 °C to +85 °C, H: -40 °C to +105 °C			
Frequency tolerance	f _{tol}	C: $\pm 20 \times 10^{-6}$ Max.		Includes initial frequency tolerance, frequency / temperature characteristics, frequency / voltage coefficient and 10 years aging (+25 °C)	
Current consumption	I _{CC}	60 mA Max.	—	OE or \overline{ST} = V _{CC} , L ECL = 50 Ω 25 MHz ≤ fo < 212 MHz 212 MHz ≤ fo < 392 MHz 392 MHz ≤ fo ≤ 500 MHz Output option: A / B / C	
		—	25 mA / 30 mA / 25 mA Max. 28 mA / 35 mA / 28 mA Max. 28 mA / 35 mA / 30 mA Max.		25 mA / — / 25 mA Max.
Disable current	I _{dis}	35 mA Max.	20 mA Max.	OE = GND	
Stand-by current	I _{std}	30 μA Max.		\overline{ST} = GND, T _{use} Max. = +85 °C \overline{ST} = GND, T _{use} Max. = +105 °C	
		60 μA Max.			
Symmetry	SYM	45 % to 55 %		At output crossing point	
Output voltage (LV-PECL)	V _{OH} V _{OL}	V _{CC} - 1.1 V Min.	—	Output option: A, DC characteristic	
		V _{CC} - 1.5 V Max.	—		
Differential swing	V _{sw}	0.8 V to 2.0 V	500 mV to 900 mV	500 mV to 900 mV	Output option: A
		—	800 mV to 1 600 mV	—	Output option: B
		—	600 mV to 1 200 mV	600 mV to 1 200 mV	Output option: C
Output voltage (LVDS)	V _{OD}	—	250 mV to 450 mV	250 mV to 450 mV	Output option: A
		—	400 mV to 800 mV	—	Output option: B
		—	300 mV to 600 mV	300 mV to 600 mV	Output option: C
	dV _{OD}	—	50 mV Max.	—	Differential output voltage, V _{OD1} , V _{OD2}
	V _{OS}	—	1.15 V to 1.35 V	0.65 V to 0.85 V	Offset voltage, V _{OS1} , V _{OS2}
dV _{OS}	—	—	50 mV Max.	dV _{OS} = V _{OS1} - V _{OS2}	
Output load condition	L ECL	50 Ω	—	Terminated to V _{CC} - 2.0 V	
	L LVDS	—	100 Ω	Connected between OUT and \overline{OUT}	
Input voltage	V _{IH}	70 % V _{CC} Min.		OE or \overline{ST} terminal	
	V _{IL}	30 % V _{CC} Max.			
Rise/Fall times	tr/tf	0.35 ns Max.		LV-PECL: 20 % - 80 % (V _{OH} - V _{OL}) LVDS: 20 % - 80 % differential output peak to peak	
Start-up time	t _{str}	10 ms Max.		t = 0 at 90 % V _{CC}	
Phase jitter	t _{pj}	250 fs Max.	250 fs Max.	400 fs Max.	Offset frequency fo < 50 MHz: 12 kHz to 5 MHz fo ≥ 50 MHz: 12 kHz to 20 MHz
		90 fs Max.	100 fs Max.	130 fs Max.	
		70 fs Max.	60 fs Max.	70 fs Max.	
		60 fs Max.	—	—	
		50 fs Max.	50 fs Max.	60 fs Max.	

Product Name **SG2016 EHN 156.250000MHz C C H P Z A**

(Standard form) ① ② ③ ④⑤⑥⑦⑧⑨

- ① Model ② Output (E: LV-PECL, V: LVDS) ③ Frequency ④ Supply voltage ⑤ Frequency tolerance
 ⑥ Operating temperature ⑦ Function ⑧ Output disable type (Z: High impedance) ⑨ Output option

④ Supply voltage	⑤ Freq. tolerance
C 3.3 V Typ.	C $\pm 20 \times 10^{-6}$
D 2.5 V Typ.	
E* 1.8 V Typ.	

⑥ Operating temp.
G -40 °C to +85 °C
H -40 °C to +105 °C

⑦ Function
P OE
S \overline{ST}

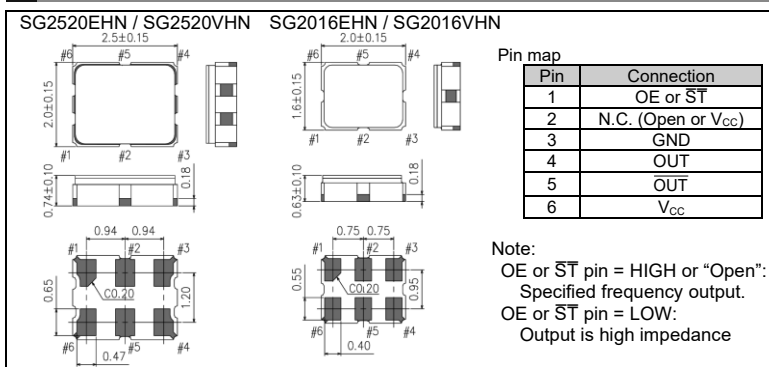
⑨ Output option	SG2016EHN / SG2520EHN	SG2016VHN / SG2520VHN
	A Default	V _{OD} = 250 mV to 450 mV
	B* —	V _{OD} = 400 mV to 800 mV
C —	V _{OD} = 300 mV to 600 mV	

E is only for SG2016VHN and SG2520VHN

*Not available for V_{CC} = 1.8 V Typ.

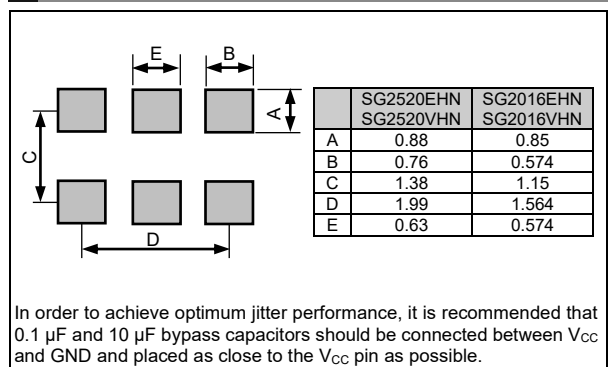
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



CRYSTAL OSCILLATOR (SPXO)
OUTPUT : HCSL



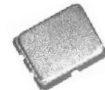
Product Number
SG2016HHN: X1G006231xxxx15
SG2520HHN: X1G005931xxxx15

SG2016HHN / SG2520HHN

- Frequency range : 25 MHz to 500 MHz
- Supply voltage : 2.5 V Typ. / 3.3 V Typ.
- Frequency tolerance : $\pm 20 \times 10^{-6}$
- Operating temperature : -40 °C to +85 °C, -40 °C to +105 °C
- Function : Output enable (OE) or Standby (\overline{ST})
- Phase jitter : 90 fs Max. (100 MHz < fo ≤ 156 MHz, Vcc = 2.5 V, 3.3 V)
- PCIe Gen5,6 Jitter specification compliant



SG2016HHN
(2.0 × 1.6 × 0.63 mm)



SG2520HHN
(2.5 × 2.0 × 0.74 mm)

Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
Output frequency range	fo	25 MHz to 500 MHz	Please contact us for available frequencies.
Supply voltage	Vcc	D: 2.5 V ± 5 %, C: 3.3 V ± 5 %	
Storage temperature	T_stg	-55 °C to +125 °C	
Operating temperature	T_use	G: -40 °C to +85 °C, H: -40 °C to +105 °C	
Frequency tolerance	f_tol	C: $\pm 20 \times 10^{-6}$ Max.	Includes initial frequency tolerance, frequency / temperature characteristics, frequency / voltage coefficient and 10 years aging (+25 °C)
Current consumption	Icc	35 mA Max. 40 mA Max.	25 MHz ≤ fo < 212 MHz 212 MHz ≤ fo < 500 MHz
Disable current	I_dis	25 mA Max. 30 µA Max.	OE = GND \overline{ST} = GND, T_use Max. = +85 °C
Stand-by current	I_std	60 µA Max.	\overline{ST} = GND, T_use Max. = +105 °C
Symmetry	SYM	45 % to 55 %	At output crossing point
Output voltage	VOH	0.5 V to 0.7 V	25 MHz ≤ fo < 212 MHz
		0.4 V to 0.65 V	212 MHz ≤ fo < 500 MHz
		0.6 V to 0.8 V	25 MHz ≤ fo < 212 MHz
		0.5 V to 0.75 V	212 MHz ≤ fo < 500 MHz
	VOL	-0.15 V to +0.15 V	
Differential swing	Vsw	0.7 V to 1.4 V	Output option: A
		0.8 V to 1.6 V	Output option: B
Crossing voltage	Vcr	0.25 V to 0.55 V	
Rise time / Fall time	tr/tf	0.7 ns Max.	20 % - 80 % (VOH - VOL)
Differential output rise slew rate / fall slew rate	Rr/Rf	2 V/ns to 10 V/ns	Between -0.15 V and 0.15 V of differential output
Output load condition	L_HCSL	50 Ω	
Input voltage	VIH	70 % Vcc Min.	OE or \overline{ST} terminal
	VIL	30 % Vcc Max.	
Output enable time	tsta_oe	500 ns Max.	t = 0 at OE = 70 % Vcc
	tsta_st	10 ms Max.	t = 0 at \overline{ST} = 70 % Vcc
Output disable time	tstp_oe	100 ns Max.	t = 0 at OE = 30 % Vcc
	tstp_st	100 ns Max.	t = 0 at \overline{ST} = 30 % Vcc
Start-up time	t_str	10 ms Max.	t = 0 at 90 % Vcc
Phase jitter	tpj	200 fs Max.	25 MHz ≤ fo < 100 MHz
		90 fs Max.	100 MHz ≤ fo ≤ 156 MHz
		70 fs Max.	156 MHz < fo ≤ 212 MHz
		60 fs Max.	212 MHz < fo ≤ 391 MHz
		50 fs Max.	391 MHz < fo ≤ 500 MHz
Jitter	tc-c	60 ps Max.	Cycle to cycle jitter (Peak to Peak)
PCIe jitter limits for CC architecture	-	0.1 ps Max.	For PCIe Gen5
		0.06 ps Max.	For PCIe Gen6

Product name

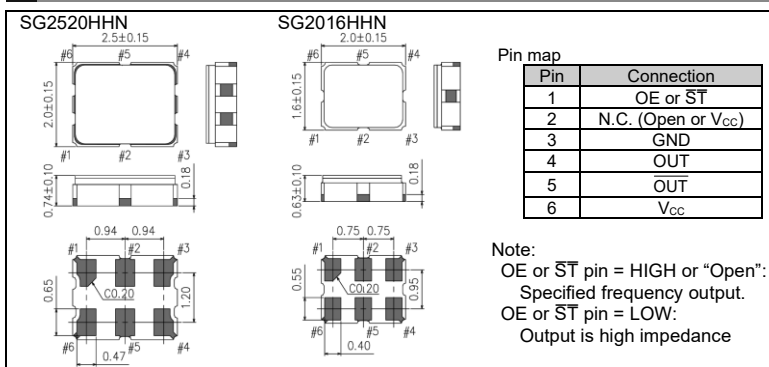
Product Name SG2016HHN 100.000000MHz C C H P Z A
 (Standard form) a b c d e f g h i

- a: Model b: Output (H: HCSL) c: Frequency d: Supply voltage e: Frequency tolerance
 f: Operating temperature g: Function h: Output disable type (Z: High impedance) i: Output option

d: Supply voltage	e: Freq. tolerance	f: Operating temp.	g: Function	i: Output option
C 3.3 V Typ.	C $\pm 20 \times 10^{-6}$	G -40 °C to +85 °C H -40 °C to +105 °C	P OE S \overline{ST}	A Vsw = 0.7 V to 1.4 V B Vsw = 0.8 V to 1.6 V

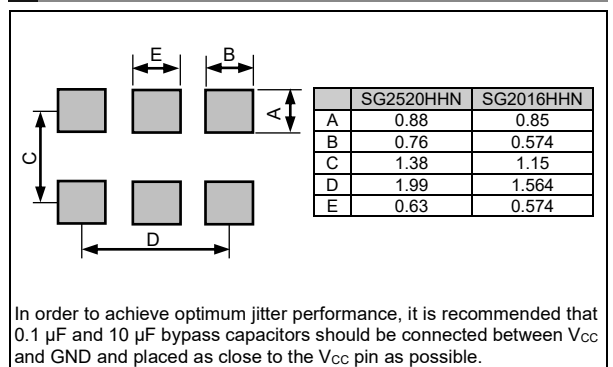
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



CRYSTAL OSCILLATOR (SPXO)

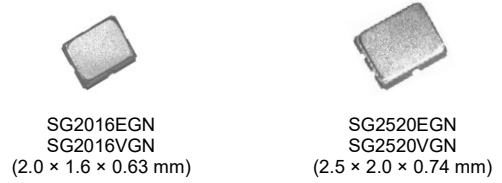
OUTPUT : LV-PECL, LVDS



Product Number
 SG2016EGN: X1G006131xxxx15
 SG2016VGN: X1G006111xxxx15
 SG2520EGN: X1G005881xxxx15
 SG2520VGN: X1G005901xxxx15

SG2016EGN / SG2520EGN
 SG2016VGN / SG2520VGN

- Frequency range : 25 MHz to 500 MHz
- Supply voltage : 1.8 V Typ. (LVDS only) / 2.5 V Typ. / 3.3 V Typ.
- Frequency tolerance : $\pm 25 \times 10^{-6}$; $\pm 50 \times 10^{-6}$
- Operating temperature : -40 °C to +85 °C, -40 °C to +105 °C
- Function : Output enable (OE) or Standby (\overline{ST})
- Phase jitter : 50 fs Max. (391 MHz < fo \leq 500 MHz, V_{CC} = 2.5 V, 3.3 V)



Specifications (characteristics)

Item	Symbol	Specifications		Conditions / Remarks	
		LV-PECL SG2016EGN / SG2520EGN	LVDS SG2016VGN / SG2520VGN		
Output frequency range	f _o	25 MHz to 500 MHz		Please contact us for available frequencies.	
Supply voltage	V _{CC}	C: 3.3 V \pm 5 % D: 2.5 V \pm 5 %	E: 1.8 V \pm 5 %		
Storage temperature	T _{stg}	-55 °C to +125 °C			
Operating temperature	T _{use}	G: -40 °C to +85 °C, H: -40 °C to +105 °C			
Frequency tolerance	f _{tol}	D: $\pm 25 \times 10^{-6}$ Max. J: $\pm 50 \times 10^{-6}$ Max.		Includes initial frequency tolerance, frequency / temperature characteristics, frequency / voltage coefficient and 10 years aging (+25 °C)	
Current consumption	I _{CC}	60 mA Max.	-	OE or \overline{ST} = V _{CC} , L ECL = 50 Ω 25 MHz \leq fo < 212 MHz 212 MHz \leq fo < 392 MHz 392 MHz \leq fo \leq 500 MHz Output option: A / B / C	
		-	25 mA / 30 mA / 25 mA Max. 28 mA / 35 mA / 28 mA Max. 28 mA / 35 mA / 30 mA Max.		25 mA / - / 25 mA Max.
Disable current	I _{dis}	35 mA Max.	20 mA Max.	OE = GND	
Stand-by current	I _{std}	30 μ A Max.		\overline{ST} = GND, T _{use} Max. = +85 °C \overline{ST} = GND, T _{use} Max. = +105 °C	
		60 μ A Max.			
Symmetry	SYM	45 % to 55 %		At output crossing point	
Output voltage (LV-PECL)	V _{OH} V _{OL}	V _{CC} - 1.1 V Min.	-	Output option: A, DC characteristic	
		V _{CC} - 1.5 V Max.	-		
Differential swing	V _{sw}	0.8 V to 2.0 V	500 mV to 900 mV	500 mV to 900 mV	
		-	800 mV to 1 600 mV	-	
		-	600 mV to 1 200 mV	600 mV to 1 200 mV	
Output voltage (LVDS)	V _{OD}	-	250 mV to 450 mV	250 mV to 450 mV	
		-	400 mV to 800 mV	-	
		-	300 mV to 600 mV	300 mV to 600 mV	
	dV _{OD}	-	50 mV Max.	dV _{OD} = V _{OD1} - V _{OD2}	
	V _{OS}	-	1.15 V to 1.35 V	0.65 V to 0.85 V	
dV _{OS}	-	50 mV Max.	50 mV Max.	dV _{OS} = V _{OS1} - V _{OS2}	
Output load condition	L ECL	50 Ω	-	Terminated to V _{CC} - 2.0 V	
	L LVDS	-	100 Ω	Connected between OUT and \overline{OUT}	
Input voltage	V _{IH}	70 % V _{CC} Min.		OE or \overline{ST} terminal	
	V _{IL}	30 % V _{CC} Max.			
Rise/Fall times	tr/tf	0.35 ns Max.		LV-PECL: 20 % - 80 % (V _{OH} - V _{OL}) LVDS: 20 % - 80 % differential output peak to peak	
Start-up time	t _{str}	10 ms Max.		t = 0 at 90 % V _{CC}	
Phase jitter	t _{pj}	250 fs Max.	250 fs Max.	400 fs Max.	Offset frequency fo < 50 MHz: 12 kHz to 5 MHz fo \geq 50 MHz: 12 kHz to 20 MHz
		90 fs Max.	100 fs Max.	130 fs Max.	
		70 fs Max.	60 fs Max.	70 fs Max.	
		60 fs Max.	50 fs Max.	60 fs Max.	
		50 fs Max.	50 fs Max.	60 fs Max.	

Product Name SG2016 EGN 156.250000MHz C D H P Z A

(Standard form) ① ② ③ ④⑤⑥⑦⑧⑨

- ①Model ②Output (E: LV-PECL, V: LVDS) ③Frequency ④Supply voltage ⑤Frequency tolerance
 ⑥Operating temperature ⑦Function ⑧Output disable type (Z: High impedance) ⑨Output option

④Supply voltage	⑤Freq. tolerance
C 3.3 V Typ.	D $\pm 25 \times 10^{-6}$
D 2.5 V Typ.	J $\pm 50 \times 10^{-6}$
E* 1.8 V Typ.	

⑥Operating temp.	⑦Function
G -40 °C to +85 °C	P OE
H -40 °C to +105 °C	S \overline{ST}

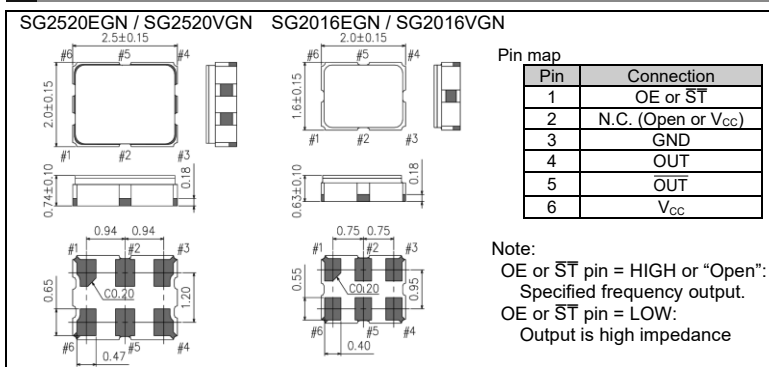
⑨Output option	SG2016EGN / SG2520EGN	SG2016VGN / SG2520VGN
A Default		V _{OD} = 250 mV to 450 mV
B*		V _{OD} = 400 mV to 800 mV
C		V _{OD} = 300 mV to 600 mV

*E is only for SG2016VGN and SG2520VGN

*Not available for V_{CC} = 1.8 V Typ.

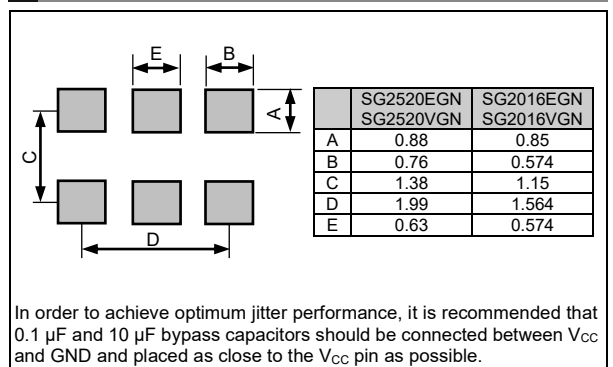
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



CRYSTAL OSCILLATOR (SPXO)

OUTPUT : LV-PECL, LVDS

SG3225EEN / VEN

SG5032EEN / VEN

SG7050EEN / VEN



Product Number
 SG3225EEN: X1G005221xxxx00 (fo ≤ 200 MHz)
 X1G005511xxxx00 (fo > 200 MHz)
 SG5032EEN: X1G005531xxxx00
 SG7050EEN: X1G005131xxxx00 (fo ≤ 200 MHz)
 X1G005551xxxx00 (fo > 200 MHz)
 SG3225VEN: X1G005351xxxx00 (fo ≤ 200 MHz)
 X1G005521xxxx00 (fo > 200 MHz)
 SG5032VEN: X1G005541xxxx00
 SG7050VEN: X1G005331xxxx00 (fo ≤ 200 MHz)
 X1G005561xxxx00 (fo > 200 MHz)

- Frequency range : 25 MHz to 500 MHz
- Supply voltage : 2.5 V Typ. / 3.3 V Typ.
- Output : LV-PECL or LVDS
- Function : Output enable (OE)
- Phase jitter : 50 fs Typ. (fo = 156.25 MHz, LV-PECL)
- Operating temperature : -40 °C to +105 °C



Specifications (characteristics)

Item	Symbol	Specifications		Conditions / Remarks	
		LV-PECL SG3225EEN / SG5032EEN / SG7050EEN	LVDS SG3225VEN / SG5032VEN / SG7050VEN		
Output frequency range	fo	25 MHz to 500 MHz		Except for SG5032EEN / SG5032VEN	Please contact us for available frequencies.
		200.1 MHz to 500 MHz		SG5032EEN / SG5032VEN	
Supply voltage	V _{CC}	D: 2.5 V ± 0.125 V, C: 3.3 V ± 0.165 V			
Storage temperature	T _{stg}	-55 °C to +125 °C			
Operating temperature	T _{use}	G: -40 °C to +85 °C, H: -40 °C to +105 °C			
Frequency tolerance	f _{tol}	D: ±25 × 10 ⁻⁶ Max.		Includes initial frequency tolerance, temperature variation, supply voltage change and 5 years aging (+25 °C)	
		J: ±50 × 10 ⁻⁶ Max.		Includes initial frequency tolerance, temperature variation, supply voltage change and 10 years aging (+25 °C)	
		L: ±100 × 10 ⁻⁶ Max.			
Current consumption	I _{CC}	60 mA Max.	25 mA Max.	OE = V _{CC} , L ECL = 50 Ω or L LVDS = 100 Ω	
Disable current	I _{dis}	25 mA Max.	15 mA Max.	OE = GND	
Symmetry	SYM	45 % to 55 %		At output crossing point	
Output voltage (LV-PECL)	V _{OH}	V _{CC} - 1.1 V Min.		DC characteristics	
	V _{OL}	V _{CC} - 1.5 V Max.			
Output voltage (LVDS)	V _{OD}	250 mV to 450 mV		Differential output voltage, V _{OD1} , V _{OD2}	
	dV _{OD}	50 mV Max.		dV _{OD} = V _{OD1} - V _{OD2}	
	V _{OS}	1.15 V to 1.35 V		Offset voltage, V _{OS1} , V _{OS2}	
	dV _{OS}	50 mV Max.		dV _{OS} = V _{OS1} - V _{OS2}	
Output load condition	L _{ECL}	50 Ω		Terminated to V _{CC} - 2.0 V	
	L _{LVDS}	100 Ω		Connected between OUT to OUT	
Input voltage	V _{IH}	70 % V _{CC} Min.		OE terminal	
	V _{IL}	30 % V _{CC} Max.			
Rise/Fall times	tr / tf	0.3 ns Max.		V _{CC} = 3.3 V, 25 MHz ≤ fo ≤ 200 MHz	LV-PECL: Between 20 % and 80 % of (V _{OH} - V _{OL}) LVDS: Between 20 % and 80 % of Differential Output peak to peak voltage
		0.35 ns Max.		All other	
Startup time	t _{str}	10 ms Max.		Time at minimum supply voltage to be 0 s	

Phase Jitter

Product Name	100 MHz	125 MHz	156.25 MHz	200 MHz	312.5 MHz	491.52 MHz	Conditions
SG3225EEN / SG5032EEN / SG7050EEN	75 fs Typ.	60 fs Typ.	50 fs Typ.	40 fs Typ.	30 fs Typ.	20 fs Typ.	Offset frequency: 12 kHz to 20 MHz
SG3225VEN / SG5032VEN / SG7050VEN	90 fs Typ.	70 fs Typ.	60 fs Typ.	50 fs Typ.	40 fs Typ.	30 fs Typ.	

Product Name SG3225 EEN 156.250000MHz C D G A (⑤⑥: Unavailable code DH, DG and JH at fo > 200 MHz, Refer to figure *1)

(Standard form) ① ② ③ ④⑤⑥⑦

*1 : Maximum T_{use} of operating range

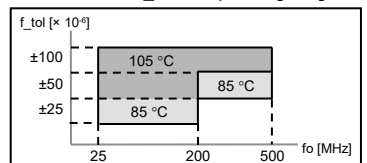
① Model ② Output (E: LV-PECL, V: LVDS) ③ Frequency ④ Supply voltage

⑤ Frequency tolerance ⑥ Operating temperature ⑦ Internal identification code("A" is default)

④ Supply voltage	
C	3.3 V Typ.
D	2.5 V Typ.

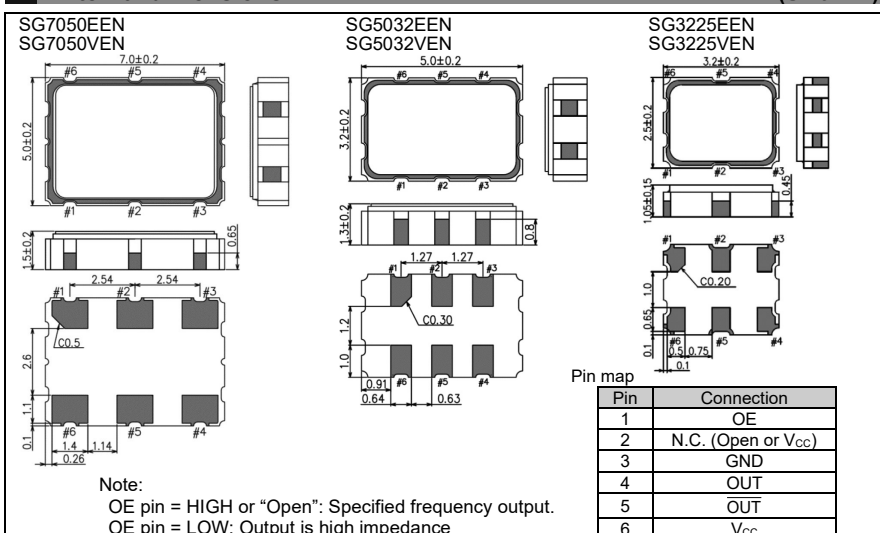
⑤ Frequency tolerance	
D	±25 × 10 ⁻⁶
J	±50 × 10 ⁻⁶
L	±100 × 10 ⁻⁶

⑥ Operating temperature	
G	-40 to +85 °C
H	-40 to +105 °C



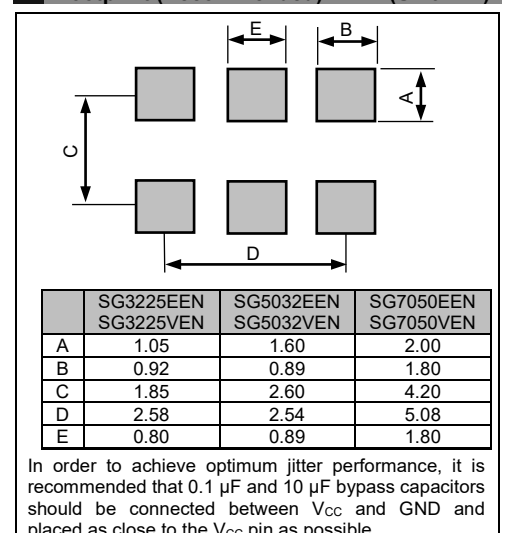
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



CRYSTAL OSCILLATOR (SPXO)
OUTPUT : HCSL



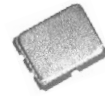
Product Number
SG2016HGN:X1G006221xxxx15
SG2520HGN:X1G005891xxxx15

SG2016HGN / SG2520HGN

- Frequency range : 25 MHz to 500 MHz
- Supply voltage : 2.5 V Typ. / 3.3 V Typ.
- Frequency tolerance : $\pm 25 \times 10^{-6}$, $\pm 50 \times 10^{-6}$
- Operating temperature : -40 °C to +85 °C, -40 °C to +105 °C
- Function : Output enable (OE) or Standby (\overline{ST})
- Phase jitter : 90 fs Max. (100 MHz < fo ≤ 156 MHz, Vcc = 2.5 V, 3.3 V)
- PCIe Gen5,6 Jitter specification compliant



SG2016HGN
(2.0 × 1.6 × 0.63 mm)



SG2520HGN
(2.5 × 2.0 × 0.74 mm)

Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
Output frequency range	fo	25 MHz to 500 MHz	Please contact us for available frequencies.
Supply voltage	Vcc	D: 2.5 V ± 5 %, C: 3.3 V ± 5 %	
Storage temperature	T_stg	-55 °C to +125 °C	
Operating temperature	T_use	G: -40 °C to +85 °C, H: -40 °C to +105 °C	
Frequency tolerance	f_tol	D: $\pm 25 \times 10^{-6}$ Max. J: $\pm 50 \times 10^{-6}$ Max.	Includes initial frequency tolerance, frequency / temperature characteristics, frequency / voltage coefficient and 10 years aging (+25 °C)
Current consumption	Icc	35 mA Max. 40 mA Max.	25 MHz ≤ fo < 212 MHz 212 MHz ≤ fo < 500 MHz
Disable current	I_dis	25 mA Max.	OE = GND
Stand-by current	I_std	30 µA Max. 60 µA Max.	\overline{ST} = GND, T_use Max. = +85 °C \overline{ST} = GND, T_use Max. = +105 °C
Symmetry	SYM	45 % to 55 %	At output crossing point
Output voltage	VOH	0.5 V to 0.7 V	25 MHz ≤ fo < 212 MHz
		0.4 V to 0.65 V	212 MHz ≤ fo < 500 MHz
		0.6 V to 0.8 V	25 MHz ≤ fo < 212 MHz
VOL	0.5 V to 0.75 V	212 MHz ≤ fo < 500 MHz	Output option: B
	-0.15 V to +0.15 V		
Differential swing	Vsw	0.7 V to 1.4 V	Output option: A
		0.8 V to 1.6 V	Output option: B
Crossing voltage	Vcr	0.25 V to 0.55 V	
Rise time / Fall time	tr/tf	0.7 ns Max.	20 % - 80 % (VOH - VOL)
Differential output rise slew rate / fall slew rate	Rr/Rf	2 V/ns to 10 V/ns	Between -0.15 V and 0.15 V of differential output
Output load condition	L_HCSL	50 Ω	
Input voltage	Vih	70 % Vcc Min.	OE or \overline{ST} terminal
	Vil	30 % Vcc Max.	
Output enable time	tsta_oe	500 ns Max.	t = 0 at OE = 70 % Vcc
	tsta_st	10 ms Max.	t = 0 at \overline{ST} = 70 % Vcc
Output disable time	tstp_oe	100 ns Max.	t = 0 at OE = 30 % Vcc
	tstp_st	100 ns Max.	t = 0 at \overline{ST} = 30 % Vcc
Start-up time	t_str	10 ms Max.	t = 0 at 90 % Vcc
Phase jitter	tpj	200 fs Max.	25 MHz ≤ fo < 100 MHz
		90 fs Max.	100 MHz ≤ fo ≤ 156 MHz
		70 fs Max.	156 MHz < fo ≤ 212 MHz
		60 fs Max.	212 MHz < fo ≤ 391 MHz
		50 fs Max.	391 MHz < fo ≤ 500 MHz
Jitter	tc-c	60 ps Max.	Cycle to cycle jitter (Peak to Peak)
		0.1 ps Max.	For PCIe Gen5
PCIe jitter limits for CC architecture	-	0.06 ps Max.	For PCIe Gen6

Product name

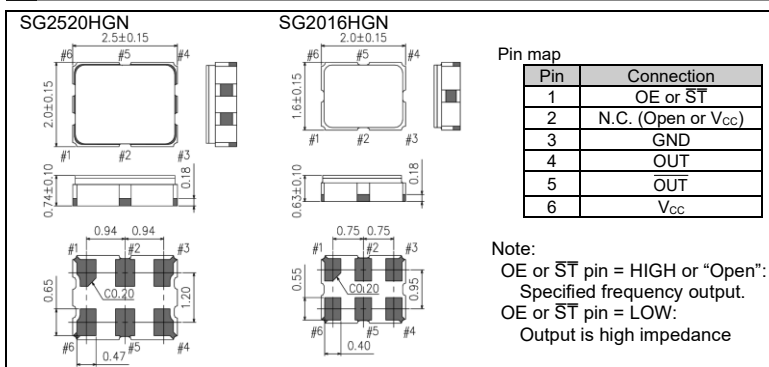
Product Name **SG2016 HGN 100.000000MHz C D H P Z A**
 (Standard form) a b c d e f g h i

- a: Model b: Output (H: HCSL) c: Frequency d: Supply voltage e: Frequency tolerance
 f: Operating temperature g: Function h: Output disable type (Z: High impedance) i: Output option

d: Supply voltage	e: Freq. tolerance	f: Operating temp.	g: Function	i: Output option
C 3.3 V Typ.	D $\pm 25 \times 10^{-6}$	G -40 °C to +85 °C	P OE	A Vsw = 0.7 V to 1.4 V
D 2.5 V Typ.	J $\pm 50 \times 10^{-6}$	H -40 °C to +105 °C	S \overline{ST}	B Vsw = 0.8 V to 1.6 V

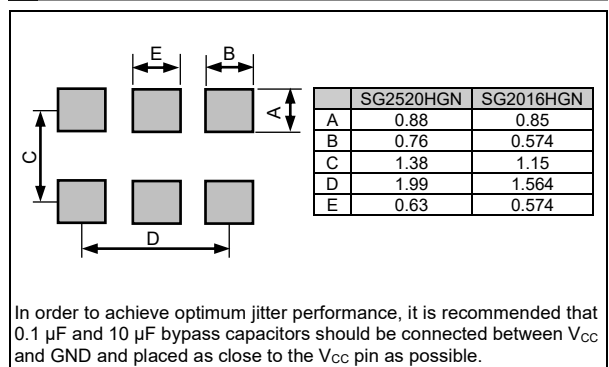
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



CRYSTAL OSCILLATOR (SPXO)

OUTPUT : LV-PECL, LVDS

SG3225EAN / VAN
SG5032EAN / VAN
SG7050EAN / VAN



Product Number
SG3225EAN: X1G004251xxxx00
SG3225VAN: X1G004241xxxx00
SG5032EAN: X1G004271xxxx00
SG5032VAN: X1G004261xxxx00
SG7050EAN: X1G004291xxxx00
SG7050VAN: X1G004281xxxx00



SG3225EAN/VAN
(3.2 × 2.5 × 1.05 mm)



SG5032EAN/VAN
(5.0 × 3.2 × 1.0 mm)



SG7050EAN/VAN
(7.0 × 5.0 × 1.4 mm)

- Achieved wide frequency range by PLL technology and Fundamental AT crystal units
- Frequency range : 73.5 MHz to 700 MHz
- Supply voltage : 2.5 V to 3.3 V
- Function : Output enable (OE)
- Output : LV-PECL or LVDS

Specifications (characteristics)

Item	Symbol	Specifications		Conditions / Remarks
		LV-PECL SG3225EAN / SG5032EAN / SG7050EAN	LVDS SG3225VAN / SG5032VAN / SG7050VAN	
Output frequency range	f _o	73.5 MHz to 700 MHz		Please contact us about available frequencies.
Supply voltage	V _{cc}	K: 2.5 V - 10 % to 3.3 V + 10 %		
Storage temperature	T _{stg}	-40 °C to +125 °C		Storage as single product.
Operating temperature	T _{use}	B: -20 °C to +70 °C, G: -40 °C to +85 °C		
Frequency tolerance	f _{tol}	J: ±50 × 10 ⁻⁶ , E: ±30 × 10 ⁻⁶ , C: ±20 × 10 ⁻⁶		
Current consumption	I _{cc}	65 mA Max.	30 mA Max.	OE = V _{cc} , L _{ECL} = 50 Ω or L _{LVDS} = 100 Ω
Disable current	I _{dis}	20 mA Max.		OE = GND
Symmetry	SYM	45 % to 55 %		At outputs crossing point
Output voltage (LV-PECL)	V _{OH}	V _{cc} - 1.0 V to V _{cc} - 0.8 V		DC characteristics
	V _{OL}	V _{cc} - 1.78 V to V _{cc} - 1.62 V		
Output voltage (LVDS)	V _{OD}	250 mV to 450 mV		DC characteristics
	dV _{OD}	50 mV Max.		
	V _{OS}	1.15 V to 1.35 V		
	dV _{OS}	150 mV Max.		
Output load condition (ECL) / (LVDS)	L _{ECL}	50 Ω		Terminated to V _{cc} - 2.0 V
	L _{LVDS}	100 Ω		Connected between OUT to OUT
Input voltage	V _{IH}	70 % V _{cc} Min.		OE terminal
	V _{IL}	30 % V _{cc} Max.		
Rise time / Fall time	t _r / t _f	350 ps Max.	300 ps Max.	LV-PECL: Between 20 % and 80 % of (V _{OH} -V _{OL}). LVDS: Between 20 % and 80 % of Differential Output peak to peak voltage
Start-up time	t _{str}	3 ms Max.		Time at minimum supply voltage to be 0 s
Phase Jitter	t _{pj}	0.6 ps Max.*1		Offset frequency: 12 kHz to 20 MHz
Frequency aging	f _{age}	±5 × 10 ⁻⁶ / year Max.		+25 °C, First year, V _{cc} = 2.5 V, 3.3 V

*1 0.9 ps Max. (f_o = 243 MHz ~ 250 MHz, 486 MHz ~ 500 MHz)

Product Name **SG3225 E AN 156.250000MHz K J G A** (5)(6): CG is not available
(Standard form) ① ② ③ ④⑤⑥⑦

- ① Model ② Output (E: LV-PECL, V: LVDS) ③ Frequency ④ Supply voltage ⑤ Frequency tolerance
⑥ Operating temperature ⑦ Internal identification code ("A" is default)

④ Supply voltage	
K	2.5 V ~ 3.3 V

⑤ Frequency tolerance	
J	±50 × 10 ⁻⁶
E	±30 × 10 ⁻⁶
C	±20 × 10 ⁻⁶

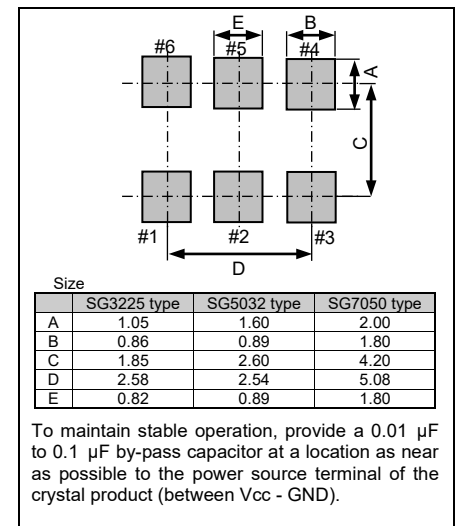
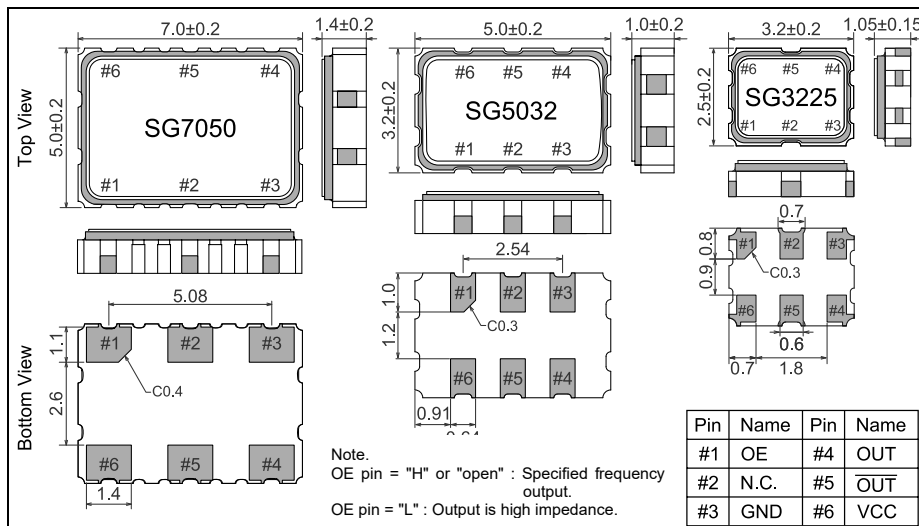
⑥ Operating temperature	
B	-20 °C ~ +70 °C
G	-40 °C ~ +85 °C

External dimensions

(Unit: mm)

Footprint (Recommended)

(Unit: mm)



CRYSTAL OSCILLATOR (SPXO)

OUTPUT : HCSL

Product Number
X1G005141xxxx00

SG3225HBN

- Frequency range : 100 MHz to 325 MHz
- Supply voltage : 2.5V, 3.3 V
- Output : HCSL
- Function : Output enable (OE)
- External dimensions : 3.2 × 2.5 × 1.05 mm
- Phase jitter : 85 fs Typ (fo = 156.25MHz)



Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
Output frequency range	fo	100 MHz to 325 MHz	Please contact us for inquiries regarding available frequencies.
Supply voltage	V _{CC}	D : 2.5 V ± 0.125 V , C : 3.3 V ± 0.165 V	
Storage temperature	T _{stg}	-55 °C to +125 °C	Store as bare product.
Operating temperature	T _{use}	G: -40 °C to +85 °C, H: -40 °C to +105 °C	
Frequency tolerance	f _{tol}	J: ± 50 × 10 ⁻⁶ (Not available H : -40 °C to +105 °C) L: ± 100 × 10 ⁻⁶	Includes initial frequency tolerance, temperature variation, supply voltage change and 10 years aging(+25 °C)
Current consumption	I _{CC}	25 mA Typ. 35 mA Max.	OE= V _{CC} , with output load
Disable current	I _{dis}	15 mA Max.	OE=GND
Symmetry	SYM	45 % to 55 %	At outputs crossing point
Output voltage	V _{OH}	0.75 V Typ., 0.66 V to 0.85 V	DC characteristics, single output
	V _{OL}	0 V Typ., -0.15 V to 0.15 V	
Crossing voltage	V _{CR}	0.25 V to 0.55 V	
Output load condition	L HCSL	50 Ω	
	R _s	33 Ω	
Input voltage	V _{IH}	70 % V _{CC} Min.	OE terminal
	V _{IL}	30 % V _{CC} Max.	
differential output rise slew rate/ fall slew rate	R _r / R _f	1 V/ns to 4 V/ns	Between -0.15 V and 0.15 V of differential output
Start-up time	t _{str}	10 ms Max.	Time at minimum supply voltage to be 0 s

Phase Jitter

Phase Jitter [fs] (Offset Frequency 12k to 20MHz)	Output frequency	100 MHz	125 MHz	156.25 MHz	200 MHz	322.265625 MHz	Supply voltage
	Typ.	110	95	85	75	65	V _{CC} =3.3V±0.165V
Max.	180	160	140	125	110		

Product Name **SG3225 HBN 156.250000MHz C J G A** (⑤⑥: Not Available code JH)

(Standard form)

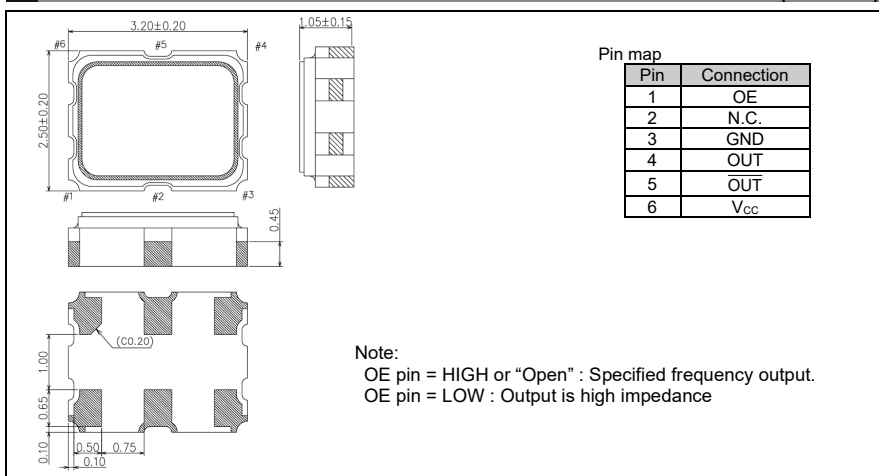
① Model ② Output (H: HCSL) ③ Frequency ④ Supply voltage (D: 2.5 V Typ. , C: 3.3 V Typ.)

⑤ Frequency tolerance (J: ±50 × 10⁻⁶ L: ±100 × 10⁻⁶)

⑥ Operating temperature (G:-40 to +85°C, H:-40 to +105°C) ⑦ Internal identification code("A" is default)

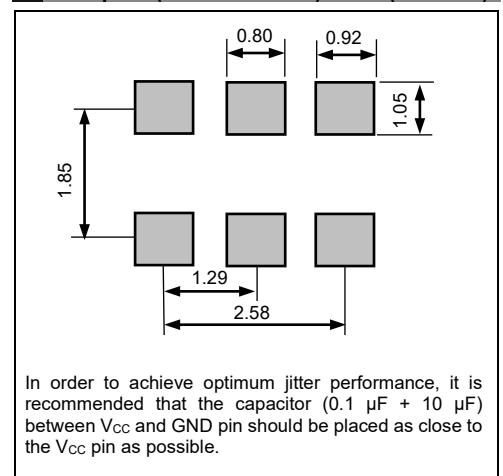
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



**I²C-Bus Programmable Crystal Oscillator (SPXO)**

Output: LV-PECL

SG-8506CA

- Frequency range : 50 MHz to 800 MHz
- Supply voltage : 2.5 V to 3.3 V
- External dimensions : 7.0 × 5.0 × 1.5 mm (8 pins)

Features

- User-specified one startup frequency, 7-bit I²C
- User Programming: I²C Interface
- High frequency fundamental tone crystal, Low jitter PLL technology
- Available field oscillator programmer "SG-Writer II"

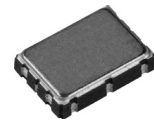
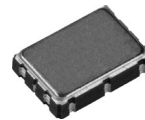
Application

- OTN, BTS, Test Instrument

*The I²C-Bus is a trademark of
NXP Semiconductors



Product Number
X1G005031xxxx00

**Specifications (characteristics)**

Item	Symbol	Specifications	Conditions / Remarks
Output frequency range	f _o	50 MHz to 800 MHz	It can be changed by I ² C
Supply voltage	V _{CC}	2.5 V - 0.125 V to 3.3 V + 0.33 V	-
Storage temperature range	T _{stg}	-55 °C to +125 °C	Store as bare product after packing
Operating temperature range	T _{use}	-40 °C to +85 °C	-
Frequency tolerance *1	f _{tol}	K : ±31.5 × 10 ⁻⁶	Customized Product (Option)
		L : ±50 × 10 ⁻⁶	
Current consumption	I _{CC}	90 mA Max.	OE Active, L_ECL=50 Ω
Disable current	I _{dis}	40 mA Max.	OE Inactive, Output Standby: Hi-Z mode
		70 mA Max.	OE Inactive, Output Standby: Fix mode
Symmetry	SYM	45 % to 55 %	At outputs crossing point
Output voltage	V _{OH}	V _{CC} - 1.025 V Min.	DC characteristics
	V _{OL}	V _{CC} - 1.62 V Max.	
Output load condition	L_ECL	50 Ω	Termination to V _{CC} - 2.0 V
Input voltage	V _{IH}	70% V _{CC} Min.	OE, SDA and SCL
	V _{IL}	30% V _{CC} Max.	
Rise time / Fall time	tr / tf	400 ps Max.	Between 20% and 80% of (V _{OH} - V _{OL})
Start-up time	t _{str}	10 ms Max.	Time at minimum supply voltage to be 0 s
Setting time for frequency change	t _{SET1}	1.5 ms Max.	From setting NEW_FREQ bit to output new frequency

*1 Frequency tolerance includes initial frequency tolerance, temperature variation, supply voltage change, reflow drift and 10 years aging at +25 °C.

Product Name SG-8506 CA 156.2MHz 0x37 A P R L Z
(Standard form) ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨

① Model, ② Package type,

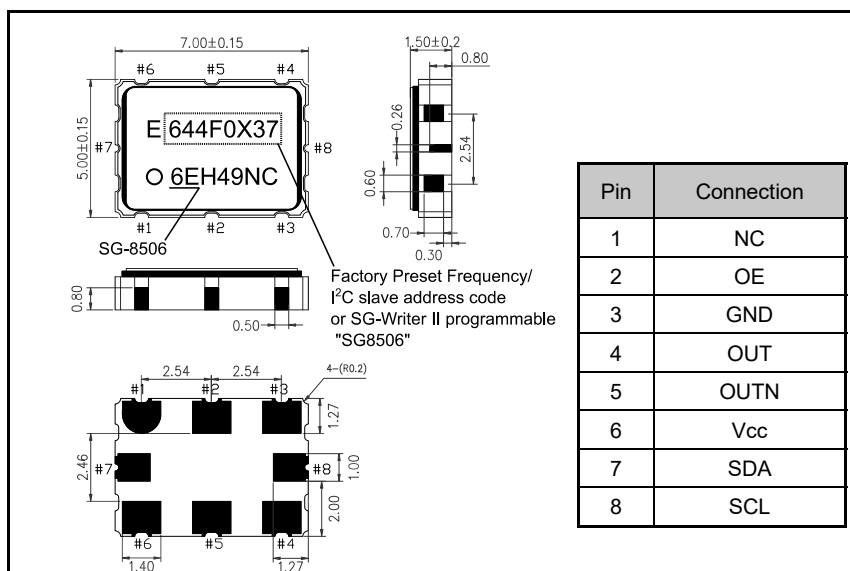
③ Power-on default output frequency (50 ~ 800 MHz), ④ I²C slave address, ⑤ Internal crystal frequency,

⑥ Output enable pin Polarity, ⑦ Supply voltage/Output format, ⑧ Frequency tolerance/Operating temperature, ⑨ Output standby type

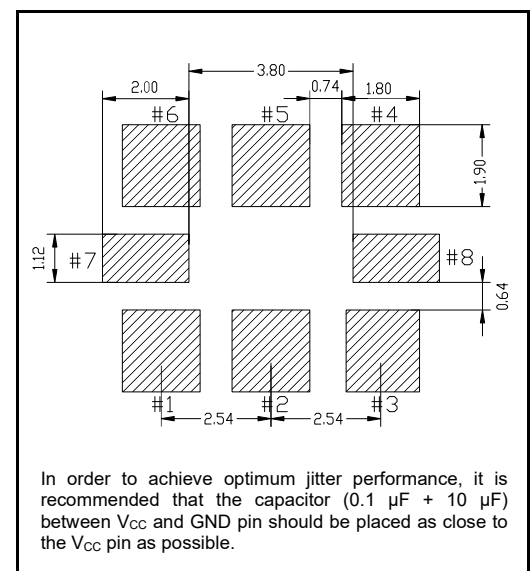
⑤ Internal crystal frequency	⑥ Output enable pin Polarity	⑦ Supply voltage/Output format	⑧ Frequency tolerance/Operating temperature	⑨ Output standby type
A 114.1444 MHz	P Active High Q Active Low	R 2.5 V ~ 3.3 V/LVPECL	K ±31.5 × 10 ⁻⁶ /-40 to +85 °C L ±50 × 10 ⁻⁶ /-40 to +85 °C	F Fix (OUT="L", OUTN="H") Z High-Z

External dimensions

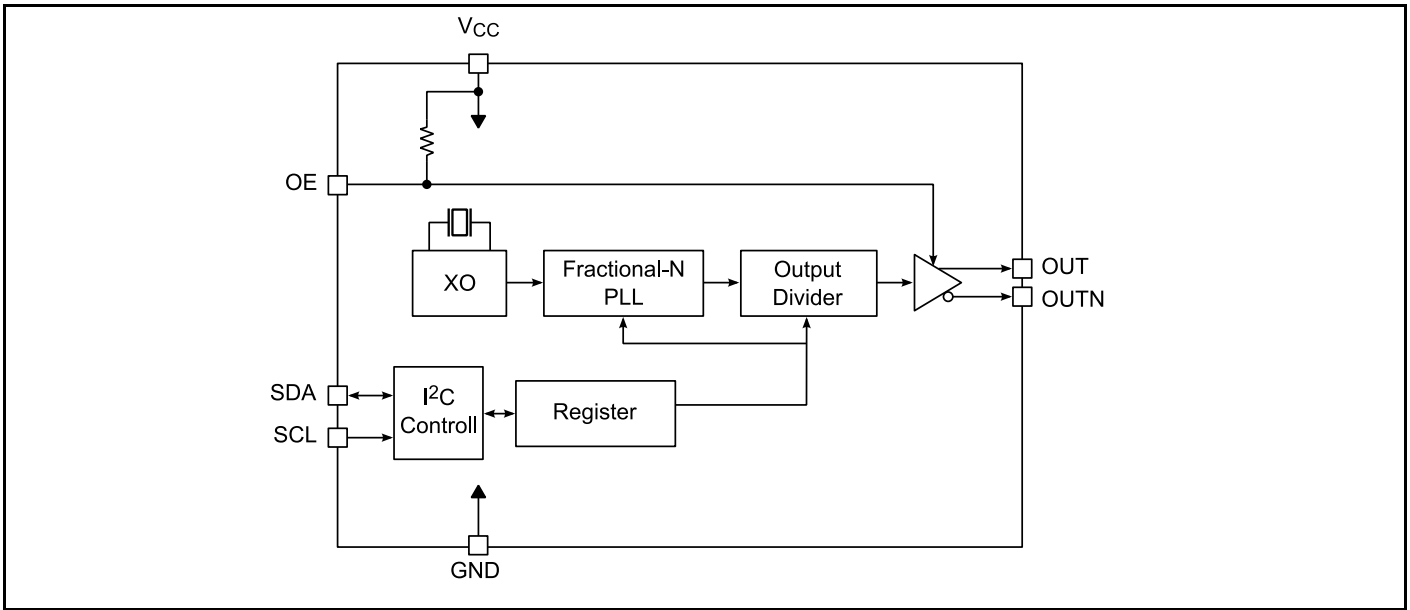
(Unit: mm)

**Footprint (Recommended)**

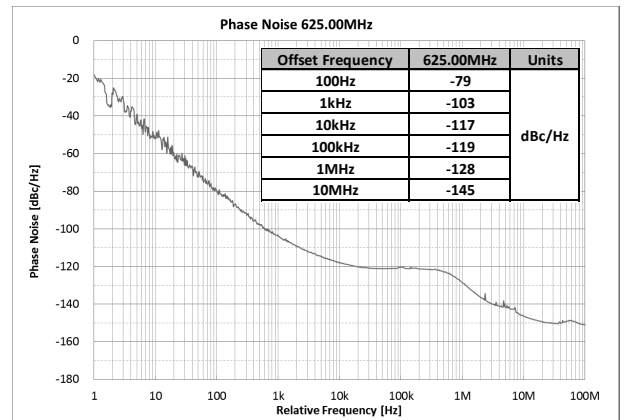
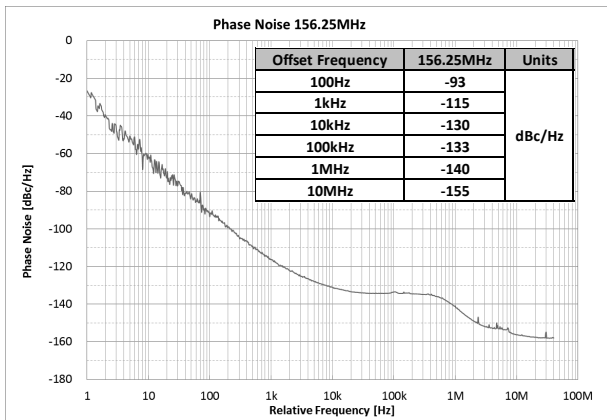
(Unit: mm)



Block diagram



Phase Noise



Phase Jitter

	Offset Frequency	100.00 MHz	125.00 MHz	156.25 MHz	250.00 MHz	312.50 MHz	500.00 MHz	625.00 MHz
Phase jitter *2 Typ.	12 kHz to 20 MHz	0.31 ps	0.30 ps	0.26 ps	0.26 ps	0.29 ps	0.28 ps	0.29 ps

*2 In order to achieve optimum jitter performance, it is recommended that the capacitor (0.1 μF + 10 μF) between V_{CC} and GND pin should be placed as close to the V_{CC} pin as possible.



Dual or Quad Selectable Programmable Crystal Oscillator Output: LV-PECL

SG-8503CA / SG-8504CA

- Dual frequency Selectable: SG-8503CA, 7.0 × 5.0 × 1.5 mm (6 pins)
- Quad frequency Selectable: SG-8504CA, 7.0 × 5.0 × 1.5 mm (8 pins)
- Frequency range: 50 MHz to 800 MHz
- Supply voltage: 2.5 V to 3.3 V

Features

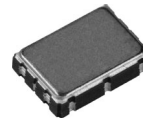
- User-specified two (FSEL) or four (FSEL0, FSEL1) startup frequencies
- High frequency fundamental tone crystal, Low jitter PLL technology
- Available field oscillator programmer "SG-Writer II"

Application

- OTN, BTS, Test Instrument



Product Number
SG-8503CA : X1G005011xxxx00
SG-8504CA : X1G005021xxxx00



Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
Output frequency range	f _o	50 MHz to 800 MHz	-
Supply voltage	V _{CC}	2.5 V - 0.125 V to 3.3 V + 0.33 V	-
Storage temperature range	T _{stg}	-55 °C to +125 °C	Store as bare product after packing
Operating temperature range	T _{use}	-40 °C to +85 °C	-
Frequency tolerance *1	f _{tol}	K : ±31.5 × 10 ⁻⁶ L : ±50 × 10 ⁻⁶	Customized Product (Option)
Current consumption	I _{CC}	90 mA Max.	OE Active, L _{ECL} = 50 Ω
Disable current	I _{dis}	40 mA Max.	OE Inactive, Output Standby: Hi-Z mode
		70 mA Max.	OE Inactive, Output Standby: Fix mode
Symmetry	SYM	45 % to 55 %	At outputs crossing point
Output voltage	V _{OH}	V _{CC} - 1.025 V Min.	DC characteristics
	V _{OL}	V _{CC} - 1.62 V Max.	
Output load condition	L _{ECL}	50 Ω	Termination to V _{CC} - 2.0 V
Input voltage	V _{IH}	70% V _{CC} Min.	SG-8503CA : OE, FSEL
	V _{IL}	30% V _{CC} Max.	SG-8504CA : OE, FSEL0, FSEL1
Rise time / Fall time	t _r / t _f	400 ps Max.	Between 20% and 80% of (V _{OH} - V _{OL})
Start-up time	t _{str}	10 ms Max.	Time at minimum supply voltage to be 0 s
Setting time for frequency change	t _{SET1}	1.5 ms Max.	SG-8503CA : From setting FSEL pin to output new frequency SG-8504CA : From setting FSEL0 / FSEL1 pin to output new frequency

*1 Frequency tolerance includes initial frequency tolerance, temperature variation, supply voltage change, reflow drift and 10 years aging at +25 °C.

Product Name SG-8503 CA 156MHz 625MHz A P R L Z
(Standard form) ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨

① Model, ② Package type,

③ Frequency-0 (50 ~ 800 MHz), ④ Frequency-1 (50 ~ 800 MHz), ⑤ Internal crystal frequency, ⑥ Output enable pin Polarity,

⑦ Supply voltage/Output format, ⑧ Frequency tolerance/Operating temperature, ⑨ Output standby type

Product Name SG-8504 CA 156.2MHz nnnn A P R L Z
(Standard form) ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨

① Model, ② Package type,

③ Frequency-0 (50 ~ 800 MHz), ④ Parameter identifier, ⑤ Internal crystal frequency, ⑥ Output enable pin Polarity,

⑦ Supply voltage/Output format, ⑧ Frequency tolerance/Operating temperature, ⑨ Output standby type

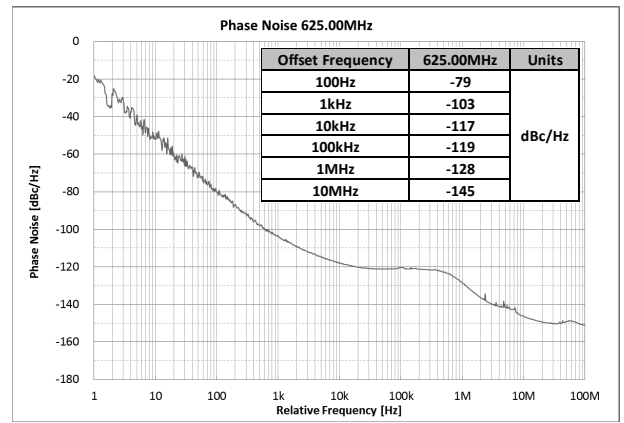
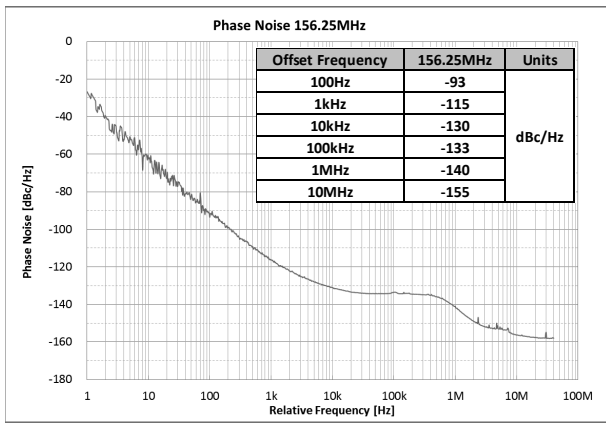
⑤ Internal crystal frequency	⑥ Output enable pin Polarity	⑦ Supply voltage/ Output format	⑧ Frequency tolerance/ Operating temperature	⑨ Output standby type
A 114.1444 MHz	P Active High Q Active Low	R 2.5 V ~ 3.3 V/LVPECL	K ±31.5 × 10 ⁻⁶ /-40 to +85 °C L ±50 × 10 ⁻⁶ /-40 to +85 °C	F Fix (OUT="L", OUTN="H") Z High-Z

Phase Jitter

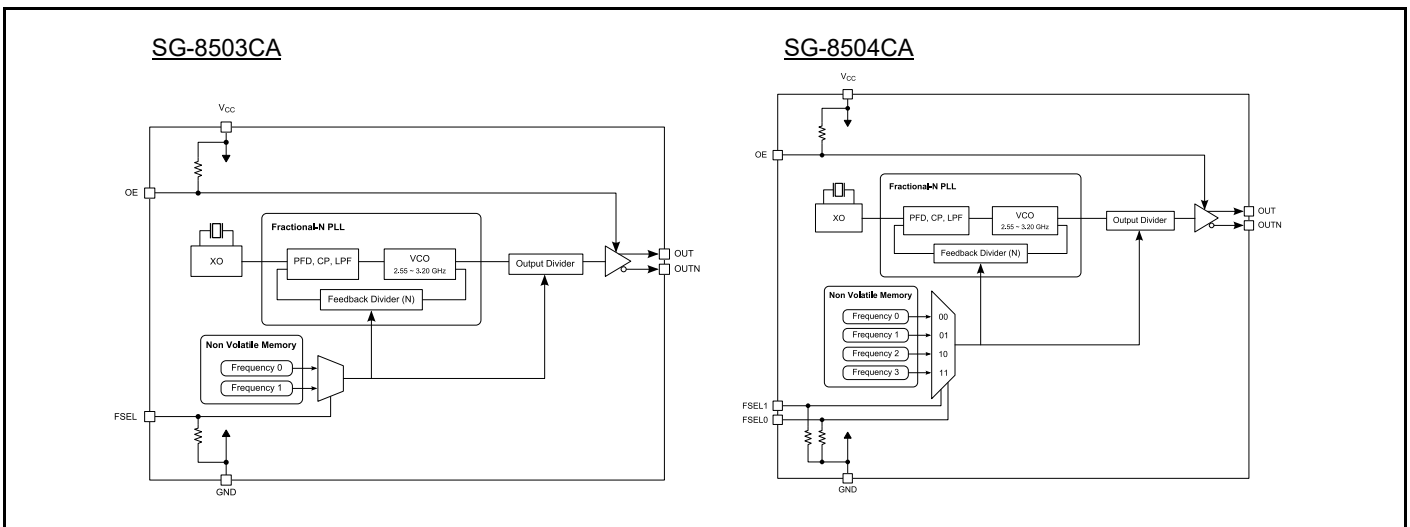
	Offset Frequency	100.00 MHz	125.00 MHz	156.25 MHz	250.00 MHz	312.50 MHz	500.00 MHz	625.00 MHz
Phase jitter *2 Typ.	12 kHz to 20 MHz	0.31 ps	0.30 ps	0.26 ps	0.26 ps	0.29 ps	0.28 ps	0.29 ps

*2 In order to achieve optimum jitter performance, it is recommended that the capacitor (0.1 μF + 10 μF) between V_{CC} and GND pin should be placed as close to the V_{CC} pin as possible.

Phase Noise



Block diagram



External dimensions

(Unit: mm)

Footprint (Recommended)(Unit: mm)

SG-8503CA

Factory Preset Frequency code or SG-Writer II programmable "SG8503"

Pin	Connection
1	OE
2	FSEL (L = Frequency-0, H = Frequency-1)
3	GND
4	OUT
5	OUTN
6	Vcc

SG-8504CA

Factory Preset Frequency code or SG-Writer II programmable "SG8504"

Pin	Connection	Pin	Connection
1	NC	5	OUTN
2	OE	6	Vcc
3	GND	7	FSEL0
4	OUT	8	FSEL1


FSEL1, FSEL0	Output Frequency
LL	Frequency-0
LH	Frequency-1
HL	Frequency-2
HH	Frequency-3

SG-8503CA

SG-8504CA

In order to achieve optimum jitter performance, it is recommended that the capacitor (0.1 μF + 10 μF) between Vcc and GND pin should be placed as close to the Vcc pin as possible.

High Precision Oscillator (TCXO / VC-TCXO)

Size [mm]	Clipped sine wave	CMOS output
	3.2 x 2.5 mm	
2.5 x 2.0 mm	<p>For Industrial / Consumer (+85 °C Operation, H-Shape)</p> <ul style="list-style-type: none"> - 2.5 x 2.0 mm: TG2520SMN - 2.0 x 1.6 mm: TG2016SMN <p>18 Standard frequencies: 16 / 16.368 / 16.369 / 19.2 / 20 / 24 / 25 / 25.6 / 26 / 27 / 27.6 / 30 / 32 / 38.4 / 40 / 48 / 50 / 52 MHz</p>	
2.0 x 1.6 mm	<p>For Automotive (AEC-Q100, Single-seal) </p> <p>Operating Temp. Max. SKA: +105 °C / SLA: +85 °C</p> <ul style="list-style-type: none"> - 2.0 x 1.6 mm: TG2016SKA 26 / 49.58 MHz - 2.0 x 1.6 mm: TG2016SLA 26 / 49.58 MHz 	
1.6 x 1.2 mm	<p>For Industrial / Consumer (+105 °C Operation, ST-Function, Single-seal)</p> <ul style="list-style-type: none"> - 2.0 x 1.6 mm: TG2016SLN 26 / 38.4 MHz - 1.6 x 1.2 mm: TG1612SLN 26 MHz 	

► Output: Clipped sine wave

P	Model	Size [mm]	Nominal frequency range	Frequency tolerance (× 10 ⁻⁶)	Frequency / Temperature Characteristics (× 10 ⁻⁶)	Operating temperature [°C]	Supply voltage [V]	Current consumption Max. [mA]	Output load condition	Output control
82	TG2016SMN	2.0 × 1.6 × 0.73t	10 MHz — 55 MHz	±1.5	±0.5	-40 to +85	1.8 ± 0.1 2.8 ± 5 % 3.0 ± 5 % 3.3 ± 5 %	1.5 (≤ 26 MHz) 1.8 (≤ 40 MHz) 2.0 (≤ 50 MHz) 2.1 (≤ 55 MHz)	10 kΩ//10 pF	-
	TG2520SMN	2.5 × 2.0 × 0.8t								
93	TG1612SLN	1.6 × 1.2 × 0.45t	13 MHz — 55.2 MHz	±2.0	±0.5 (≤ 85 °C) ±5.0 (≤ 105 °C)	-40 to +105	1.8 ± 0.1 2.8 ± 5 % 3.0 ± 5 % 3.3 ± 5 %	1.7 (≤ 26 MHz) 2.0 (≤ 38.4 MHz) 2.5 (≤ 55.2 MHz)	10 kΩ//10 pF	ST
84	TG2016SLN	2.0 × 1.6 × 0.7t	10 MHz — 55.2 MHz							
134	TG2016SKA (AEC-Q100)	2.0 × 1.6 × 0.7t	13 MHz — 55 MHz	±2.0	±0.5	-40 to +105	1.8 ± 0.1 3.3 ± 5 %	2.0 (≤ 40 MHz) 2.5 (≤ 55 MHz)	10 kΩ//10 pF	ST
135	TG2016SLA (AEC-Q100)	2.0 × 1.6 × 0.7t	13 MHz — 55 MHz	±2.0	±0.5	-40 to +85	1.8 ± 0.1 3.3 ± 5 %	2.0 (≤ 40 MHz) 2.5 (≤ 55 MHz)	10 kΩ//10 pF	ST
Web site	TG-5006CJ	2.0 × 1.6 × 0.73t	13 MHz — 52 MHz	±2.0	±0.5	-30 to +85	1.8 ± 0.1 2.8 ± 5 % 3.0 ± 5 % 3.3 ± 5 %	1.5 (≤ 26 MHz) 2.0 (> 26 MHz)	10 kΩ//10 pF	-
	TG-5006CG	2.5 × 2.0 × 0.8t								
	TG-5006CE	3.2 × 2.5 × 0.9t	13 MHz 20 MHz 26 MHz — 40 MHz							

► Output: CMOS

P	Model	Size [mm]	Nominal frequency range	Frequency tolerance (× 10 ⁻⁶)	Frequency / Temperature Characteristics (× 10 ⁻⁶)	Operating temperature [°C]	Supply voltage [V]	Current consumption Max. [mA]	Output load condition	Output control
85	TG2520CEN	2.5 × 2.0 × 0.8t	12 MHz — 52 MHz	±2.0	±2.0	-40 to +85	2.8 ± 5 % 3.0 ± 5 % 3.3 ± 5 %	4.0 (≤ 26 MHz) 6.0 (≤ 39 MHz) 6.5 (≤ 52 MHz)	15pF	-
	TG3225CEN	3.2 × 2.5 × 0.9t								



Wired network/Wireless network Solutions (TCXO / VC-TCXO)

		TCXO	VC-TCXO
Size [mm]	7.0 x 5.0 mm	TG7050SKN / TG7050SMN Clipped sine wave, +/- 100 x 10 ⁻⁹ at -40 °C to +105 °C SKN: 10pin, SMN: 4pin From 10MHz to 54MHz	
		TG7050CKN / TG7050CMN CMOS, +/- 100 x 10 ⁻⁹ at -40 °C to +105 °C CKN: 10pin, CMN: 4pin From 10MHz to 54MHz	
		TG-5510CA / TG-5511CA Clipped sine wave/CMOS, +/- 280 x 10 ⁻⁹ at -40 °C to +85 °C TG-5510CA: 10pin, TG-5511CA: 4pin From 10MHz to 54MHz	
5.0 x 3.2 mm		TG5032SKN / TG5032SMN Clipped sine wave, +/- 100 x 10 ⁻⁹ at -40 °C to +105 °C SKN: 10pin, SMN: 4pin From 10MHz to 54MHz	TG5032SGN / TG5032SFN Clipped sine wave, +/- 100 x 10 ⁻⁹ at -40 °C to +85 °C SGN: 10pin, SFN: 4pin From 10MHz to 40MHz
		TG5032CKN / TG5032CMN CMOS, +/- 100 x 10 ⁻⁹ at -40 °C to +105 °C CKN: 10pin, CMN: 4pin From 10MHz to 54MHz	TG5032CGN / TG5032CFN CMOS, +/- 100 x 10 ⁻⁹ at -40 °C to +85 °C CGN: 10pin, CFN: 4pin From 10MHz to 40MHz
		TG-5510CB / TG-5511CB Clipped sine wave/CMOS, +/- 280 x 10 ⁻⁹ at -40 °C to +85 °C TG-5510CB: 10pin, TG-5511CB: 4pin From 10MHz to 54MHz	

► Output: Clipped sine wave or CMOS

P	Model	Size [mm]	Nominal frequency range	Frequency tolerance (× 10 ⁻⁶)	Frequency / Temperature Characteristics (× 10 ⁻⁶)	Operating temperature [°C]	Supply voltage [V]	Current consumption Max. [mA]	Output load condition	Output control
86	TG7050CKN (CMOS)	7.0 × 5.0 × 1.5t (10 pin)	10 MHz — 54 MHz	±1.0	±0.1	-40 to +105	3.3 ±5 %	7.0 (≤ 26 MHz) 9.0 (≤ 40 MHz) 10.0 (≤ 54 MHz)	15 pF	OE
	TG7050SKN (Clipped sine wave)							6.0	10 kΩ/10 pF	
	TG7050CMN (CMOS)	7.0 × 5.0 × 1.5t (4 pin)	10 MHz — 54 MHz	±1.0	±0.1	-40 to +105	3.3 ±5 %	7.0 (≤ 26 MHz) 9.0 (≤ 40 MHz) 10.0 (≤ 54 MHz)	15 pF	-
	TG7050SMN (Clipped sine wave)							6.0	10 kΩ/10 pF	
87	TG-5510CA (CMOS)	7.0 × 5.0 × 1.5t (10 pin)	10 MHz — 54 MHz	±1.0	±0.28	-40 to +85 (+105 option)	3.3 ±5 %	7.0 (≤ 26 MHz) 9.0 (≤ 40 MHz) 10.0 (≤ 54 MHz)	15 pF	OE
	TG-5510CA (Clipped sine wave)							6.0	10 kΩ/10 pF	
	TG-5511CA (CMOS)	7.0 × 5.0 × 1.5t (4 pin)	10 MHz — 54 MHz	±1.0	±0.28	-40 to +85 (+105 option)	3.3 ±5 %	7.0 (≤ 26 MHz) 9.0 (≤ 40 MHz) 10.0 (≤ 54 MHz)	15 pF	-
	TG-5511CA (Clipped sine wave)							6.0	10 kΩ/10 pF	
89	TG5032CKN (CMOS)	5.0 × 3.2 × 1.45t (10 pin)	10 MHz — 54 MHz	±1.0	±0.1	-40 to +105	3.3 ±5 %	7.0 (≤ 26 MHz) 9.0 (≤ 40 MHz) 10.0 (≤ 54 MHz)	15 pF	OE
	TG5032SKN (Clipped sine wave)							6.0	10 kΩ/10 pF	
	TG5032CMN (CMOS)	5.0 × 3.2 × 1.45t (4 pin)	10 MHz — 54 MHz	±1.0	±0.1	-40 to +105	3.3 ±5 %	7.0 (≤ 26 MHz) 9.0 (≤ 40 MHz) 10.0 (≤ 54 MHz)	15 pF	-
	TG5032SMN (Clipped sine wave)							6.0	10 kΩ/10 pF	
88	TG-5510CB (CMOS)	5.0 × 3.2 × 1.45t (10 pin)	10 MHz — 54 MHz	±1.0	±0.28	-40 to +85 (+105 option)	3.3 ±5 %	7.0 (≤ 26 MHz) 9.0 (≤ 40 MHz) 10.0 (≤ 54 MHz)	15 pF	OE
	TG-5510CB (Clipped sine wave)							6.0	10 kΩ/10 pF	
	TG-5511CB (CMOS)	5.0 × 3.2 × 1.45t (4 pin)	10 MHz — 54 MHz	±1.0	±0.28	-40 to +85 (+105 option)	3.3 ±5 %	7.0 (≤ 26 MHz) 9.0 (≤ 40 MHz) 10.0 (≤ 54 MHz)	15 pF	-
	TG-5511CB (Clipped sine wave)							6.0	10 kΩ/10 pF	



P	Model	Size [mm]	Nominal frequency range	Frequency tolerance ($\times 10^{-6}$)	Frequency / Temperature Characteristics ($\times 10^{-6}$)	Operating temperature [°C]	Supply voltage [V]	Current consumption Max. [mA]	Output load condition	Output control
91	TG5032CGN (CMOS)	5.0 × 3.2 × 1.45t (10 pin)	 10 MHz 40 MHz	±1.0	±0.1	-40 to +85	2.375 to 3.63	5.0 (≤ 26 MHz) 6.0 (> 26 MHz)	15 pF	OE
	5.0							10 kΩ/10 pF		
90	TG5032CFN (CMOS)	5.0 × 3.2 × 1.45t (4 pin)	 10 MHz 40 MHz	±1.0	±0.1	-40 to +85	2.375 to 3.63	5.0 (≤ 26 MHz) 6.0 (> 26 MHz)	15 pF	-
	5.0							10 kΩ/10 pF		

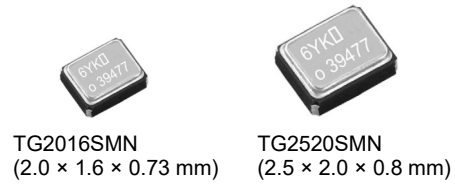
TCXO / VC-TCXO
HIGH STABILITY / Low noise



Product Number
TG2016SMN : X1G005441xxxx25
TG2520SMN : X1G005421xxxx27

TG2016SMN / TG2520SMN

- Output frequency : 10 MHz to 55 MHz
- Supply voltage : 1.8 V Typ./ 2.8 V Typ./ 3.0 V Typ./ 3.3 V Typ.
- Frequency / temperature characteristics : $\pm 0.5 \times 10^{-6}$ Max. (-40 °C to +85 °C)
 $\pm 2.0 \times 10^{-6}$ Max. (-40 °C to +85 °C)
- External dimensions: 2.0 × 1.6 × 0.73 mm / 2.5 × 2.0 × 0.8 mm
- Applications : GPS, RF
 Wireless communication devices
 (LTE, WiMAX, Wi-Fi, W-LAN, IoT other)
- Features : Low noise



Specifications (characteristics)

Item	Symbol	TCXO	VC-TCXO	Conditions / Remarks
Output frequency range	f _o	10 MHz to 55MHz 16, 16.368, 16.369, 19.2, 20, 24, 25, 26, 27, 27.6, 30, 32, 38.4, 40, 48, 50, 52 MHz		Standard frequency
Supply voltage	V _{CC}	1.8 V ± 0.1 V / 2.8 V ± 5 % / 3.0 V ± 5 % / 3.3 V ± 5 %		Supply voltage range :1.7 V to 3.63 V
Storage temperature range	T _{stg}	-40 °C to +90 °C		Storage as single product.
Operating temperature range	T _{use}	G: -40 °C to +85 °C		
Frequency tolerance	f _{tol}	±1.5 × 10 ⁻⁶ Max.		After reflow, +25 °C
Frequency/temperature characteristics	f _o -T _c	C: ±0.5 × 10 ⁻⁶ Max. / -40 °C to +85 °C F: ±2.0 × 10 ⁻⁶ Max. / -40 °C to +85 °C		Standard stability version
Frequency/load coefficient	f _o -Load	±0.1 × 10 ⁻⁶ Max.		10 kΩ // 10 pF ± 10 %
Frequency/voltage coefficient	f _o -V _{CC}	±0.1 × 10 ⁻⁶ Max.		V _{CC} ± 5 %
Frequency aging	f _{age}	±0.5 × 10 ⁻⁶ Max.		+25 °C, First year, f _o = 10MHz, 12 MHz ≤ f _o ≤ 20 MHz, 24 MHz ≤ f _o ≤ 40 MHz
		±1.5 × 10 ⁻⁶ Max.		+25 °C, First year, 10 MHz < f _o < 12 MHz, 20 MHz < f _o < 24 MHz, 40 MHz < f _o ≤ 55 MHz
Current consumption	I _{CC}	1.5 mA Max.		10 MHz ≤ f _o ≤ 26 MHz
		1.8 mA Max.		26 MHz < f _o ≤ 40 MHz
		2.0 mA Max.		40 MHz < f _o ≤ 50 MHz
		2.1 mA Max.		50 MHz < f _o ≤ 55 MHz
Input impedance	Z _{in}	500 kΩ Min.	-	V _c - GND (DC)
Frequency control range	f _{cont}	-	±5.0 × 10 ⁻⁶ Min.	B: V _c = 0.9 V ± 0.6 V (V _{CC} = 1.8 V) or C: V _c = 1.4 V ± 1.0 V (V _{CC} = 2.8 V) or D: V _c = 1.5 V ± 1.0 V (V _{CC} = 3.0 V) or E: V _c = 1.65 V ± 1.0 V (V _{CC} = 3.3 V)
Frequency change polarity	f _{cp}	-	Positive polarity	
Symmetry	SYM	40 % to 60 %		GND level (DC cut)
Output voltage	V _{pp}	0.8 V Min.		Peak to Peak
Start-up time	t _{str}	1.0 ms Max.		t = 0 at 90% V _{CC}
Output load	Load _R	10 kΩ		DC cut capacitor = 0.01 μF
	Load _C	10 pF		

* Note : Please contact us for requirements not listed in this specification.

Product Name **TG2016 SMN 26.000000MHz** E C G N N M
 (Standard form) ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨

① Model (TG2016, TG2520)

② Output (S: Clipped sine wave) ③ Frequency

④ Supply voltage (Refer to symbol table) ⑤ Frequency / temperature characteristics (C: ±0.5 × 10⁻⁶ Max., F: ±2.0 × 10⁻⁶ Max.)

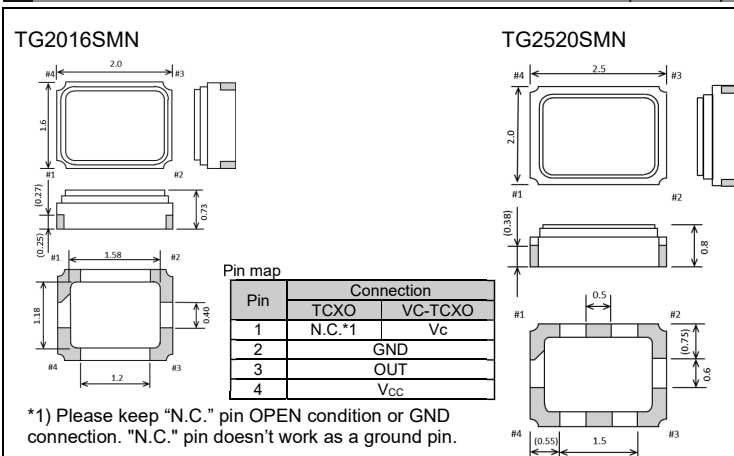
⑥ Operating temperature (G: -40 °C to +85 °C) ⑦ ST function (N: Non)

⑧ V_c function (Refer to symbol table , A: V_c=any) ⑨ Internal identification code ("M" is default)

④ Supply voltage[V _{CC}], ⑧ V _c function[V _c] (Symbol table)				
Voltage [V]	TCXO	VC-TCXO		
④ V _{CC} (Typ.)	E:1.8 M:2.8 to 3.3	E:1.8	B:2.8	A:3.0 C:3.3
⑧ V _c (Typ.)	N: Non	B 0.9	C:1.4	D 1.5 E 1.65

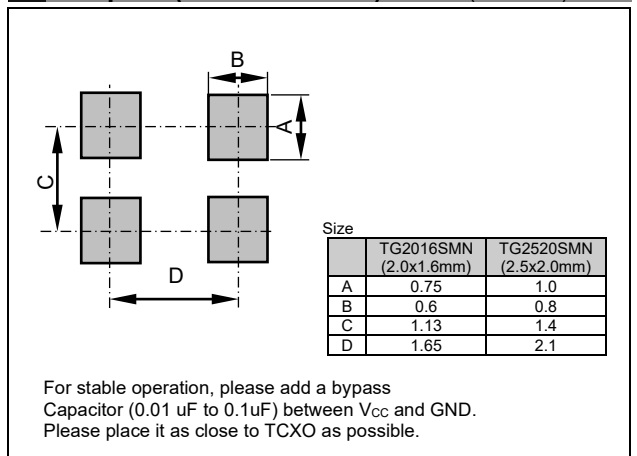
External dimensions

(Unit: mm)



Footprint (Recommended)

(Unit: mm)



TCXO / VC-TCXO / TCXO-Standby
105 °C High temperature range



Product Number
TG1612SLN : X1G005721xxxx16

TG1612SLN

- Output frequency : 13 MHz to 55.2 MHz
- Supply voltage : 1.8 V Typ./ 2.8 V Typ./ 3.0 V Typ./ 3.3 V Typ.
- Frequency / temperature characteristics : $\pm 0.5 \times 10^{-6}$ Max. (-40 °C to +85 °C) and $\pm 5.0 \times 10^{-6}$ Max. (+85 °C to +105 °C)
- External dimensions: 1.6 × 1.2 × 0.45 mm Max.
- Applications : Smart phone, LPWA module
Wireless communication devices
- Features : 105 °C High temp, Standby function (\overline{ST})



TG1612SLN
(1.6 × 1.2 × 0.45 mm)

Specifications (characteristics)

Item	Symbol	TCXO	VC-TCXO	TCXO-Standby	Conditions / Remarks
Output frequency range	fo	13 MHz to 55.2 MHz			
		26 MHz			Standard frequency
Supply voltage	V _{CC}	1.8 V ± 0.1 V / 2.8 V ± 5 % / 3.0 V ± 5 % / 3.3 V ± 5 %			Supply voltage range : 1.7 V to 3.63 V
Storage temperature range	T _{stg}	-40 °C to +105 °C			Storage as single product.
Operating temperature range	T _{use}	G: -40 °C to +85 °C / H: -40 °C to +105 °C			
Frequency tolerance	f _{tol}	$\pm 2.0 \times 10^{-6}$ Max.			After 3times reflow, +25 °C
Frequency/temperature characteristics	fo-Tc	C: $\pm 0.5 \times 10^{-6}$ Max. / -40 °C to +85 °C W: $\pm 0.5 \times 10^{-6}$ Max. / -40 °C to +85 °C and $\pm 5.0 \times 10^{-6}$ Max. / +85 °C to +105 °C (Option)			Standard stability version Customized product (Option)
Frequency/load coefficient	fo-Load	$\pm 0.1 \times 10^{-6}$ Max.			10 kΩ // 10 pF ± 10 %
Frequency/voltage coefficient	fo-V _{CC}	$\pm 0.2 \times 10^{-6}$ Max.			V _{CC} ± 5 %
Frequency aging	f _{age}	$\pm 1.0 \times 10^{-6}$ Max.			+25 °C, First year, 13 MHz ≤ fo ≤ 20 MHz, 26 MHz ≤ fo ≤ 40 MHz
		$\pm 1.5 \times 10^{-6}$ Max.			+25 °C, First year, 20 MHz < fo < 26 MHz, 40 MHz < fo ≤ 55.2 MHz
Current consumption	I _{CC}	1.5 mA Max. 1.7 mA Max. 2.0 mA Max. 2.5 mA Max.			13 MHz < fo ≤ 26 MHz (-40 to +85 °C) 13 MHz < fo ≤ 26 MHz (-40 to +105 °C) 26 MHz < fo ≤ 38.4 MHz (-40 to +105 °C) 38.4 MHz < fo ≤ 55.2 MHz (-40 to +105 °C)
Input resistance	Z _{in}	-	500 kΩ Min.	-	V _C - GND (DC)
Frequency control range	f _{cont}	-	$\pm 8.0 \times 10^{-6}$ to $\pm 15.0 \times 10^{-6}$	-	B: V _C = 0.9 V ± 0.6 V (V _{CC} = 1.8 V) or C: V _C = 1.4 V ± 1.0 V (V _{CC} = 2.8 V) or D: V _C = 1.5 V ± 1.0 V (V _{CC} = 3.0 V) or E: V _C = 1.65 V ± 1.0 V (V _{CC} = 3.3 V)
Frequency change polarity	f _{cp}	-	Positive polarity	-	
Stand-by current	I _{std}	-		3 μA Max.	\overline{ST} = GND
Input voltage	V _{IH} V _{IL}	-		80 % V _{CC} Min. 20 % V _{CC} Max.	\overline{ST} terminal
Symmetry	SYM	45 % to 55 %			GND level (DC cut)
Output voltage	V _{pp}	0.8 V Min. / 1.5 V Max.			Peak to Peak
Start-up time	t _{str}	1.0 ms Max.			t = 0 at 90 % V _{CC}
Output load	Load _R Load _C	10 kΩ 10 pF			DC cut capacitor = 0.01 μF

* Note : Please contact us for requirements not listed in this specification.

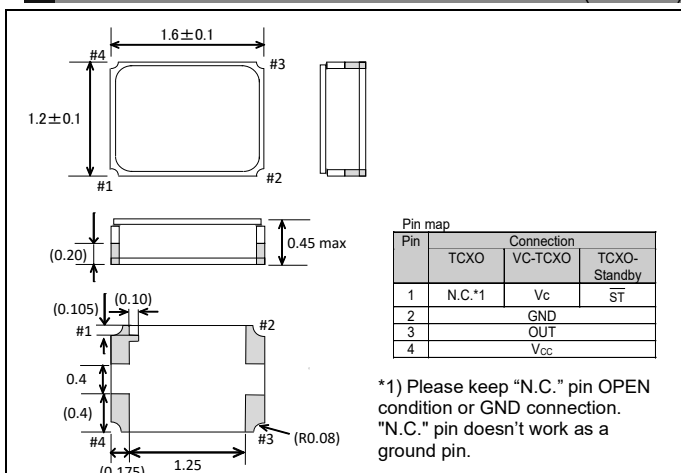
Product Name TG1612 SLN 26.000000MHz E W H S N M
(Standard form) ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨

④ Supply voltage [V _{CC}], ⑥ V _C function [V _C] (Symbol table)				
Voltage [V]	Suffix symbol: Voltage(Typ.) [V]			
④ V _{CC} :	E: 1.8	B: 2.8	A: 3.0	C: 3.3
⑥ V _C :	N: Non	B: 0.9	C: 1.4	D: 1.5 E: 1.65

- ① Model ② Output (S: Clipped sine wave)
- ③ Frequency ④ Supply voltage (Refer to symbol table)
- ⑤ Frequency / temperature characteristics (C: $\pm 0.5 \times 10^{-6}$ Max., W: $\pm 0.5 \times 10^{-6}$ Max. and $\pm 5.0 \times 10^{-6}$ Max.)
- ⑥ Operating temperature (H: -40 °C to +105 °C, G: -40 °C to +85 °C) ⑦ ST function (N: Non, S: Standby)
- ⑧ V_C function (Refer to symbol table, N: Non for TCXO / TCXO-Standby) ⑨ Internal identification code ("M" is default)

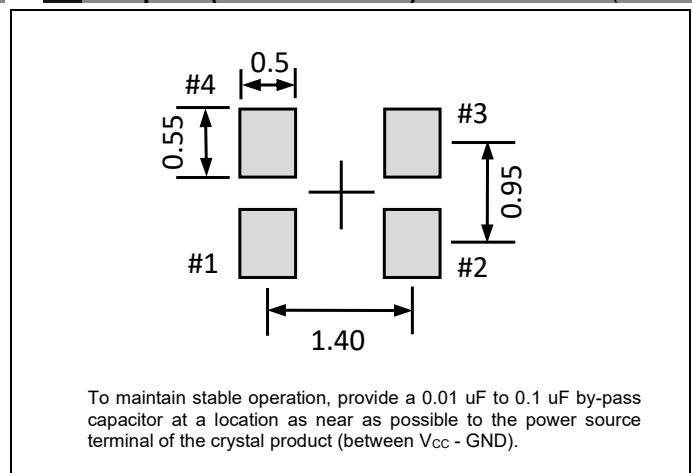
External dimensions

(Unit: mm)



Footprint (Recommended)

(Unit: mm)

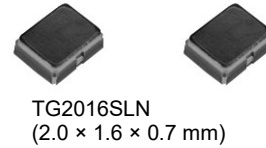


TCXO / VC-TCXO / TCXO-Standby
105 °C High temperature range
TG2016SLN



Product Number
TG2016SLN : X1G005731xxxx16

- Output frequency : 10 MHz to 55.2 MHz
- Supply voltage : 1.8 V Typ. / 2.8 V Typ. / 3.0 V Typ. / 3.3 V Typ.
- Frequency / temperature characteristics : $\pm 0.5 \times 10^{-6}$ Max. (-40 °C to +85 °C) and $\pm 5.0 \times 10^{-6}$ Max. (+85 °C to +105 °C)
- External dimensions: 2.0 × 1.6 × 0.7 mm Max.
- Applications : GNSS, Industrial, Wireless communication devices
- Features : 105 °C High temp, Stand-by function (\overline{ST})



Specifications (characteristics)

Item	Symbol	TCXO	VC-TCXO	TCXO-Standby	Conditions / Remarks
Output frequency range	fo	10 MHz to 55.2 MHz			
		26 MHz, 32 MHz, 38.4 MHz, 49.58 MHz			Standard frequency
Supply voltage	V _{CC}	1.8 V ± 0.1 V / 2.8 V ± 5 % / 3.0 V ± 5 % / 3.3 V ± 5 %			Supply voltage range: 1.7 V to 3.63 V
Storage temperature range	T _{stg}	-40 °C to +105 °C			Storage as single product.
Operating temperature range	T _{use}	G: -40 °C ~ +85 °C / H: -40 °C to +105 °C			
Frequency tolerance	f _{tol}	±2.0 × 10 ⁻⁶ Max.			After 3times reflow, +25 °C
Frequency/temperature characteristics	fo-Tc	C: ±0.5 × 10 ⁻⁶ Max. / -40 °C to +85 °C W: ±0.5 × 10 ⁻⁶ Max. / -40 °C to +85 °C, and ±5.0 × 10 ⁻⁶ Max. / +85 °C to +105 °C (Option)			
Frequency/load coefficient	fo-Load	±0.2 × 10 ⁻⁶ Max.			10 kΩ // 10 pF ± 10 %
Frequency/voltage coefficient	fo-V _{CC}	±0.2 × 10 ⁻⁶ Max.			V _{CC} ± 5 %
Frequency aging	f _{age}	±1.0 × 10 ⁻⁶ Max.			+25 °C, First year, 10 MHz ≤ fo ≤ 20 MHz, 26 MHz ≤ fo ≤ 40 MHz
		±1.5 × 10 ⁻⁶ Max.			+25 °C, First year, 20 MHz < fo < 26 MHz, 40 MHz < fo ≤ 55.2 MHz
Current consumption	I _{CC}	1.5 mA Max. 1.7 mA Max. 2.0 mA Max. 2.5 mA Max.			fo ≤ 26 MHz (-40 °C to +85 °C) fo ≤ 26 MHz (-40 °C to +105 °C) 10 MHz ≤ fo ≤ 38.4 MHz (-40 °C to +105 °C) 38.4MHz < fo ≤ 55.2 MHz (-40 °C to +105 °C)
Input resistance	Z _{in}	-	500 kΩ Min.	-	V _C - GND (DC)
Frequency control range	f _{cont}	-	±8.0 × 10 ⁻⁶ to ±12.0 × 10 ⁻⁶	-	B: V _C = 0.9 V ± 0.6 V (V _{CC} = 1.8 V) or C: V _C = 1.4 V ± 1.0 V (V _{CC} = 2.8 V) or D: V _C = 1.5 V ± 1.0 V (V _{CC} = 3.0 V) or E: V _C = 1.65 V ± 1.0 V (V _{CC} = 3.3 V)
Frequency change polarity	f _{cp}	-	Positive polarity	-	
Stand-by current	I _{std}	-	-	3 μA Max.	\overline{ST} = GND
Input voltage	V _{IH}	-	-	80 % V _{CC} Min.	\overline{ST} terminal
	V _{IL}	-	-	20 % V _{CC} Max.	
Symmetry	SYM	45 % to 55 %			GND level (DC cut)
Output voltage	V _{pp}	0.8 V Min. / 1.5 V Max.			Peak to Peak
Start-up time	t _{str}	2.0 ms Max.			t = 0 at 90% V _{CC}
Output load	Load _R	10 kΩ			DC cut capacitor = 0.01 μF
	Load _C	10 pF			

* Note : Please contact us for requirements not listed in this specification.

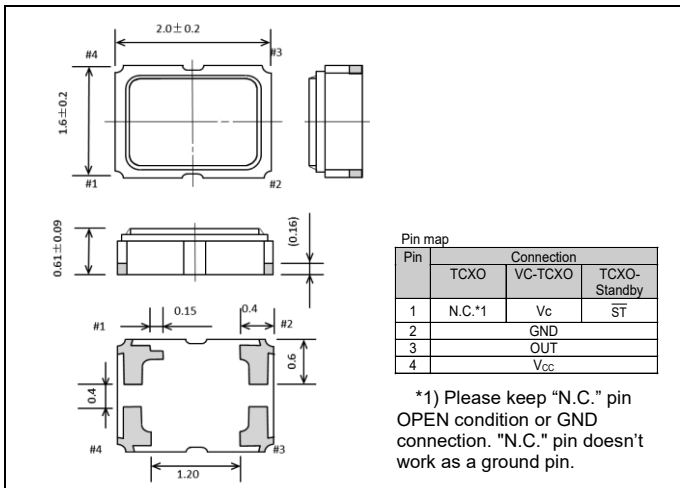
Product Name **TG2016 SLN 26.000000MHz** E W H S N M
 (Standard form) ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨

- ① Model ② Output (S: Clipped sine wave)
- ③ Frequency ④ Supply voltage (Refer to symbol table)
- ⑤ Frequency / temperature characteristics (C: $\pm 0.5 \times 10^{-6}$ Max., W: $\pm 0.5 \times 10^{-6}$ Max. and $\pm 5.0 \times 10^{-6}$ Max.)
- ⑥ Operating temperature (G: -40 °C ~ +85 °C, H: -40 °C to +105 °C) ⑦ ST function (N: Non, S: Standby)
- ⑧ Vc function (Refer to symbol table, N: Non for TCXO / TCXO-Standby) ⑨ Internal identification code ("M" is default)

④ Supply voltage [V _{CC}], ⑧ Vc function [V _C] (Symbol table)					
Voltage [V]	Suffix symbol: Voltage(Typ.) [V]				
④ V _{CC}	E: 1.8	B: 2.8	A: 3.0	C: 3.3	
⑧ V _C	N: Non	B: 0.9	C: 1.4	D: 1.5	E: 1.65

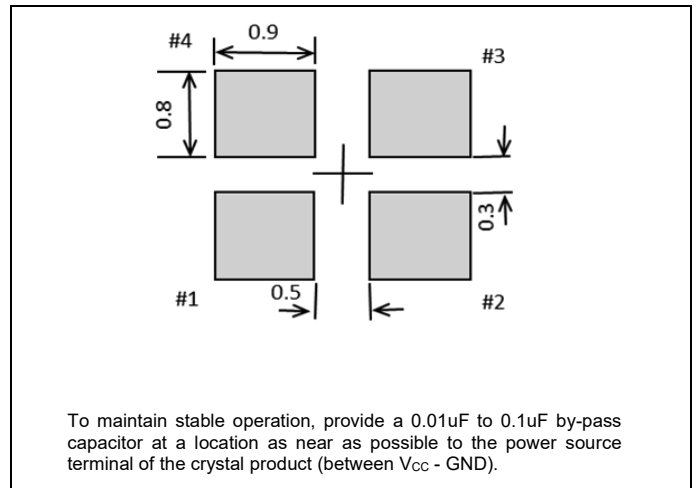
External dimensions

(Unit: mm)



Footprint (Recommended)

(Unit: mm)





TCXO / VC-TCXO HIGH STABILITY, CMOS OUTPUT



Product Number
 TG3225CEN : X1G005101xxxxxx
 TG2520CEN : X1G005161xxxxxx

TG3225CEN / TG2520CEN

- Output frequency : 12 MHz to 52 MHz
- Supply voltage : 2.8 V Typ. / 3.0 V Typ. / 3.3 V Typ.
- Frequency / temperature characteristics : $\pm 2.0 \times 10^{-6}$ Max.
- External dimensions: 3.2 × 2.5 × 0.9 mm / 2.5 × 2.0 × 0.8 mm
- Applications : Reference clock for measurement machine
Wireless communication devices
(Smart meter, Telemeter, other)
- Features : High stability, CMOS output



TG3225CEN
(3.2 × 2.5 × 0.9 mm)



TG2520CEN
(2.5 × 2.0 × 0.8 mm)

Specifications (characteristics)

Item	Symbol	TCXO	VC-TCXO	Conditions / Remarks
Output frequency range	fo	12 MHz to 52 MHz 12 MHz, 20 MHz, 24 MHz, 25 MHz, 26 MHz, 27 MHz, 32 MHz, 36 MHz, 38.4 MHz, 39 MHz and 40 MHz		Standard frequency
Supply voltage	V _{CC}	2.8 V ± 5 % / 3.0 V ± 5 % / 3.3 V ± 5 %		Supply voltage range: 2.375 V to 3.63 V
Storage temperature range	T _{stg}	-40 °C to +90 °C		Storage as single product.
Operating temperature range	T _{use}	G: -40 °C to +85 °C		
Frequency tolerance	f _{tol}	$\pm 2.0 \times 10^{-6}$ Max.		After reflow, +25 °C
Frequency/temperature characteristics	fo-Tc	F: $\pm 2.0 \times 10^{-6}$ Max. / -40 °C to +85 °C		Standard stability version
Frequency/load coefficient	fo-Load	$\pm 0.2 \times 10^{-6}$ Max.		15 pF ± 10 %
Frequency/voltage coefficient	fo-V _{CC}	$\pm 0.3 \times 10^{-6}$ Max.		V _{CC} ± 5 %
Frequency aging	f _{age}	$\pm 1.0 \times 10^{-6}$ Max.		+25 °C, First year, 12 MHz ≤ fo ≤ 20 MHz, 24 MHz ≤ fo ≤ 40 MHz
		$\pm 1.5 \times 10^{-6}$ Max.		+25 °C, First year, 20 MHz < fo < 24 MHz, 40 MHz < fo ≤ 52 MHz
Current consumption	I _{CC}	4.0 mA Max. 6.0 mA Max. 6.5 mA Max.		12 MHz ≤ fo ≤ 26 MHz 26 MHz < fo ≤ 39 MHz 39 MHz < fo ≤ 52 MHz
Input impedance	Z _{in}	-	500 kΩ Min.	V _c - GND (DC)
Frequency control range	f _{cont}	-	$\pm 5.0 \times 10^{-6}$ Min.	C: V _c = 1.4 V ± 1.0 V (V _{CC} = 2.8 V) or D: V _c = 1.5 V ± 1.0 V (V _{CC} = 3.0 V) or E: V _c = 1.65 V ± 1.0 V (V _{CC} = 3.3 V)
Frequency change polarity	f _{cp}	-	Positive polarity	
Symmetry	SYM	45 % to 55 %		50 % V _{CC} level, L _{CMOS} ≤ 15 pF
Output voltage	V _{OH}	90 % V _{CC} Min.		
	V _{OL}	10 % V _{CC} Max.		
Start-up time	t _{str}	2.0 ms Max.		t = 0 at 90 % V _{CC}
Rise time / Fall time	tr/tf	8.0 ns Max.		10 % V _{CC} to 90 % V _{CC} level, Load: 15 pF
CMOS load condition	L _{CMOS}	15 pF		15 pF ± 10 %

* Note : Please contact us for requirements not listed in this specification.

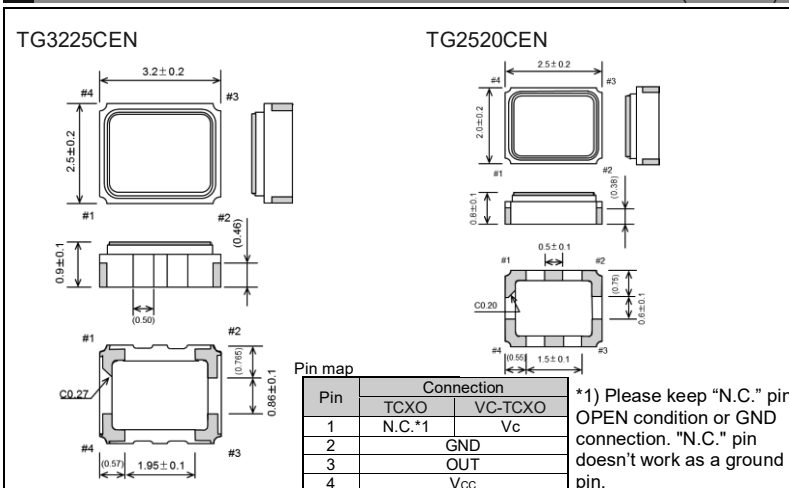
Product Name **TG3225 CEN 39.000000MHz K F G N N M**
 (Standard form) ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨

- ① Model ② Output (C: CMOS)
 ③ Frequency ④ Supply voltage (Refer to symbol table)
 ⑤ Frequency / temperature characteristics (F: $\pm 2.0 \times 10^{-6}$ Max.)
 ⑥ Operating temperature (G: -40 °C to +85 °C)
 ⑦ OE function (N: Non) ⑧ Vc function (Refer to symbol table, A: Vc = any)
 ⑨ Internal identification code ("M" is default)

Voltage [V]	④ Supply voltage [V _{CC}] , ⑧ Vc function [V _c] (Symbol table)			
	TCXO	VC-TCXO		
④ V _{CC} (Typ.)	K: 2.5 to 3.3	K: 2.5 to 3.3	P: 2.6 to 3.3	M: 2.8 to 3.3
⑧ V _c (Typ.)	N: Non	C: 1.4	D: 1.5	E: 1.65

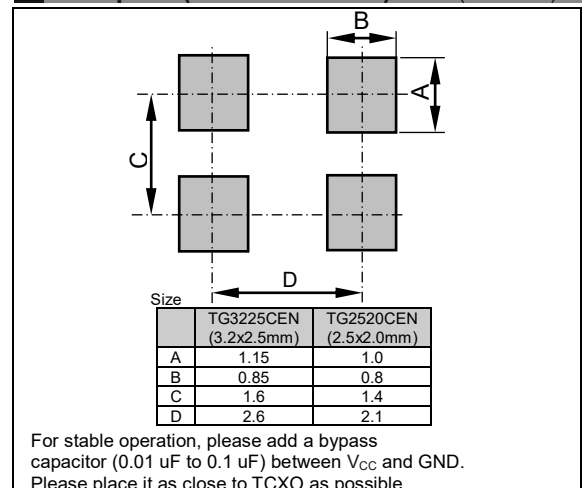
External dimensions

(Unit: mm)



Footprint (Recommended)

(Unit: mm)



TCXO HIGH STABILITY

105 °C HIGH TEMPERATURE

TG7050CKN / SKN

TG7050CMN / SMN



Product Number
 TG7050CKN: X1G005661xxxx99
 TG7050SKN: X1G005671xxxx99
 TG7050CMN: X1G005681xxxx99
 TG7050SMN: X1G005691xxxx99

- Frequency range : 10 MHz to 54 MHz
- Supply voltage : 3.3 V Typ.
- Frequency / temperature characteristics
 - : $\pm 0.1 \times 10^{-6}$ Max. (-40 °C to +105 °C)
- Free-run accuracy : $\pm 4.6 \times 10^{-6}$ Max. / 20 years (for Stratum3)
- External dimensions : 7.0 × 5.0 × 1.5 mm (10 pins or 4 pins)
- Applications : Network synchronization, Stratum3, BTS, SyncE, IEEE1588, Microwave, BTS
- Features : 105 °C High temp, High stability



TG7050CKN
TG7050SKN
(10 pins)



TG7050CMN
TG7050SMN
(4 pins)

Specifications (characteristics)

Item	Symbol	CMOS	Clipped sine wave	Condition
Output frequency range	f _o	10 MHz to 54 MHz		Please contact us about available frequencies.
Supply voltage	V _{CC}	3.3 V ± 5 %		
Storage temperature range	T _{stg}	-40 °C to +105 °C		Storage as single product.
Operating temperature range	T _{use}	-40 °C to +105 °C		
a) Frequency tolerance	f _{tol}	±1.0 × 10 ⁻⁶ Max.		After reflow, +25 °C
b) Frequency/temperature characteristics	f _o -T _c	±0.1 × 10 ⁻⁶ Max.		-40 °C to +105 °C
c) Frequency/load coefficient	f _o -Load	±0.1 × 10 ⁻⁶ Max.		Load ± 10 %
d) Frequency/voltage coefficient	f _o -V _{CC}	±0.1 × 10 ⁻⁶ Max.		V _{CC} ± 5 %
e) Frequency aging	f _{age}	±0.5 × 10 ⁻⁶ Max.		+25 °C, First year
		±3.0 × 10 ⁻⁶ Max.		+25 °C, 20 years
Holdover stability (Constant temperature)	-	±0.01 × 10 ⁻⁶ Max. (+25 °C, 24 hours)		After 10 days of continuous operation
		±0.04 × 10 ⁻⁶ Max. (+25 °C, 24 hours)		After 48 hours of continuous operation
Wander generation (MTIE, TDEV)		Compliant with GR-1244CORE, ITU-T G.8262		
Free-run accuracy	-	±4.6 × 10 ⁻⁶ Max. / 20 years		This includes Item a), b), c), d) and e)
Current consumption	I _{CC}	7.0 mA Max.	6.0 mA Max.	10 MHz ≤ f _o ≤ 26 MHz
		9.0 mA Max.		26 MHz < f _o ≤ 40 MHz
		10.0 mA Max.		40 MHz < f _o ≤ 54 MHz
Symmetry	SYM	45 % to 55 %	-	GND level (DC cut)
Output voltage	V _{OH}	90 % V _{CC} Min.	-	
	V _{OL}	10 % V _{CC} Max.	-	
Rise time / Fall time	tr/tf	8.0 ns Max.	-	10 % V _{CC} to 90 % V _{CC} level, Load: 15 pF
Start-up time	t _{str}		5 ms. Max.	t = 0 at 90 % V _{CC}
Output level	V _{pp}	-	0.8 V Min.	Peak to Peak
Output load condition	Load	15 pF	10 kΩ // 10 pF	
Input voltage	V _{IH}		70 % V _{CC} Min.	OE terminal (Enable voltage)
	V _{IL}		30 % V _{CC} Max.	OE terminal (Disable voltage)

* Note : Please contact us for requirements not listed in this specification.

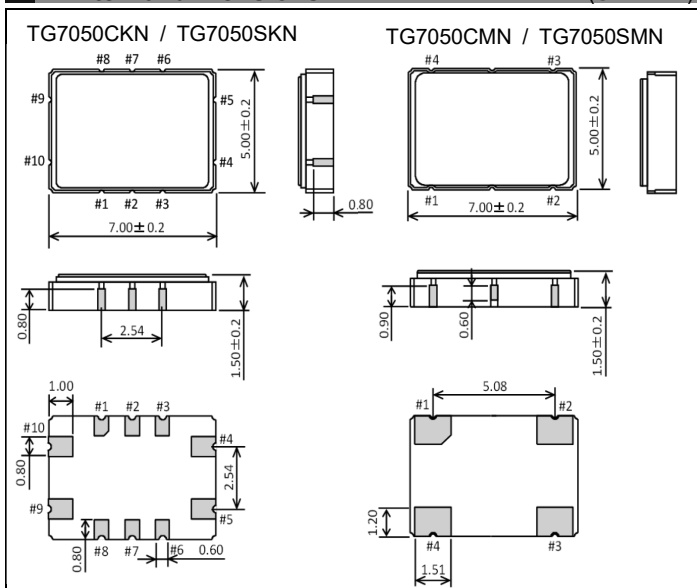
Product Name TG7050CKN30.72000MHzCAHHGA

(Standard form) ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩

- ① Model ② Output (C: CMOS, S: Clipped sine wave) ③ Package type (K: 10 pins, M: 4 pins) ④ Frequency ⑤ Supply voltage (C: 3.3 V Typ.)
 ⑥ Frequency / temperature characteristics (A: $\pm 0.1 \times 10^{-6}$ Max.) ⑦ Operating temperature (H: -40 °C to +105 °C)
 ⑧ OE function (H: Active High, N: Non) ⑨ Vc function (G: Vc Non) ⑩ Internal identification code ("A" is default)

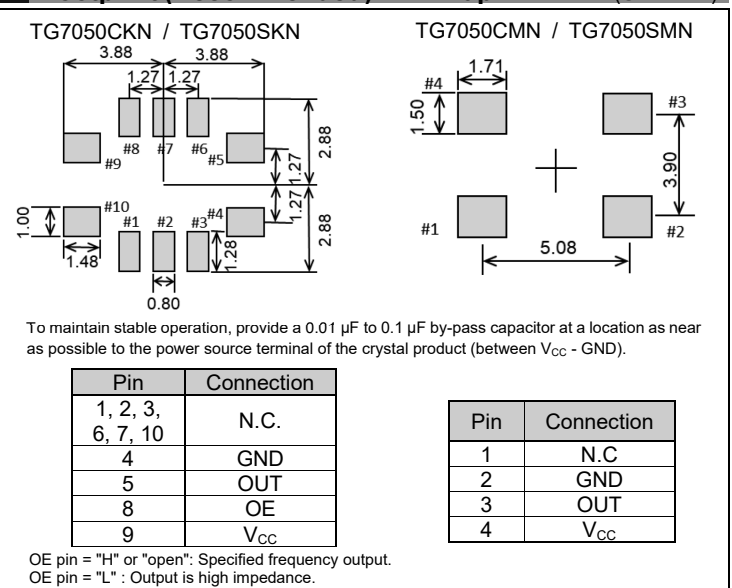
External dimensions

(Unit : mm)



Footprint (Recommended) / Pin Map

(Unit : mm)



TCXO HIGH STABILITY 105 °C HIGH TEMPERATURE



Product Number
TG-5510CA: X1G006001xxxx99
TG-5511CA: X1G006011xxxx99

TG-5510CA / TG-5511CA

- Frequency range : 10 MHz to 54 MHz
- Supply voltage : 3.3 V Typ.
- Frequency / temperature characteristics : $\pm 0.28 \times 10^{-6}$ Max. (-40 °C to +85 °C, 105 °C option)
- Free-run accuracy : $\pm 4.6 \times 10^{-6}$ Max. / 20 years (for Stratum3)
- External dimensions : 7.0 × 5.0 × 1.5 mm (10 pins or 4 pins)
- Applications : Network synchronization, Stratum3, BTS, SyncE, IEEE1588, Microwave, BTS
- Features : 105 °C High temp, High stability



TG-5510CA
(10 pins)



TG-5511CA
(4 pins)

Specifications (characteristics)

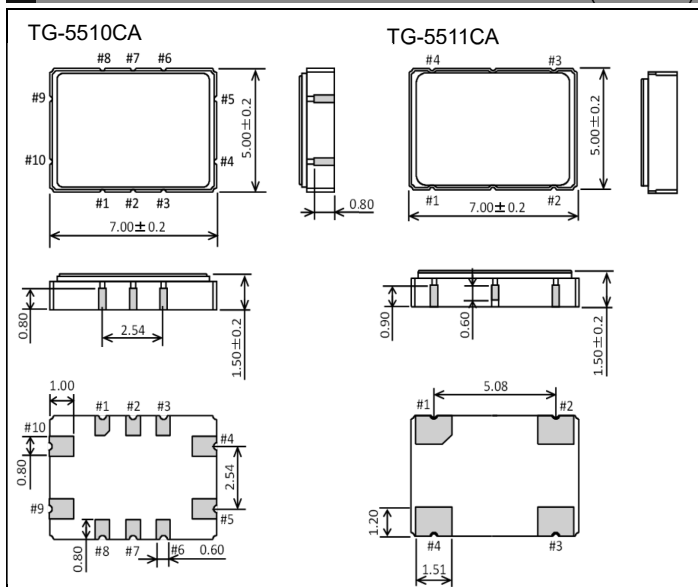
Item	Symbol	CMOS	Clipped sine wave	Condition
Output frequency range	fo	10 MHz to 54 MHz		Please contact us about available frequencies.
Supply voltage	V _{CC}	3.3 V ± 5 %		
Storage temperature range	T _{stg}	-40 °C to +105 °C		Storage as single product.
Operating temperature range	T _{use}	-40 °C to +85 °C (-40 °C to +105 °C)		Standard (Option)
a) Frequency tolerance	f _{tol}	$\pm 1.0 \times 10^{-6}$ Max.		After reflow, +25 °C
b) Frequency/temperature characteristics	fo-Tc	$\pm 0.28 \times 10^{-6}$ Max. ($\pm 0.25 \times 10^{-6}$ Max.)		Standard (Option)
c) Frequency/load coefficient	fo-Load	$\pm 0.1 \times 10^{-6}$ Max.		Load ± 10 %
d) Frequency/voltage coefficient	fo-V _{CC}	$\pm 0.1 \times 10^{-6}$ Max.		V _{CC} ± 5 %
e) Frequency aging	f _{age}	$\pm 0.5 \times 10^{-6}$ Max. $\pm 3.0 \times 10^{-6}$ Max.		+25 °C, First year +25 °C, 20 years
Holdover stability (Constant temperature)	-	$\pm 0.01 \times 10^{-6}$ Max. (+25 °C, 24 hours) $\pm 0.04 \times 10^{-6}$ Max. (+25 °C, 24 hours)		After 10 days of continuous operation After 48 hours of continuous operation
Wander generation (MTIE, TDEV)		Compliant with GR-1244CORE, ITU-T G.8262		
Free-run accuracy	-	$\pm 4.6 \times 10^{-6}$ Max. / 20 years		This includes Item a), b), c), d) and e)
Current consumption	I _{CC}	7.0 mA Max.	6.0 mA Max.	10 MHz ≤ fo ≤ 26 MHz
		9.0 mA Max.		26 MHz < fo ≤ 40 MHz
		10.0 mA Max.		40 MHz < fo ≤ 54 MHz
Symmetry	SYM	45 % to 55 %	-	GND level (DC cut)
Output voltage	V _{OH}	90 % V _{CC} Min.	-	
	V _{OL}	10 % V _{CC} Max.	-	
Rise time / Fall time	tr/ff	8.0 ns Max.	-	10 % V _{CC} to 90 % V _{CC} level, Load: 15 pF
Start-up time	t _{str}	5 ms. Max.		t = 0 at 90 % V _{CC}
Output level	V _{pp}	-	0.8 V Min.	Peak to Peak
Output load condition	Load	15 pF	10 kΩ // 10 pF	
Input voltage	V _{IH}	70 % V _{CC} Min.		OE terminal (Enable voltage)
	V _{IL}	30 % V _{CC} Max.		OE terminal (Disable voltage)

* Note : Please contact us for requirements not listed in this specification.

Product Name TG-5510CA-*** 30.720000MHz
 (Standard form) ① ② ③ ④
 ①Model ②Package type ③Spec segment (Please contact us) ④Frequency

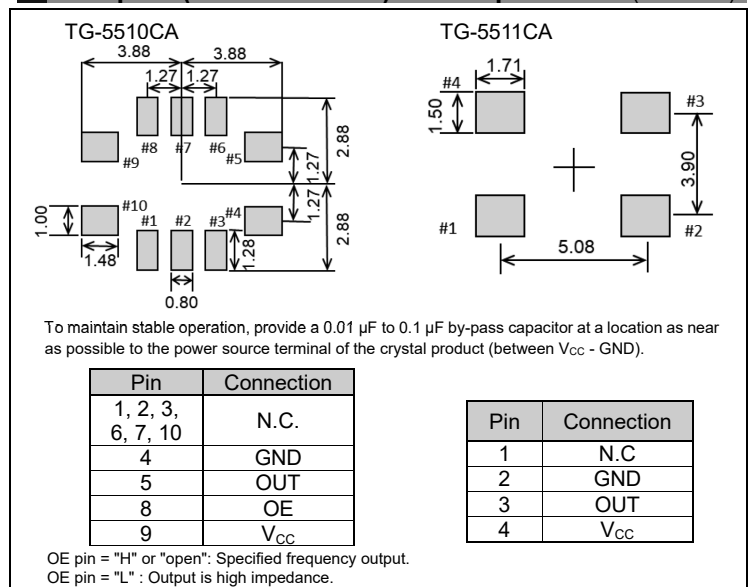
External dimensions

(Unit : mm)



Footprint (Recommended) / Pin Map

(Unit : mm)





TCXO HIGH STABILITY 105 °C HIGH TEMPERATURE



Product Number
TG-5510CB: X1G006061xxxx14
TG-5511CB: X1G006071xxxx14

TG-5510CB / TG-5511CB

- Frequency range : 10 MHz to 54 MHz
- Supply voltage : 3.3 V Typ.
- Frequency / temperature characteristics : $\pm 0.28 \times 10^{-6}$ Max. (-40 °C to +85 °C, 105 °C option)
- Free-run accuracy : $\pm 4.6 \times 10^{-6}$ Max. / 20 years (for Stratum3)
- External dimensions : 5.0 × 3.2 × 1.45 mm (10 pins or 4 pins)
- Applications : Network synchronization, Stratum3, BTS, SyncE, IEEE1588, Microwave, BTS
- Features : 105 °C High temp, High stability



TG-5510CB
(10 pins)



TG-5511CB
(4 pins)

Specifications (characteristics)

Item	Symbol	CMOS	Clipped sine wave	Condition
Output frequency range	f _o	10 MHz to 54 MHz		Please contact us about available frequencies.
Supply voltage	V _{CC}	3.3 V ± 5 %		
Storage temperature range	T _{stg}	-40 °C to +105 °C		Storage as single product.
Operating temperature range	T _{use}	-40 °C to +85 °C (-40 °C to +105 °C)		Standard (Option)
a) Frequency tolerance	f _{tol}	$\pm 1.0 \times 10^{-6}$ Max.		After reflow, +25 °C
b) Frequency/temperature characteristics	f _o -T _c	$\pm 0.28 \times 10^{-6}$ Max. ($\pm 0.25 \times 10^{-6}$ Max.)		Standard (Option)
c) Frequency/load coefficient	f _o -Load	$\pm 0.1 \times 10^{-6}$ Max.		Load ± 10 %
d) Frequency/voltage coefficient	f _o -V _{CC}	$\pm 0.1 \times 10^{-6}$ Max.		V _{CC} ± 5 %
e) Frequency aging	f _{age}	$\pm 0.5 \times 10^{-6}$ Max. $\pm 3.0 \times 10^{-6}$ Max.		+25 °C, First year +25 °C, 20 years
Holdover stability (Constant temperature)	-	$\pm 0.01 \times 10^{-6}$ Max. (+25 °C, 24 hours) $\pm 0.04 \times 10^{-6}$ Max. (+25 °C, 24 hours)		After 10 days of continuous operation After 48 hours of continuous operation
Wander generation (MTIE, TDEV)		Compliant with GR-1244CORE, ITU-T G.8262		
Free-run accuracy	-	$\pm 4.6 \times 10^{-6}$ Max. / 20 years		This includes Item a), b), c), d) and e)
Current consumption	I _{CC}	7.0 mA Max.	6.0 mA Max.	10 MHz ≤ f _o ≤ 26 MHz
		9.0 mA Max.		26 MHz < f _o ≤ 40 MHz
		10.0 mA Max.		40 MHz < f _o ≤ 54 MHz
Symmetry	SYM	45 % to 55 %	-	GND level (DC cut)
Output voltage	V _{OH}	90 % V _{CC} Min.	-	
	V _{OL}	10 % V _{CC} Max.	-	
Rise time / Fall time	tr/ff	8.0 ns Max.	-	10 % V _{CC} to 90 % V _{CC} level, Load: 15 pF
Start-up time	t _{str}	5 ms. Max.		t = 0 at 90 % V _{CC}
Output level	V _{pp}	-	0.8 V Min.	Peak to Peak
Output load condition	Load	15 pF	10 kΩ // 10 pF	
Input voltage	V _{IH}	70 % V _{CC} Min.		OE terminal (Enable voltage)
	V _{IL}	30 % V _{CC} Max.		OE terminal (Disable voltage)

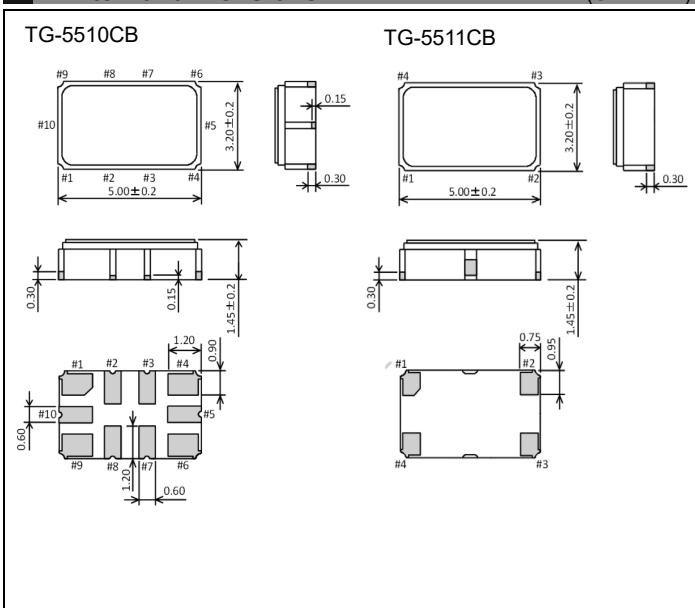
* Note : Please contact us for requirements not listed in this specification.

Product Name **TG-5510CB-*** 38.880000MHz**
 (Standard form) ① ② ③ ④

① Model ② Package type ③ Spec segment (Please contact us) ④ Frequency

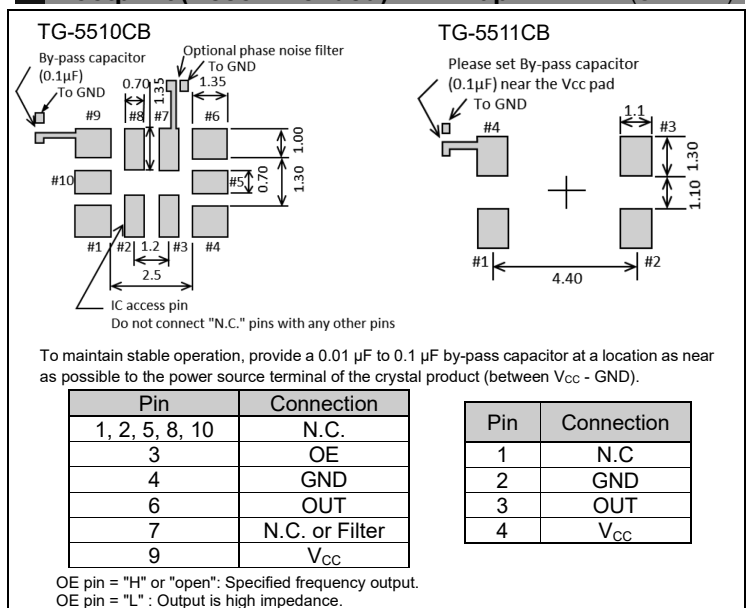
External dimensions

(Unit : mm)



Footprint (Recommended) / Pin Map

(Unit : mm)



TCXO HIGH STABILITY

105 °C HIGH TEMPERATURE

TG5032CKN / SKN

TG5032CMN / SMN



Product Number
 TG5032CKN: X1G006021xxxx14
 TG5032SKN: X1G006031xxxx14
 TG5032CMN: X1G006041xxxx14
 TG5032SMN: X1G006051xxxx14

- Frequency range : 10 MHz to 54 MHz
- Supply voltage : 3.3 V Typ.
- Frequency / temperature characteristics : $\pm 0.1 \times 10^{-6}$ Max. (-40 °C to +105 °C)
- Holdover stability : $\pm 4.6 \times 10^{-6}$ Max. / 20 years (for Stratum3)
- External dimensions : $5.0 \times 3.2 \times 1.45$ mm (10 pins or 4 pins)
- Applications : Network synchronization, Stratum3, BTS, SyncE, IEEE1588, Microwave
- Features : 105 °C High temp, High stability



TG5032CKN
TG5032SKN
(10 pins)



TG5032CMN
TG5032SMN
(4 pins)

Specifications (characteristics)

Item	Symbol	CMOS	Clipped sine wave	Condition
Output frequency range	fo	10 MHz to 54 MHz		Please contact us about available frequencies.
Supply voltage	V _{CC}	3.3 V \pm 5 %		
Storage temperature range	T _{stg}	-40 °C to +105 °C		Storage as single product.
Operating temperature range	T _{use}	-40 °C to +105 °C		
a) Frequency tolerance	f _{tol}	$\pm 1.0 \times 10^{-6}$ Max.		After reflow, +25 °C
b) Frequency/temperature characteristics	fo-Tc	$\pm 0.1 \times 10^{-6}$ Max.		-40 °C to +105 °C
c) Frequency/load coefficient	fo-Load	$\pm 0.1 \times 10^{-6}$ Max.		Load \pm 10 %
d) Frequency/voltage coefficient	fo-V _{CC}	$\pm 0.1 \times 10^{-6}$ Max.		V _{CC} \pm 5 %
e) Frequency aging	f _{age}	$\pm 0.5 \times 10^{-6}$ Max.		+25 °C, First year
		$\pm 3.0 \times 10^{-6}$ Max.		+25 °C, 20 years
Wander generation (MTIE, TDEV)	Compliant with GR-1244CORE, ITU-T G.8262.1, G.8273.2			
Holdover stability (Free-run accuracy)	f _{hos}	$\pm 4.6 \times 10^{-6}$ Max. / 20 years		This includes Item a), b), c), d) and e)
Current consumption	I _{CC}	7.0 mA Max.	6.0 mA Max.	10 MHz \leq fo \leq 26 MHz
		9.0 mA Max.		26 MHz < fo \leq 40 MHz
		10.0 mA Max.		40 MHz < fo \leq 54 MHz
Symmetry	SYM	45 % to 55 %	-	GND level (DC cut)
Output voltage	V _{OH} V _{OL} V _{pp}	90 % V _{CC} Min.	-	
		10 % V _{CC} Max.	-	
		-	0.8 V Min.	Peak to Peak
Rise time / Fall time	tr/ff	8.0 ns Max.	-	10 % V _{CC} to 90 % V _{CC} level, Load: 15 pF
Start-up time	t _{str}	5 ms. Max.		t = 0 at 90 % V _{CC}
Output load condition	Load	15 pF	10 k Ω // 10 pF	
Input voltage	V _{IH}	70 % V _{CC} Min.		OE terminal (Enable voltage)
	V _{IL}	30 % V _{CC} Max.		OE terminal (Disable voltage)

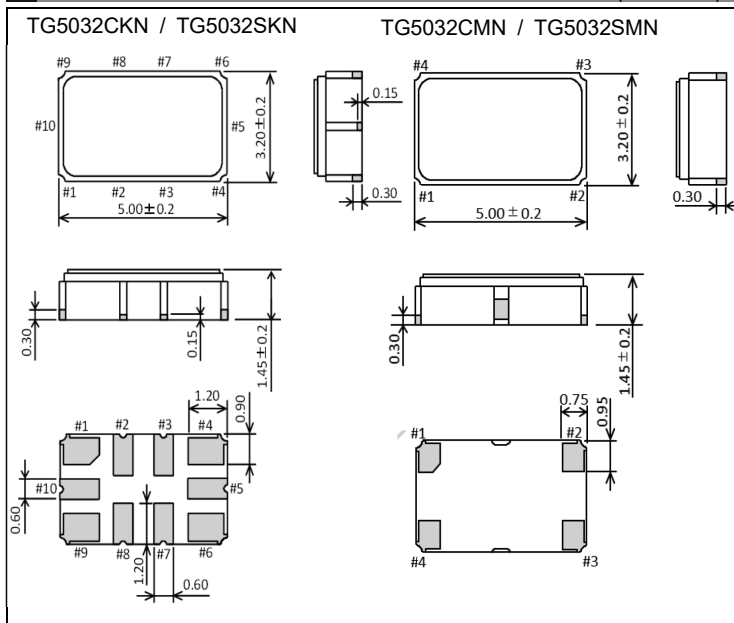
* Note : Please contact us for requirements not listed in this specification.

Product Name TG5032CKN 38.880000MHz C A H H G A
 (Standard form) ① ②③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩

- ①Model ②Output (C: CMOS, S: Clipped sine wave) ③Package type (K: 10 pins, M: 4 pins) ④Frequency ⑤Supply voltage (C: 3.3 V Typ.)
 ⑥Frequency / temperature characteristics (A: $\pm 0.1 \times 10^{-6}$ Max.) ⑦Operating temperature (H: -40 °C to +105 °C)
 ⑧OE function (H: Active High, N: Non) ⑨Filter function (G: Filter-ON, N: NO-Filter) ⑩Internal identification code ("A" is default)

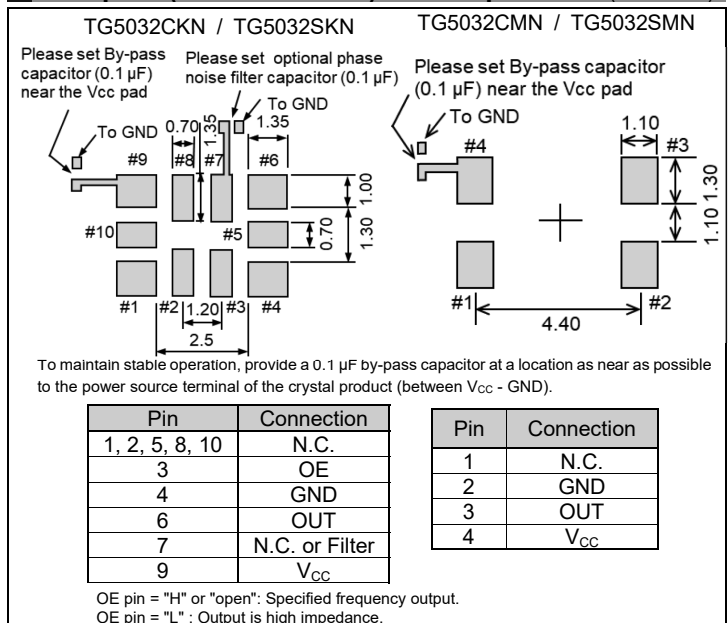
External dimensions

(Unit : mm)



Footprint (Recommended) / Pin Map

(Unit : mm)



TCXO / VC-TCXO
HIGH STABILITY



Product Number
TG5032CFN : X1G005391xxxxxx
TG5032SFN : X1G005401xxxxxx

TG5032CFN / SFN

- Frequency range : 10 MHz to 40 MHz
- Supply voltage : 3.3 V Typ.
- Frequency / temperature characteristics : $\pm 0.1 \times 10^{-6}$ Max. (-40 °C to +85 °C)
- Frequency aging : $\pm 3.0 \times 10^{-6}$ Max. / 20years
- External dimensions: 5.0 × 3.2 × 1.45 mm (4 pins)
- Applications : Small Cells, Stratum3, SyncE, IEEE1588
- Features : High stability, Wide temperature range



Specifications (characteristics)

Item	Symbol	TG5032CFN (CMOS output)		TG5032SFN(Clipped sine wave)		Conditions / Remarks
		TCXO	VC-TCXO	TCXO	VC-TCXO	
Output frequency range	f _o	10 MHz to 40 MHz				Standard frequency
Supply voltage	V _{CC}	10, 12.8, 19.2, 20, 24.576, 25, 25.6, 26, 30.72, 38.4, 38.88, 40 MHz				
Storage temperature range	T _{stg}	C: 3.3 V ± 5 % (Supply voltage range: 2.375 V to 3.63 V)				Storage as single product
Operating temperature range	T _{use}	-40 °C to +90 °C				Standard temp. range
a) Frequency tolerance	f _{tol}	G: -40 °C to +85 °C				After reflow, +25 °C
b) Frequency/temperature Characteristics	f _o -Tc	$\pm 1.0 \times 10^{-6}$ Max.				Reference to (f _{max} + f _{min}) / 2
c) Frequency/load coefficient	f _o -Load	A: $\pm 0.1 \times 10^{-6}$ Max. / -40 °C to +85 °C				
d) Frequency/voltage coefficient	f _o -V _{CC}	H: $\pm 0.25 \times 10^{-6}$ Max. / -40 °C to +85 °C				
e) Frequency aging	f _{age}	B: $\pm 0.28 \times 10^{-6}$ Max. / -40 °C to +85 °C				Load ± 10 %
Holdover stability (Constant temperature)	-	$\pm 0.1 \times 10^{-6}$ Max.				V _{CC} ± 5%
Wander generation (MTIE, TDEV)	-	$\pm 0.1 \times 10^{-6}$ Max.				+25 °C, First year
Free-run accuracy	-	$\pm 0.5 \times 10^{-6}$ Max.				+25 °C, 20 years
Current consumption	I _{CC}	$\pm 3.0 \times 10^{-6}$ Max.				After 10 days of continuous operation.
Input resistance	R _{in}	$\pm 0.01 \times 10^{-6}$ Max. (+25 °C, 24 hours)				After 48 hours of continuous operation.
Frequency control range	f _{cont}	$\pm 0.04 \times 10^{-6}$ Max. (+25 °C, 24 hours)				Compliant with GR-1244CORE, ITU-T G.8262
Frequency change polarity	-	-				This includes Item a), b), c), d) and e)
Symmetry	SYM	$\pm 4.6 \times 10^{-6}$ Max.				10 MHz ≤ f _o ≤ 26 MHz
Output voltage	V _{OH} V _{OL}	5.0 mA Max. 6.0 mA Max.				26 MHz < f _o ≤ 40 MHz
Output level	V _{pp}	-				V _{CC} - GND (DC)
Rise time / Fall time	tr/tf	-				D : V _c = 1.5 V ± 1.0 V at V _{CC} = 3.3 V
Start-up time	t _{str}	-				E : V _c = 1.65 V ± 1.0 V at V _{CC} = 3.3 V
Output load condition	Load	15 pF				50 % V _{CC} level, L_CMOS ≤ 15 pF

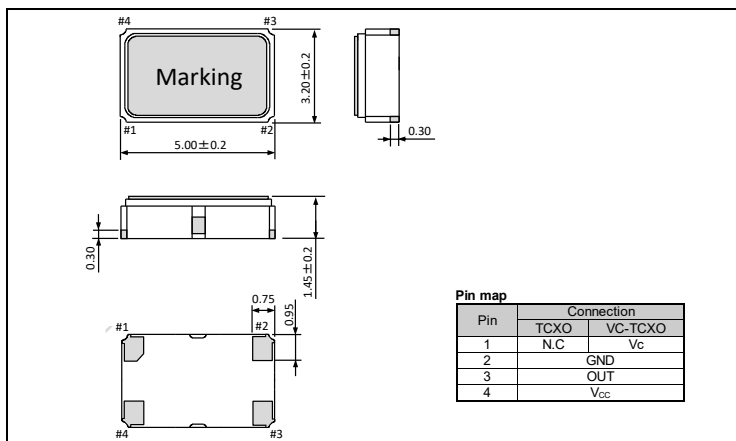
* Note : Please contact us for requirements not listed in this specification.

Product Name **TG5032 C FN 30.720000MHz C A G N D A**
(Standard form) ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨

- ① Model ② Output (C: CMOS, S: Clipped sine wave) ③ Frequency ④ Supply voltage (C: 3.3 V Typ.)
- ⑤ Frequency / temperature characteristics (A: $\pm 0.1 \times 10^{-6}$ Max., H: $\pm 0.25 \times 10^{-6}$ Max., B: $\pm 0.28 \times 10^{-6}$ Max.)
- ⑥ Operating temperature (G: -40 °C to +85 °C) ⑦ OE function (N: Non)
- ⑧ V_c function (A: V_c = any, D: V_c = 1.5 V, E: V_c = 1.65 V, N: Non) ⑨ Internal identification code ("A" is default)

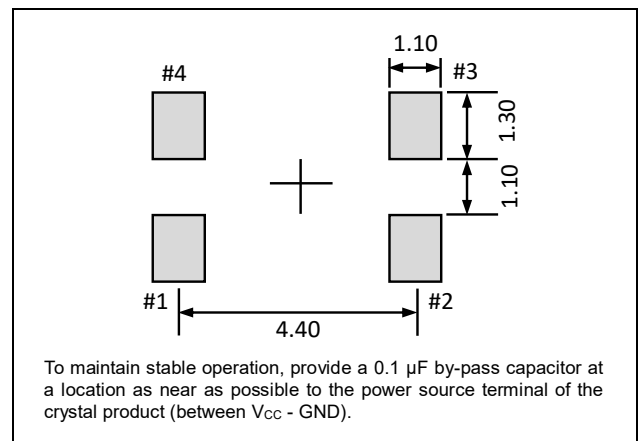
External dimensions

(Unit : mm)



Footprint (Recommended)

(Unit: mm)



TCXO / VC-TCXO
HIGH STABILITY



Product Number
TG5032CGN : X1G005231xxxxxx
TG5032SGN : X1G005241xxxxxx

TG5032CGN / SGN

- Frequency range : 10 MHz to 40 MHz
- Supply voltage : 3.3 V Typ.
- Frequency / temperature characteristics : $\pm 0.1 \times 10^{-6}$ Max. (-40 °C to +85 °C)
- Frequency aging : $\pm 3.0 \times 10^{-6}$ Max. / 20 years
- External dimensions : 5.0 × 3.2 × 1.45 mm (10 pins)
- Applications : Small Cells, Stratum3, SyncE, IEEE1588
- Features : High stability, Wide temperature range



TG5032CGN (CMOS)



TG5032SGN (Clipped Sine)

Specifications (characteristics)

Item	Symbol	TG5032CGN (CMOS)		TG5032SGN(Clipped sine wave)		Conditions / Remarks
		TCXO	VC-TCXO	TCXO	VC-TCXO	
Output frequency range	f _o	10 MHz to 40 MHz				Standard frequency
Supply voltage	V _{CC}	10, 12.8, 19.2, 20, 24.576, 25, 25.6, 26, 30.72, 38.4, 38.88, 40 MHz				
Storage temperature range	T _{stg}	C: 3.3 V ± 5 % (Supply voltage range: 2.375 V to 3.63 V)				Storage as single product
Operating temperature range	T _{use}	-40 °C to +90 °C				
a) Frequency tolerance	f _{tol}	G: -40 °C to +85 °C				After reflow, +25 °C
b) Frequency/temperature Characteristics	f _o -T _c	$\pm 1.0 \times 10^{-6}$ Max.				
c) Frequency/load coefficient	f _o -Load	A: $\pm 0.1 \times 10^{-6}$ Max. / -40 °C to +85 °C H: $\pm 0.25 \times 10^{-6}$ Max. / -40 °C to +85 °C B: $\pm 0.28 \times 10^{-6}$ Max. / -40 °C to +85 °C				Reference to (f _{max} + f _{min}) / 2
d) Frequency/voltage coefficient	f _o -V _{CC}	$\pm 0.1 \times 10^{-6}$ Max.				
e) Frequency aging	f _{age}	$\pm 0.5 \times 10^{-6}$ Max.				+25 °C, First year
Holdover stability (Constant temperature)	-	$\pm 3.0 \times 10^{-6}$ Max.				
Wander generation (MTIE, TDEV)	-	$\pm 0.01 \times 10^{-6}$ Max. (+25 °C, 24 hours)				After 10 days of continuous operation.
Free-run accuracy	-	$\pm 0.04 \times 10^{-6}$ Max. (+25 °C, 24 hours)				
Current consumption	I _{CC}	5.0 mA Max.		5.0 mA Max.		Compliant with GR-1244CORE, ITU-T G.8262
Input resistance	R _{in}	6.0 mA Max.		-		
Frequency control range	f _{cont}	-	100 kΩ Min.	-	100 kΩ Min.	10 MHz ≤ f _o ≤ 26 MHz 26 MHz < f _o ≤ 40 MHz
Frequency change polarity	-	-	±5 × 10 ⁻⁶ to ±10 × 10 ⁻⁶	-	±5 × 10 ⁻⁶ to ±10 × 10 ⁻⁶	
Symmetry	SYM	-	Positive polarity	-	Positive polarity	50 % V _{CC} level, L_CMOS ≤ 15 pF
Output voltage	V _{OH} V _{OL}	45 % to 55 %		-		
Output level	V _{pp}	90 % V _{CC} Min.		-		10 % V _{CC} to 90 % V _{CC} level, Load: 15 pF
Rise time / Fall time	tr/tf	10 % V _{CC} Max.		-		
Start-up time	t _{str}	8.0 ns Max.		0.8 V Min.		OE terminal (Enable voltage) OE terminal (Disable voltage)
Output load condition	Load	5.0 ms Max. (Non-Filter: Standard) / 2.0 sec. Max. (Filter: Option)		10 kΩ // 10 pF		
Input voltage	V _{IH} V _{IL}	15 pF		70 % V _{CC} Min. 30 % V _{CC} Max.		

* Note : Please contact us for requirements not listed in this specification.

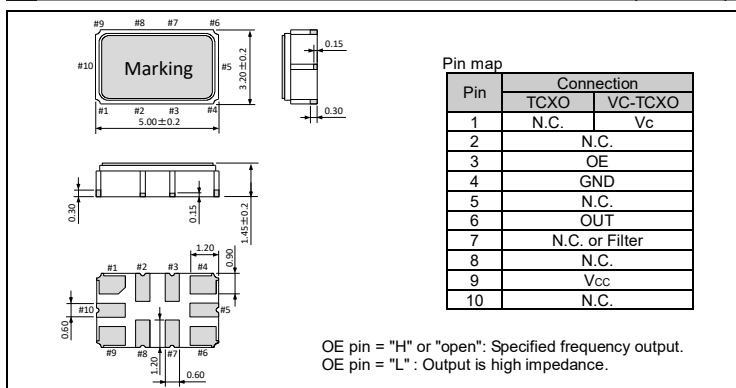
Product Name TG5032 C GN 30.720000MHz C A G H D A
(Standard form) ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨

- ① Model ② Output (C: CMOS, S: Clipped sine wave)
- ③ Frequency ④ Supply voltage (C: 3.3 V Typ)
- ⑤ Frequency / temperature characteristics (A: $\pm 0.1 \times 10^{-6}$ Max., H: $\pm 0.25 \times 10^{-6}$ Max., B: $\pm 0.28 \times 10^{-6}$ Max.)
- ⑥ Operating temperature (G: -40 °C to +85 °C) ⑦ OE function (H: Active High)
- ⑧ V_c function (Refer to symbol table) ⑨ Internal identification code ("A" is default)

V _c [V]	Non	1.5	1.65	Any
Non Filter	N	D	E	A
Filter ON	G	J	K	F

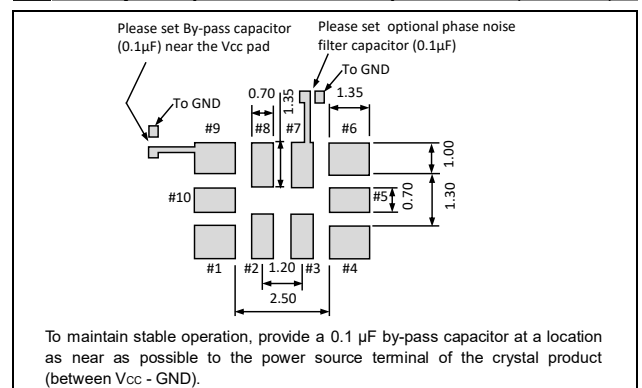
External dimensions

(Unit: mm)

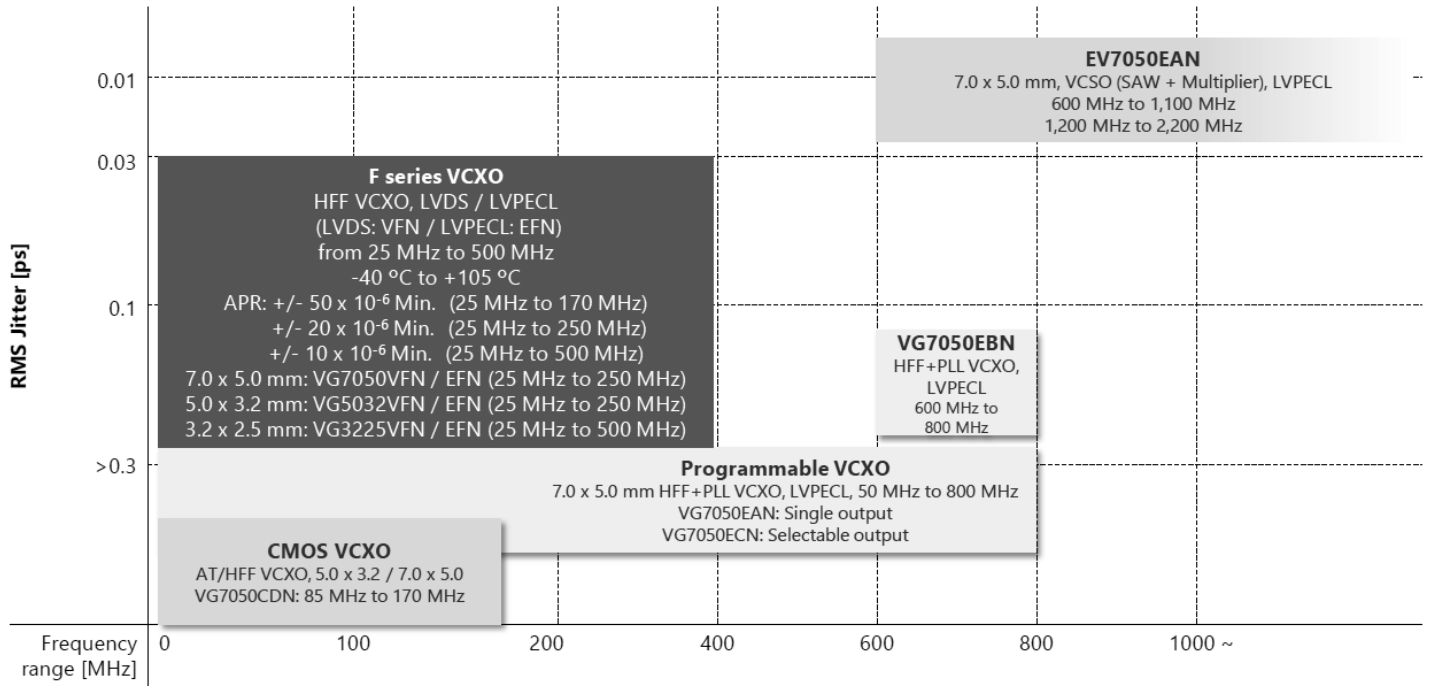


Footprint (Recommended)

(Unit: mm)



VCXO / VCSO



► Output: CMOS

P	Model	Size [mm]	Nominal frequency range	Frequency tolerance (× 10 ⁻⁶)	Operating temperature [°C]	Absolute pull range (× 10 ⁻⁶)	Supply voltage [V]	Current consumption Max. [mA]	Output Load condition [pF]	Output control
93	VG7050CDN	7.0×5.0×1.6t	85 MHz 170MHz	±50	-40 to +85 -40 to +105	±50	3.3±0.165	30	15	OE

► Output: LV-PECL

P	Model	Size [mm]	Nominal frequency range	Frequency tolerance (× 10 ⁻⁶)	Operating temperature [°C]	Absolute pull range (× 10 ⁻⁶)	Supply voltage [V]	Current consumption Max. [mA]	Output Load condition [pF]	Output control
94	VG3225EFN VG5032EFN VG7050EFN	3.2×2.5×1.05t 5.0×3.2×1.3t 7.0×5.0×1.5t	VG3225EFN 25 MHz 500MHz VG5032EFN/VG7050EFN 25 MHz 250MHz	±50	-40 to +85 -40 to +105	±50 ~170 MHz ±20 ~250 MHz ±50 ~170 MHz ±20 ~250 MHz	3.3±0.165	60	50	OE
95	VG7050EBN	7.0×5.0×1.5t	600 MHz 800 MHz	±50	-40 to +85	±50 ±100	3.3±0.33 2.5 ±0.125 3.3±0.33 2.5±0.125	90	50	OE
Web site	EV7050EAN	7.0×5.0×1.6t	600MHz 1100MHz 1200MHz 2200MHz	±100 ±120	-10 to +85 -40 to +85	±50 ±30	3.3 ±0.165	115 175	50	OE
96	VG7050EAN (Programmable 1 setup Freq.)	7.0×5.0×1.5t	50 MHz 800 MHz	±50	-40 to +85	0 to ±180	3.3 ±0.165 2.5 ±0.125	90	50	OE/ST (Active High or Low)
	VG7050ECN (Programmable 4 setup Freq.)	7.0×5.0×1.5t	50 MHz 800 MHz	±50	-40 to +85	0 to ±180	3.3±0.165 2.5 ±0.125	90	50	OE (Active High or Low)

► Output: LVDS

P	Model	Size [mm]	Nominal frequency range	Frequency tolerance (× 10 ⁻⁶)	Operating temperature [°C]	Absolute pull range (× 10 ⁻⁶)	Supply voltage [V]	Current consumption Max. [mA]	Output Load condition [pF]	Output control
94	VG3225VFN VG5032VFN VG7050VFN	3.2×2.5×1.05t 5.0×3.2×1.3t 7.0×5.0×1.5t	VG3225VFN 25 MHz 500MHz VG5032VFN/VG7050VFN 25 MHz 250MHz	±50	-40 to +85 -40 to +105	±50 ~170MHz ±20 ~250MHz ±50 ~170MHz ±20 ~250MHz	3.3 ±0.165	25	100	OE



VOLTAGE -CONTROLLED CRYSTAL OSCILLATOR (VCXO)

Product Number
X1G004861xxxx00

VG7050CDN

- Frequency range : 85 MHz to 170 MHz
- Supply voltage : 3.3 V
- Absolute pull range : $\pm 50 \times 10^{-6}$ min.
- External dimensions : $7.0 \times 5.0 \times 1.5$ mm (t: Typ.)
- Operation temperature : +85 °C / +105 °C
- Function : Output enable(OE)
- Output : CMOS



Specifications (characteristics)

Item	Symbol	Specifications	Remarks
Output frequency range	fo	85 MHz to 170 MHz	Please contact us for inquiries regarding available frequencies.
Supply voltage	Vcc	3.3 V \pm 0.165 V	
Storage temperature range	T_stg	-55 °C to +125 °C	Store as bare product after unpacking
Operating temperature range	T_use	G: -40 °C to +85 °C, H: -40 °C to +105 °C	
Frequency tolerance	f_tol	$\pm 50 \times 10^{-6}$ Max.	Includes initial tolerance, temperature change, Vcc change and 10 years aging at +25 °C. At Vc = 1.65 V, reference to fo
Current consumption	Icc	30 mA Max.	L_CMOS = 15 pF
Absolute pull range*1	APR	$\pm 50 \times 10^{-6}$ Min.	Vc = 1.65 V \pm 1.65 V
Input resistance	Rin	10 M Ω Min.	DC level
Frequency change polarity	—	Positive slope	Vc = 0 V to 3.3 V
Symmetry	SYM	45 % to 55 %	50 % Vcc level
High output voltage	VOH	90 % Vcc Min.	
Low output voltage	VoL	10 % Vcc Max.	
Output load condition (CMOS)	L_CMOS	15 pF Max.	
Output enable / disable input voltage	VIH	70 % Vcc Min.	VIH or OPEN : Enable
	VIL	30 % Vcc Max.	VIL or GND : Disable
Rise time / Fall time	tr / tf	2 ns Max.	20 % Vcc to 80 % Vcc level
Start-up time	t_str	10 ms Max.	Time at minimum supply voltage to be 0 s

*1 Absolute pull range = Frequency control range - Frequency tolerance

* Please keep Vc pin open or ground while powering up Vcc.

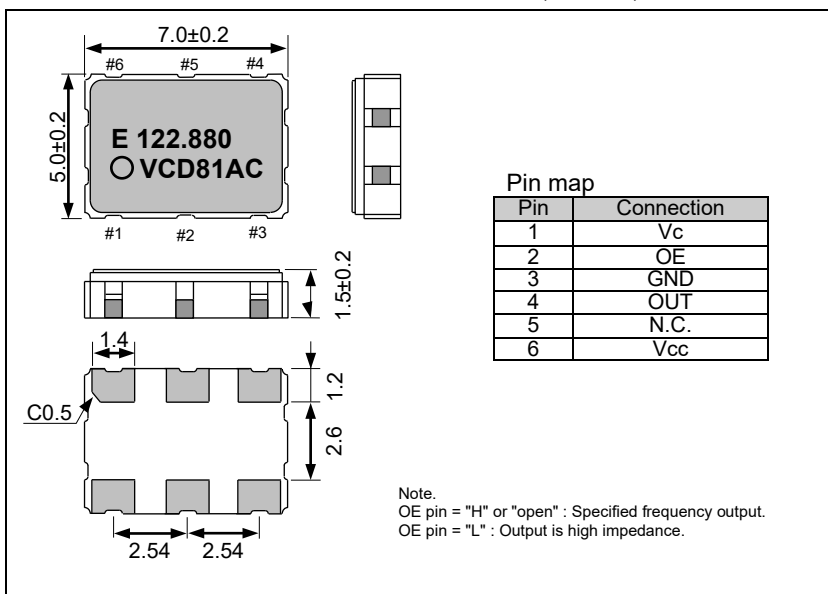
Product name VG7050 CDN 122.880000 MHz C J G H B A
(Standard form) ① ② ③ ④⑤⑥⑦⑧⑨

①Model ②Output (C: CMOS) ③Frequency ④Supply voltage (C: 3.3 V Typ)

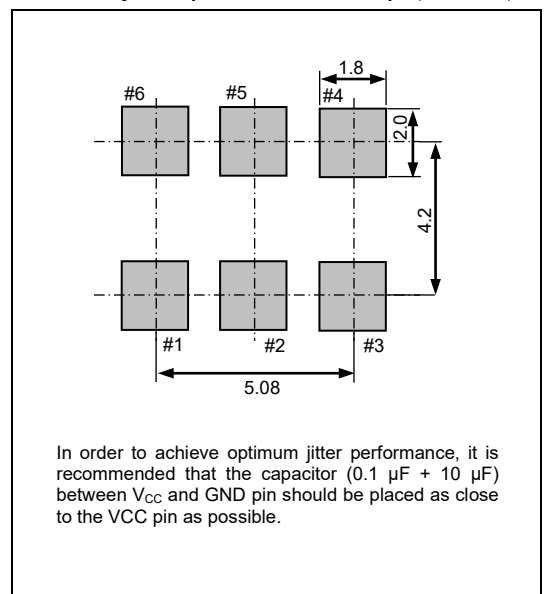
⑤Frequency tolerance (J: $\pm 50 \times 10^{-6}$ Max.) ⑥Operating temperature (G: -40 °C to +85 °C, H: -40 °C to +105 °C)⑦OE Function (H: Active High) ⑧Absolute Pull Range (B: $\pm 50 \times 10^{-6}$ Min.) ⑨Output Standby Type (A: High-Z)

External dimensions

(Unit :mm)



Footprint (Recommended) (Unit :mm)



VOLTAGE-CONTROLLED CRYSTAL OSCILLATOR (VCXO)
OUTPUT : LV-PECL, LVDS



Product Number
 VG3225EFN X1G005361xxxx00
 VG5032EFN X1G005471xxxx00
 VG7050EFN X1G005491xxxx00
 VG3225VFN X1G005461xxxx00
 VG5032VFN X1G005481xxxx00
 VG7050VFN X1G005501xxxx00

VG3225EFN / VFN
 VG5032EFN / VFN
 VG7050EFN / VFN

- Frequency range : 25 MHz to 500 MHz (VG3225EFN / VG3225VFN)
25 MHz to 250 MHz (All other)
- Supply voltage : 3.3 V Typ.
- Output : LV-PECL or LVDS
- Function : Output enable (OE)
- Absolute pull range : $\pm 10 \times 10^{-6}$ Min. / $\pm 20 \times 10^{-6}$ Min. / $\pm 50 \times 10^{-6}$ Min
- Operating temperature : -40 °C to +85 °C / -40 °C to +105 °C

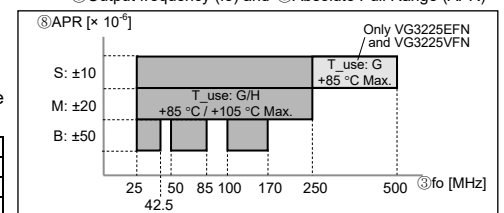


Specifications (characteristics)

Item	Symbol	Specifications		Conditions / Remarks
		LV-PECL VG3225EFN / VG5032EFN / VG7050EFN	LVDS VG3225VFN / VG5032VFN / VG7050VFN	
Output frequency range	f _o	25 MHz to 500 MHz 25 MHz to 250 MHz		VG3225EFN / VG3225VFN All other Please contact us for available frequencies.
Supply voltage	V _{CC}	C: 3.3 V ± 0.165 V		
Control voltage	V _c	1.65 V ± 1.65 V		
Storage temperature range	T _{stg}	-55 °C to +125 °C		
Operating temperature range	T _{use}	G: -40 °C to +85 °C, H: -40 °C to +105 °C		
Frequency tolerance	f _{tol}	J: ±50 × 10 ⁻⁶ Max.		Includes initial frequency tolerance, temperature variation, supply voltage change and 10 years aging (+25 °C) at V _c = 1.65 V
Absolute Pull range *1	APR	B: ±50 × 10 ⁻⁶ Min.		25 MHz ≤ f _o ≤ 42.5 MHz, 50 MHz ≤ f _o ≤ 85 MHz, 100 MHz ≤ f _o ≤ 170 MHz
		M: ±20 × 10 ⁻⁶ Min.		25 MHz ≤ f _o ≤ 250 MHz
		S: ±10 × 10 ⁻⁶ Min.		25 MHz ≤ f _o ≤ 250 MHz
				250 MHz < f _o ≤ 500 MHz, T _{use} : G (-40 °C to +85 °C)
Current consumption	I _{CC}	60 mA Max.	25 mA Max.	OE = V _{CC} , L _{ECL} = 50 Ω or L _{LVDS} = 100 Ω
Disable current	I _{dis}	25 mA Max.	15 mA Max.	OE = GND
Input impedance	Z _{in}	10 MΩ Min.		DC level
Frequency change polarity	-	Positive slope		V _c = 0 V to 3.3 V
Symmetry	SYM	45 % to 55 %		At output crossing point
Output voltage (LV-PECL)	V _{OH}	V _{CC} - 1.1 V Min.		DC characteristics
	V _{OL}	V _{CC} - 1.5 V Max.		
Output voltage (LVDS)	V _{OD}	250 mV to 450 mV		Differential output voltage, V _{OD1} , V _{OD2} DC characteristics
	V _{OS}	1.15 V to 1.35 V		
ECL load condition	L _{ECL}	50 Ω		Terminated to V _{CC} - 2.0 V
LVDS load condition	L _{LVDS}	100 Ω		Connected between OUT to $\overline{\text{OUT}}$
Input voltage	V _{IH}	70 % V _{CC} Min.		OE terminal
	V _{IL}	30 % V _{CC} Max.		
Rise/Fall times	tr / tf	0.5 ns Max.	0.3 ns Max.	LV-PECL: Between 20 % and 80 % of (V _{OH} - V _{OL}) LVDS: Between 20 % and 80 % of Differential Output peak to peak voltage
Startup time	t _{str}	10 ms Max.		Time at minimum supply voltage to be 0 s
Phase Jitter	tp _J	120 fs Max.	160 fs Max.	f _o = 122.88 MHz
		80 fs Max.	80 fs Max.	f _o = 245.76 MHz
		70 fs Max.	80 fs Max.	f _o = 491.52 MHz

*1 Absolute pull range = Frequency control range - Frequency tolerance
 * Please keep V_c pin open or ground while powering up V_{CC}.

Figure 1 Available combination of
 ③ Output frequency (f_o) and ⑤ Absolute Pull Range (APR)



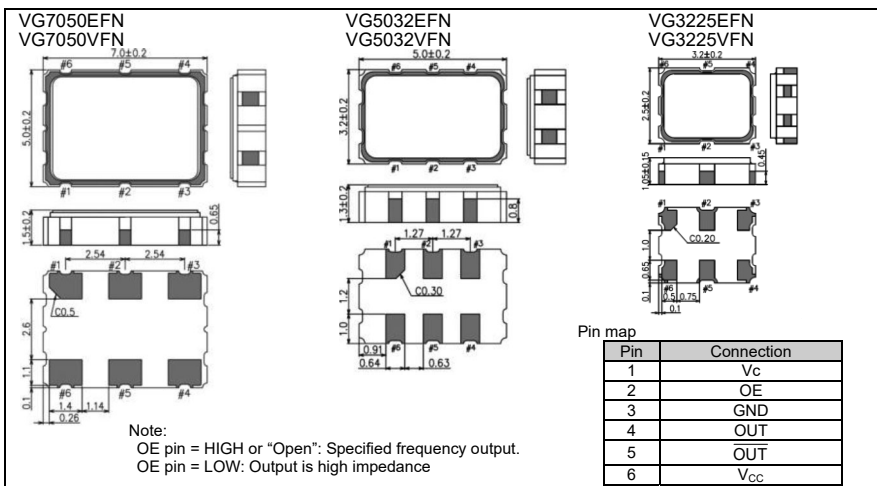
Product Name VG3225 EFN 122.880000MHz C J G H B A
 (Standard form) ① ② ③ ④⑤⑥⑦⑧⑨

- ① Model ② Output (E: LV-PECL, V: LVDS) ③ Frequency
- ④ Supply voltage (C: 3.3 V Typ.) ⑤ Frequency tolerance ⑥ Operating temperature
- ⑦ OE Function ⑧ Absolute Pull Range ⑨ Output Standby Type (A: High-Z)

⑤ Frequency tolerance J ±50 × 10 ⁻⁶	⑥ Operating temperature G -40 to +85 °C H -40 to +105 °C	⑦ OE Function H Active High	⑧ Absolute Pull Range B ±50 × 10 ⁻⁶ M ±20 × 10 ⁻⁶ S ±10 × 10 ⁻⁶
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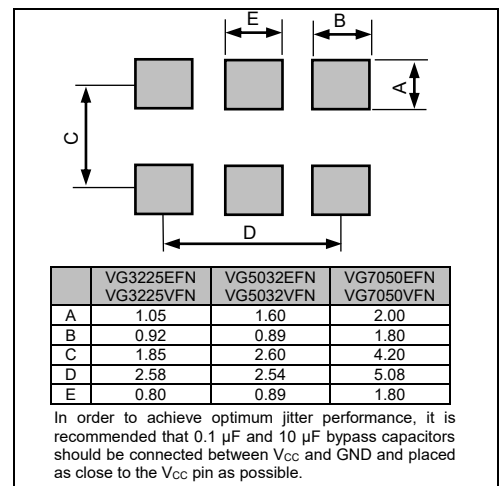
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



VOLTAGE-CONTROLLED CRYSTAL OSCILLATOR (VCXO)

OUTPUT: LV-PECL



Product Number
X1G004551xxxx00

VG7050EBN

- Frequency range : 600 MHz to 800 MHz
- Supply voltage : 2.5 V / 3.3 V
- External dimensions : 7.0 × 5.0 × 1.5 mm (6 pins)
- Absolute Pull Range : ±50 × 10⁻⁶Min., ±100 × 10⁻⁶Min.
- Function : Output Enable(OE)
- Output : LV-PECL



Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
Output frequency range	fo	600.000 MHz to 800.000 MHz	Please contact us about available frequencies.
		698.8123 MHz, 753.6211 MHz, 794.7278 MHz	Standard frequency
Supply voltage	Vcc	D: 2.5 V ± 0.125 V, C: 3.3 V ± 0.33 V	
Storage temperature range	T_stg	-55 °C to +125 °C	Store as bare product after packing
Operating temperature range	T_use	G: -40 °C to +85 °C	
Frequency tolerance*1	f_tol	J: ±50 × 10 ⁻⁶ Max.	Includes frequency aging (10 years)
Current consumption	Icc	90 mA Max.	
Absolute pull range	APR	B: ±50 × 10 ⁻⁶ Min. C: ±100 × 10 ⁻⁶ Min.	Vc = 1.65 V ±1.35 V (Vcc = 3.3 V) Vc = 1.25 V ±1.00 V (Vcc = 2.5 V)
Input resistance	Rin	5 MΩ Min.	DC level
Frequency change polarity	-	Positive slope	Vc = 0 V to Vcc
Symmetry	SYM	45 % to 55 %	at outputs cross point
High output voltage	VOH	Vcc - 1.025 V Min.	DC characteristics
Low output voltage	VOL	Vcc - 1.62 V Max.	
Output load condition	L_ECL	50 Ω	Terminated to Vcc - 2.0 V
High input voltage	VIH	70 % Vcc Min.	
Low input voltage	VIL	30 % Vcc Max.	
Rise/Fall times	tr / tf	0.4 ns Max.	between 20 % and 80 % of (VOH - VOL)
Oscillation start up time	t_str	10 ms Max.	Time at minimum supply voltage to be 0 s
Phase Jitter	tpj	0.2 ps Typ.	Offset Frequency 12 kHz to 20 MHz

*1 Frequency tolerance includes initial frequency tolerance, temperature variation, supply voltage change and reflow drift and 10 years aging at +25 °C.

Product name VG7050 EBN 698.812335MHz C J G H B Z

(Standard form)

① ② ③ ④⑤⑥⑦⑧⑨

①Model ②Output (E: LV-PECL) ③Frequency ④Supply voltage (C: 3.3 V Typ., D: 2.5 V Typ.)

⑤Frequency tolerance (J: ±50 × 10⁻⁶ Max.) ⑥Operating temperature (G: -40 to +85°C)

⑦OE Function (H: Active High, L: Active Low) ⑧Absolute Pull Range (B: ±50 × 10⁻⁶ Min., C: ±100 × 10⁻⁶ Min.)

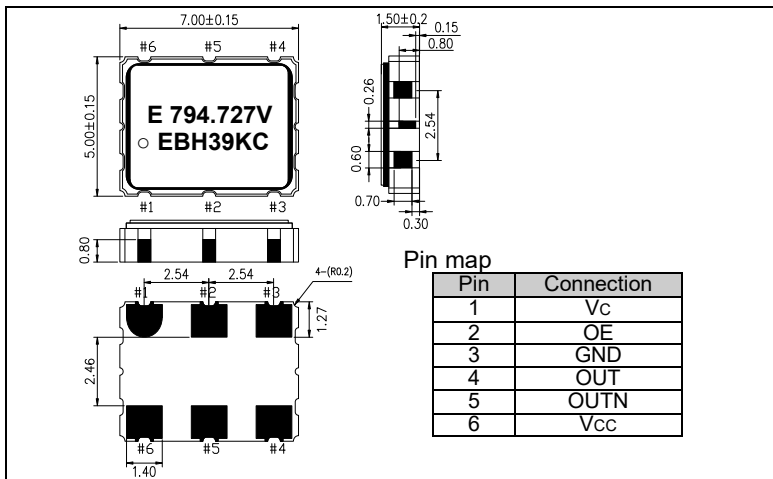
⑨Output Standby Type (F: Fix (OUT = "L", OUTN = "H"), Z: High-Z)

OE Function / OE Standby Type

OE Function	OE Standby Type	Output Enable	Output Disable	
		OE pin	OE pin	OUT, OUTN pin
H: High Active	Z: High-Z	"H" or "OPEN"	"L"	High Impedance
L: Low Active		"L" or "OPEN"	"H"	
H: High Active	F: Fix	"H" or "OPEN"	"L"	OUT = "L", OUTN = "H"
L: Low Active		"L" or "OPEN"	"H"	

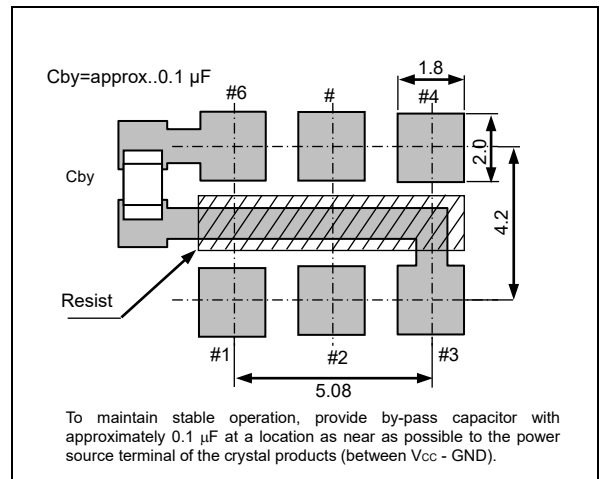
External dimensions

(Unit :mm)



Footprint (Recommended)

(Unit :mm)



**Programmable Voltage Controlled Oscillator (VCXO)****Output: LV-PECL****VG7050EAN / ECN**

Product Number
 EAN : X1G004541xxxx00
 ECN : X1G004561xxxx00

- Frequency range : 50 MHz to 800 MHz
(Tuning resolution: 2.2 to 2.8×10^{-9})
- Supply voltage : 2.5 V / 3.3 V
- External dimensions : EAN : $7.0 \times 5.0 \times 1.5$ mm (8 pins)
ECN : $7.0 \times 5.0 \times 1.5$ mm (10 pins)
- Absolute Pull Range : $\pm 0 \times 10^{-6}$ to $\pm 180 \times 10^{-6}$ (12 steps selectable)

Features

- EAN : User-specified one startup frequency, APR and 7-bit I²C address
- ECN : User-specified four startup frequency, APR and 7-bit I²C address
- User Programming : I²C Interface
- Low jitter PLL technology

Applications

SONET/SDH, OTN, GbE, Fibre Channel

**Specifications (characteristics)**

Item	Symbol	Specifications	Conditions / Remarks
Output frequency range	f _o	50 MHz to 800 MHz	It can be changed by I ² C
Supply voltage	V _{CC}	D: 2.5 V ± 0.125 V, C: 3.3 V ± 0.33 V	
Storage temperature range	T _{stg}	-55 °C to +125 °C	Store as bare product after packing
Operating temperature range	T _{use}	-40 °C to +85 °C	
Frequency tolerance *1	f _{tol}	±50 × 10 ⁻⁶	Includes frequency aging (10 years)
Current consumption	I _{CC}	90 mA Max.	OE Active, L_ECL=50 Ω
Disable current	I _{dis}	40 mA Max.	OE Inactive, Output Standby: Hi-Z mode
		70 mA Max.	OE Inactive, Output Standby: Fix mode
Absolute pull range	APR	±0 × 10 ⁻⁶ to ±180 × 10 ⁻⁶ Min.	V _c = 1.65 V ± 1.35 V (V _{CC} = 3.3 V)
		±0 × 10 ⁻⁶ to ±180 × 10 ⁻⁶ Min.	V _c = 1.25 V ± 1.00 V (V _{CC} = 2.5 V)
Control voltage tuning range	V _c	0 V to V _{CC}	
Frequency change polarity	-	Positive slope	
Symmetry	SYM	45 % to 55 %	At outputs crossing point
Output voltage	V _{OH}	V _{CC} - 1.025 V Min.	DC characteristics
	V _{OL}	V _{CC} - 1.62 V Max.	
Output load condition	L_ECL	50 Ω	Termination to V _{CC} - 2.0 V
Input voltage	V _{IH}	70 % V _{CC} Min.	EAN : OE, SDA and SCL ECN : OE, FSEL0, FSEL1, SDA and SCL
	V _{IL}	30 % V _{CC} Max.	
Rise time / Fall time	t _r / t _f	400 ps Max.	Between 20 % and 80 % of (V _{OH} - V _{OL})
Start-up time	t _{str}	10 ms Max.	Time at minimum supply voltage to be 0 s

*1 Frequency tolerance includes initial frequency tolerance, temperature variation, supply voltage change, reflow drift and 10 years aging at +25 °C.

Product name VG7050 EAN SM18xxxx C J G H P Z
 (Standard form) ① ② ③ ④⑤⑥⑦⑧⑨

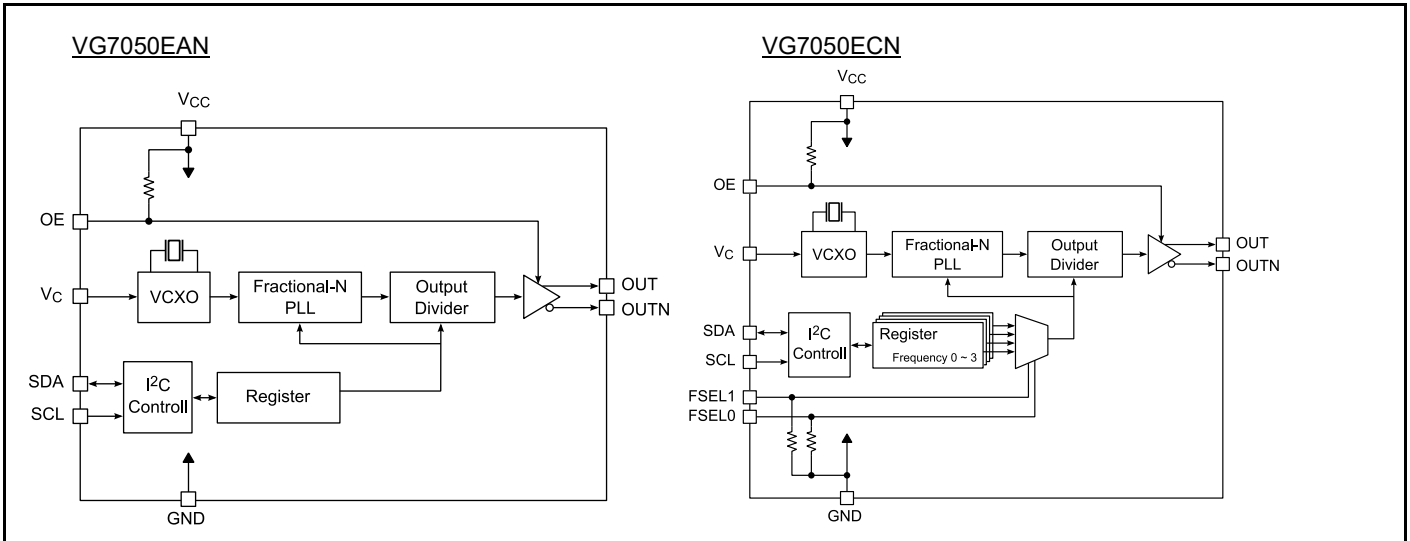
- ① Model
- ② Output (E: LV-PECL)
- ③ Parameter Designator (EAN: SM18xxxx, ECN: SM20xxxx)
- ④ Supply voltage (C: 3.3 V Typ., D: 2.5 V Typ.)
- ⑤ Frequency tolerance (J: ±50 × 10⁻⁶)
- ⑥ Operating temperature (G: -40 °C to +85 °C)
- ⑦ OE Function (H: Active High, L: Active Low)
- ⑧ Absolute Pull Range (P: Programmable)
- ⑨ Output Standby Type (F: Fix (OUT="L", OUTN="H"), Z: High-Z)

Phase Jitter

	Offset Frequency	125.00 MHz	156.25 MHz	250.00 MHz	425.00 MHz	622.08 MHz	669.33 MHz	794.73 MHz
Phase jitter*2 Typ.	12 kHz to 20 MHz	0.30 ps	0.26 ps	0.26 ps	0.25 ps	0.26 ps	0.26 ps	0.26 ps
	20 kHz to 50 MHz	0.30 ps	0.27 ps	0.27 ps	0.26 ps	0.27 ps	0.27 ps	0.27 ps
	50 kHz to 80 MHz	0.29 ps	0.27 ps	0.27 ps	0.26 ps	0.27 ps	0.27 ps	0.27 ps

*2 In order to achieve optimum jitter performance, it is recommended that the capacitor (0.1 μF + 10 μF) between V_{CC} and GND pin should be placed as close to the V_{CC} pin as possible.

Block diagram

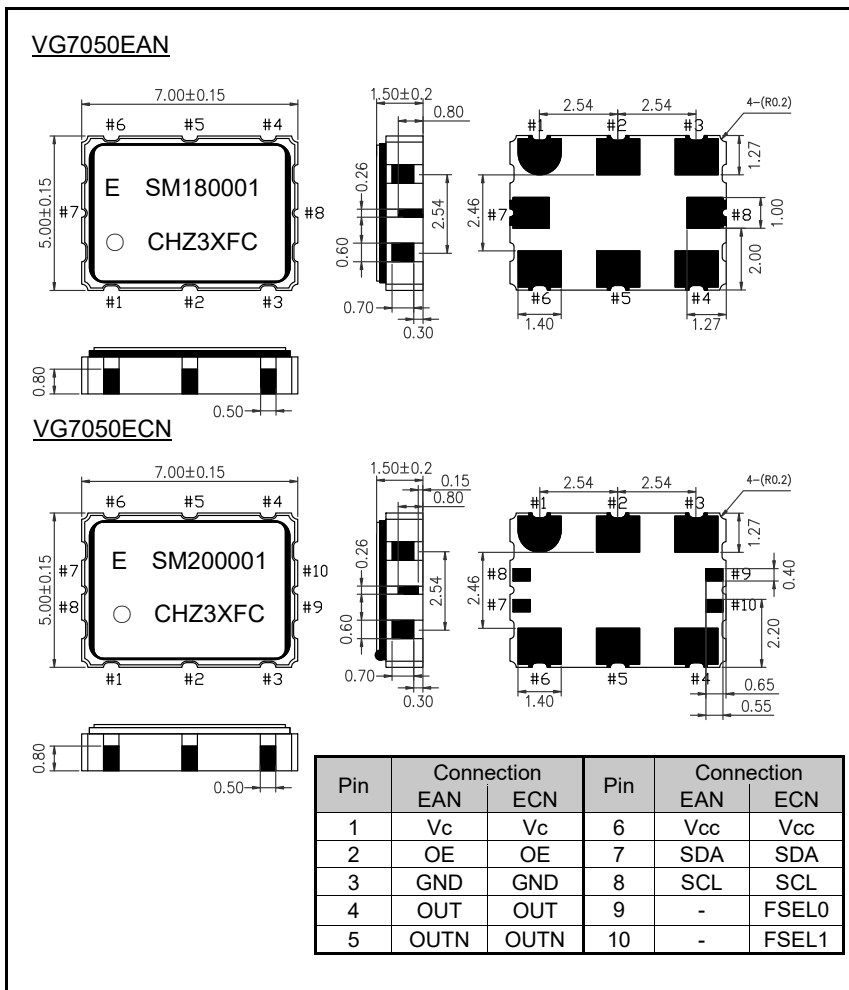


OE Function / OE Standby Type

OE Function	OE Standby Type	Output Enable			Output Disable	
		OE pin	OE pin	OUT, OUTN pin	OE pin	OUT, OUTN pin
H: High Active	Z: High-Z	"H" or "OPEN"	"L"	High Impedance		
L: Low Active		"L" or "OPEN"	"H"			
H: High Active	F: Fix	"H" or "OPEN"	"L"	OUT = "L", OUTN = "H"		
L: Low Active		"L" or "OPEN"	"H"			

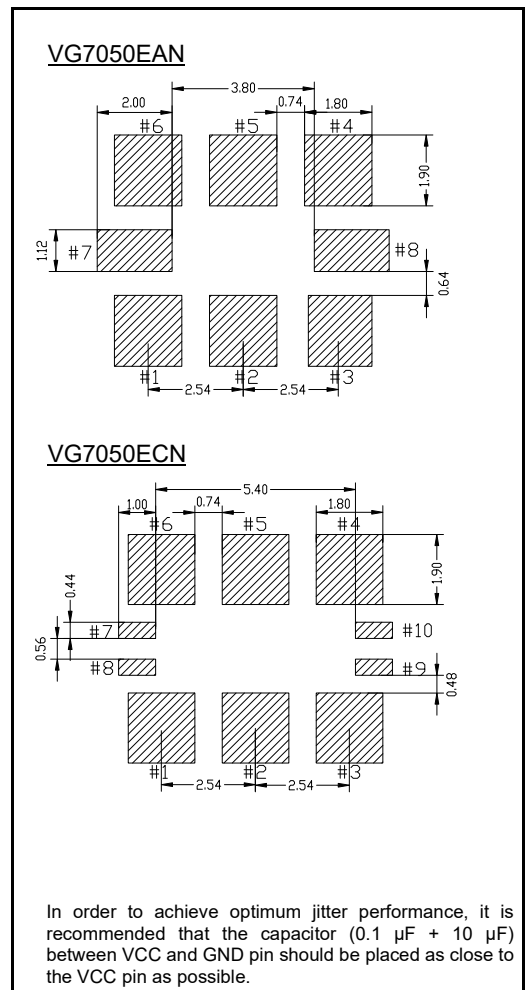
External dimensions

(Unit: mm)



Footprint (Recommended)

(Unit: mm)



RF Transmitter Module
AEC-Q100 compliant
SR3225SAA

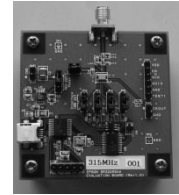


Product Number
SR3225SAA: X1G00479xxxxx00

SR3225SAA is a wireless transmitter module for UHF range. Crystal resonator, oscillator, PLL and Power Amp are integrated in 3.2 mm x 2.5 mm ceramic package. The wireless transmission function can be configured by connect to the external control devices. It is suitable for small wireless transmitter. The evaluation kit is available.

Application

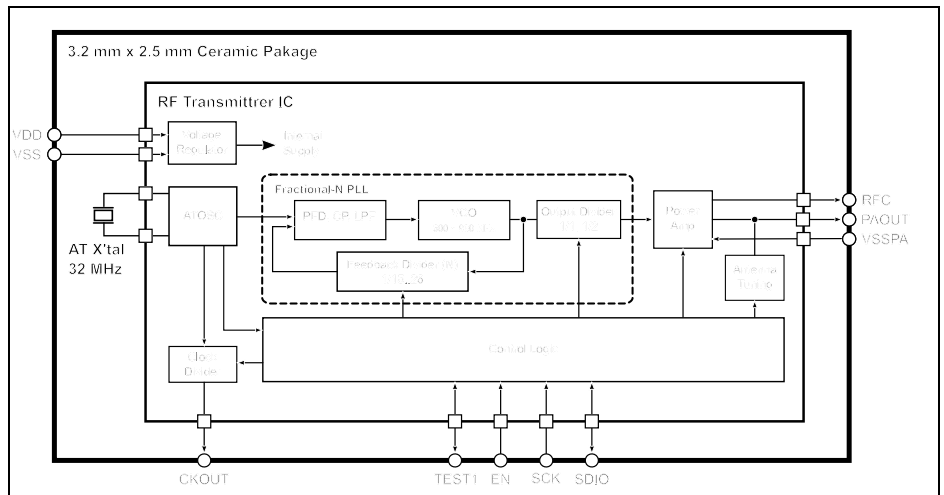
- Remote Keyless Entry, Passive Entry
- Short range radio data transmitter
- Garage door opener
- Transmitter for RFID tag



Overview

- Carrier frequency bands:
300 MHz ~ 465 MHz (0.25 kHz Step),
600 MHz ~ 930 MHz (0.49 kHz Step)
- $\Delta\Sigma$ fractional-N based PLL
- Programmable Power Amp output power:
-15 ~ 11 dBm, 128 steps
- Modulation types: ASK/OOK/FSK with Soft-ASK and/or Soft FSK shaping
- 3-wire/4-wire SPI interface
- SFR (Special Function Register)
- Embedded 32 MHz crystal resonator and oscillation circuit
- Programmable clock output via CKOUT
- Programmable voltage threshold of Under Voltage Detection: 4 steps (1.8 V ~ 2.4 V)
- Fail-Safe mechanism (PLL Loss of Lock, VCO auto-calibration error, Under Voltage Detection)

Block diagram

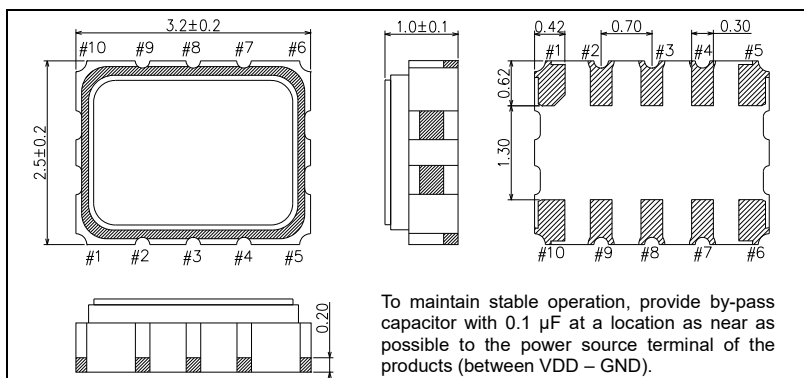


Specification (characteristics)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage	VDD	-	1.8	3.0	3.6	V
Operation temperature range	Ta	-	-40	-	+85	°C
Storage temperature range	Tstg	-	-40	-	+125	°C
Supply current Powerdown Mode	I _{DDPD}	VDD = 3.0 V, Ta = +25 °C	-	20	100	nA
Supply current Transmitter-Active Mode	I _{DDTMA}	F _{TX} = 315 MHz, P _{out} = 5 dBm,	-	10.0	11.0	mA
		F _{TX} = 315 MHz, P _{out} = 8 dBm,	-	12.7	13.7	
		F _{TX} = 315 MHz, P _{out} = 10 dBm,	-	15.0	16.0	
Carrier frequency bands	F _{TX}	-	300	-	465	MHz
		-	600	-	930	
ASK Bit rate	R _{ASK}	NRZ	-	-	100	kbps
FSK Bit rate	R _{FSK}	NRZ	-	-	50	kbps
Crystal frequency tolerance	F _{TOL}	Ta = +25 °C, without aging	-2	-	2	ppm
Crystal temperature variation	F _{TC}	-40 °C ~ +85 °C	-20	-	20	ppm
Nominal output power	P _{OUT}	Ta = +25 °C, VDD = 3.0 V, F _{TX} = 315 MHz, HPWR = 1, AM* = 0x3F	10	11	12	dBm
		Ta = +25 °C, VDD = 3.0 V, F _{TX} = 315 MHz, HPWR = 0, AM* = 0x01	-16	-15	-14	

External dimensions

(Unit: mm)



Pin descriptions

Pin No.	Pin Name	Function
1	TEST1	Test, Transmission data input or SPI interface data output
2	EN	Enable inp or SPI interface chip select
3	SCK	SPI interface clock input
4	SDIO	SPI data input / output or Transmission data input
5	CKOUT	Clock output
6	VSSPA	GND for Power Amp
7	PAOUT	Power Amp output
8	RFC	RF choke coil connect pin
9	VDD	Positive power supply
10	VSS	GND

■ IMU Product Line-up

- M-G370PDS0 : Low Noise and High Stability IMU
- M-G370PDG0 : Wide Dynamic Range and High Stability IMU
- M-G366PDG0 / M-G330PDG0 : Dynamic Tilt Function IMU
- M-G552 Series : Water and Dust Proof IMU with J1939, CANopen and RS422 Interface (Interface is selectable)

Product Name			M-G370PDS0	M-G370PDG0	M-G366PDG0	M-G330PDG0	M-G552 Series*1
Features			Low Noise High Stability	Wide Range High Stability	Standard Model Dynamic Tilt	Basic Model Dynamic Tilt	Water and Dust proof
Gyroscope	Bias Instability	°/h	0.8	0.8	1.2	3	0.8
	Output Range	°/s	± 200	± 450	± 450	± 400	± 450
	Random Walk	°/√h	0.03	0.06	0.08	0.1	0.06
	Band Width	Hz(Max.)	189	189	472	500	189
Accelerometer	Output Range	G	± 10	± 8 / ± 16 *2	± 8 / ± 16 *2	± 8 / ± 16 *2	± 10
	Initial Bias	mG, σ	2	2	3	4	3
Misalignment (Gyro/Accelerometer)		°	0.01				0.01
Current Consumption		mA(Typ.)	16				32 @12 V
Voltage Supply		V	3.3				9 ~ 32
Operating Temperature		°C	- 40 ~ + 85				-30 ~ +80
Interface			SPI / UART				J1939 (CANopen and RS422 is selectable by product code)
Size		mm ³	24 x 24 x 10				65 x 60 x 30
Weight		g	10				115
Dynamic Tilt Function EKF(Extended Kalman Filter)			---	---	Built in	Built in	---
Functions			External Trigger Input, etc				IP67

Please contact us for information on RoHS and Reach.

*1 The product model number and specification values are determined by the built-in IMU.



The values on this page are the specifications of M-G552PJ7x. Please contact us for detailed information on other products.
Interface is selectable by product code.

* 2 Selectable by register setting.



■ Vibration Sensor / Accelerometer Product Line-up

- M-A342VD10 : Built-in Type Vibration sensor (UART / SPI Interface), ISO 10816 and ISO 20816 compliant
- M-A542VR10 : Water and Dust Proof Vibration sensor (RS422 Interface), ISO 10816 and ISO 20816 compliant
- M-A352AD10 : Built-in Type Accelerometer (UART / SPI Interface), Detectable in the range from DC
- M-A552AC10/AR10 : Water and Dust Proof Accelerometer (CAN / RS422 Interface) , Detectable in the range from DC

Vibration Sensor

Product Name		M-A342VD10	M-A542VR10
Features		Built-in Type	Water and Dust Proof (IP67)
Interface		UART / SPI Selectable	RS422
Transfer Speed	bps	115.2 k~921.6 k(Default 460.8 k)	
Detected Axis		3(X/Y/Z)	
Physical Output Formats		Velocity and Displacement	
Output Formats		raw, rms, p-p (digital output)	
Output Range	Velocity mm/s	± 100	
	Displacement mm	± 200	
Measured Frequency Range	Velocity Hz	10 ~ 1,000	
	Displacement Hz	1 ~ 100	
Operating Temperature	°C	-30 ~ +85	-30 ~ +70
Current Consumption	mA(Typ.)	29 @ 3.3 V	51 @12 V
Voltage Supply	V	3.15 ~ 3.45	9 ~ 32
Size	mm ³	48 x 24 x 16	65 x 60 x 30
Weight	g	25	128
Product Image			

Accelerometer

Product Name		M-A352AD10	M-A552AC10 M-A552AR10
Features		Built-in Type	Water and Dust Proof (IP67)
Interface		UART / SPI Selectable	CANopen:M-A552AC10 RS-422 :M-A552AR10
Noise Density	μG/Hz	0.2	0.5
Output Range	G	± 15	± 15
Detected Axis		3(X/Y/Z)	3(X/Y/Z)
Physical Output Formats		Acceleration, Tilt Angle (Selectable on each axis)	Acceleration, Tilt Angle (Selectable on each axis)
Measured Frequency Range	Hz	DC ~ 460	DC ~ 460
Output Rate	Sps(Max.)	1,000	1,000
Resolution	μG/LSB	0.06	0.06
Shock	G(Max.)	1,000	1,000
Operating Temperature	°C	-30 ~ +85	-30 ~ +70
Current Consumption	mA(Typ.)	13.2	35@12 V:M-A552AC10 49@12 V:M-A552AR10
Voltage Supply	V	3.3	9 ~ 32
Size	mm ³	48 x 24 x 16	65 x 60 x 30
Weight	g	25	128
Product Image			

Please contact us for information on RoHS and Reach.

IMU (Inertial Measurement Unit) M-G330PDG0



Product Name and Number
M-G330PDG0 : X2G000201000100



- Small size & Light Weight: 24 x 24 x 10 mm³, 10 g
- Low-Noise, High-Stability
Gyro Bias Instability: 3 °/h
Angular Random Walk: 0.1 °/√h
- Calibrated Stability (Bias, Scale Factor, Axial Alignment)
- Interface: SPI / UART
- Calibration Temperature: -40 °C to +85 °C
- Power Supply Voltage: 3.3 V

Recommended Application

- Autonomous Vehicle
- Navigation Systems
- Vibration Control and Stabilization Pointing and Tracking Systems

RECOMMENDED OPERATING CONDITION

Parameter	Condition	Min.	Typ.	Max.	Unit
Power Supply Voltage, V _{CC}		3.15	3.3	3.45	V
Digital Input Voltage		GND	—	V _{CC}	V
Digital Output Voltage		-0.3	—	V _{CC} + 0.3	V
Calibration Temperature	Performance parameters are applicable	-40	—	+85	°C
Operating Temperature		-40	—	+85	°C

SPECIFICATIONS

T_a = 25 °C, V_{CC} = 3.3 V, Angular rate = 0 °/s, ≤ ±1 G, unless otherwise noted.

Parameter	Test Condition / Comment	Min.	Typ.	Max.	Unit	
GYRO SENSORS						
Sensitivity						
Output Range		—	±400	—	°/s	
Scale Factor	16 bit, when 32 bit x 2 ¹⁶	-0.2 %	66	+0.2 %	LSB/(°/s)	
Nonlinearity	1σ	—	0.05	—	% of FS	
Misalignment	1σ, Axis-to-axis, Δ = 90 ° ideal	—	0.01	—	°	
Bias						
Initial Error	1 σ, -10 °C ≤ T _A ≤ +60 °C 1 σ, -40 °C ≤ T _A ≤ +85 °C	—	720 1800	—	°/h °/h	
Repeatability ^{*1}	1σ, Turn-on to Turn-on ^{*1}	—	36	—	°/h	
Bias Instability	Average	—	3	—	°/h	
Angular Random Walk	Average	—	0.1	—	°/√h	
Noise Density	f = 10 Hz to 20 Hz	—	7	—	(°/h)/√Hz, rms	
Frequency Property						
3dB Bandwidth		—	—	500	Hz	
ACCELEROMETERS						
Sensitivity						
Output Range		—	±8 / ±16 ^{*7}	—	G	
Scale Factor	16 bit, when 32 bit x 2 ¹⁶	-0.2%	4(8 G)/2(16 G)	+0.2%	LSB/mG	
Nonlinearity	1 σ, < 1 G	—	0.1	—	% of FS	
Misalignment	1 σ, Axis-to-Axis, Δ = 90 ° ideal	—	0.01	—	°	
Bisa						
Initial Error	1 σ, -40 °C ≤ T _A ≤ +85 °C	—	4	—	mG	
Repeatability ^{*1}	1σ, Turn-on to Turn-on ^{*1}	—	4	—	mG	
Bias Instability	Average	—	34	—	μG	
Velocity Random Walk	Average	—	0.03	—	(m/s)/√h	
Noise Density	f = 10 Hz to 20 Hz	—	70	—	μG/√Hz, rms	
Frequency Property						
3dB Bandwidth		—	—	333	Hz	
ATTITUDE OUTPUT						
Dynamic Range	Inclination Mode	-80	—	+80	°	
	Euler Mode	ANG1:Roll	-45	—	+45	
		ANG2:Pitch	-180	—	+180	
		ANG3:Yaw ^{*4}	-180	—	+180	
Scale Factor	16bit	—	0.00012207 0.00699411	—	rad/LSB °/LSB	
Accuracy ^{*4*6}	1 σ, Static	—	0.3	—	°	
	1 σ, Dynamic ^{*5} (100 °/s, Max.)	—	0.3	—		
TEMPERATURE SENSOR						
Scale Factor ^{*1*2}	Output = 0 @+25 °C	—	0.00390625	—	°C/LSB	

*1) This is a reference value used for internal temperature compensation. There is no guarantee that the value gives an absolute value of the internal temperature.

*2) This is the temperature scale factor for the upper 16 bit (TEMP_HIGH). *3) Turn-on to turn-on / Day by day, estimated variation during 5 consecutive days.

*4) Yaw axis is not compensated for errors caused by drift.

*5) Dynamic accuracy is based on measurement data that has been measured from a stationary state. The accuracy that can be achieved depends on the input movement.

*6) Attitude output accuracy is based on measurement data for GLOB_CMD2[0x16(W1)],bit[5:4]= 00: modeA. *7) Selectable by register setting.

Note) The values in the specifications are based on the data calibrated at the factory. The values may change according to the way the product is used.

Note) The Typ. values in the specifications are average values or 1 σ values.

Note) Unless otherwise noted, the Max. / Min. values in the specifications are design values or Max. / Min. values at the factory tests.

IMU (Inertial Measurement Unit)
M-G366PDG0



Product Name and Number
M-G366PDG0 : X2G000191000100



- Small size & Light Weight: 24 x 24 x 10 mm³, 10 g
- Low-Noise, High-Stability
 Gyro Bias Instability: 1.2 °/h
 Angular Random Walk: 0.08 °/√h
- Calibrated Stability (Bias, Scale Factor, Axial Alignment)
- Interface: SPI / UART
- Calibration Temperature: -40 °C to +85 °C
- Power Supply Voltage: 3.3 V

Recommended Application

- Autonomous Vehicle
- Navigation Systems
- Vibration Control and Stabilization Pointing and Tracking Systems

RECOMMENDED OPERATING CONDITION

Parameter	Condition	Min.	Typ.	Max.	Unit
Power Supply Voltage, V _{CC}		3.15	3.3	3.45	V
Digital Input Voltage		GND	—	V _{CC}	V
Digital Output Voltage		-0.3	—	V _{CC} + 0.3	V
Calibration Temperature	Performance parameters are applicable	-40	—	+85	°C
Operating Temperature		-40	—	+85	°C

SPECIFICATIONS

T_a = 25 °C, V_{CC} = 3.3 V, Angular rate = 0 °/s, ≤ ±1 G, unless otherwise noted.

Parameter	Test Condition / Comment	Min.	Typ.	Max.	Unit	
GYRO SENSORS						
Sensitivity						
Output Range		—	±450	—	°/s	
Scale Factor	16 bit, when 32 bit x 2 ¹⁶	-0.2 %	66	+0.2 %	LSB/(°/s)	
Nonlinearity	1σ	—	0.05	—	% of FS	
Misalignment	1σ, Axis-to-axis, Δ = 90 ° ideal	—	0.01	—	°	
Bias						
Initial Error	1 σ, -40 °C ≤ T _A ≤ +85 °C	—	360	—	°/h	
Repeatability ^{*1}	1σ, Turn-on to Turn-on ^{*1}	—	36	—	°/h	
Bias Instability	Average	—	1.2	—	°/h	
Angular Random Walk	Average	—	0.08	—	°/√h	
Noise Density	f = 10 Hz to 20 Hz	—	6.9	—	(°/h)/√Hz, rms	
Frequency Property						
3dB Bandwidth		—	—	472	Hz	
ACCELEROMETERS						
Sensitivity						
Output Range		—	±8 / ±16 ^{*7}	—	G	
Scale Factor	16 bit, when 32 bit x 2 ¹⁶	-0.1 %	4(8 G)/2(16 G)	+0.1 %	LSB/mG	
Nonlinearity	1 σ, < 1 G	—	0.1	—	% of FS	
Misalignment	1 σ, Axis-to-Axis, Δ = 90 ° ideal	—	0.01	—	°	
Bisa						
Initial Error	1 σ, -40 °C ≤ T _A ≤ +85 °C	—	3	—	mG	
Repeatability ^{*1}	1σ, Turn-on to Turn-on ^{*1}	—	3	—	mG	
Bias Instability	Average	—	24	—	μG	
Velocity Random Walk	Average	—	0.02	—	(m/s)/√h	
Noise Density	f = 10 Hz to 20 Hz	—	50	—	μG/√Hz, rms	
Frequency Property						
3dB Bandwidth		—	—	333	Hz	
ATTITUDE OUTPUT						
Dynamic Range	Inclination Mode	-80	—	+80	°	
	Euler Mode	ANG1:Roll	-45	—	+45	
		ANG2:Pitch	-180	—	+180	
		ANG3:Yaw ^{*4}	-180	—	+180	
Scale Factor	16bit	—	0.00012207	—	rad/LSB	
Accuracy ^{*4,6}	1 σ, Static	—	0.2	—	°	
	1 σ, Dynamic ^{*5} (100 °/s, Max.)	—	0.2	—		
TEMPERATURE SENSOR						
Scale Factor ^{*1,2}	Output = 0 @+25 °C	—	0.00390625	—	°C/LSB	

*1) This is a reference value used for internal temperature compensation. There is no guarantee that the value gives an absolute value of the internal temperature.

*2) This is the temperature scale factor for the upper 16 bit (TEMP_HIGH). *3) Turn-on to turn-on / Day by day, estimated variation during 5 consecutive days.

*4) Yaw axis is not compensated for errors caused by drift.

*5) Dynamic accuracy is based on measurement data that has been measured from a stationary state. The accuracy that can be achieved depends on the input movement.

*6) Attitude output accuracy is based on measurement data for GLOB_CMD2[0x16(W1)],bit[5:4]= 00: modeA. *7) Selectable by register setting.

Note) The values in the specifications are based on the data calibrated at the factory. The values may change according to the way the product is used.

Note) The Typ. values in the specifications are average values or 1 σ values.

Note) Unless otherwise noted, the Max. / Min. values in the specifications are design values or Max. / Min. values at the factory tests.

3 Axis Vibration Sensor M-A342VD10



Product number
M-A342VD10 : X2F000021000100



- Capable of measuring velocity, velocity RMS, and velocity P-P (ISO10816 / ISO20816 compliant)
- Frequency response characteristics: 10 Hz to 1,000 Hz (-3dB)
- Insensitive to magnetic influences
- High dynamic range: ± 100 mm/s (110 dB)
- 3-axis digital output SPI / UART
- Power consumption : 29 mA Typ.

Recommended Application

- MHM (Machine Health Monitoring) • Condition Based Maintenance (CBM) • Motion analysis and control
- SHM (Structural Health Monitoring) • Vibration analysis, control and stabilization • Lissajous analysis

Recommended Operating Condition

Parameter	Condition	Min	Typ	Max	Unit
VCC to GND		3.15	3.3	3.45	V
Digital Input Voltage to GND		GND		VCC	V
Digital Output Voltage to GND		-0.3		VCC +0.3	V
Operating Temperature Range		-30		85	°C
Start up Time	Power-on to start output.			900	ms.

Specifications

T_A=-30 °C to +85 °C, VCC=3.15 V~3.45 V, $\leq \pm 1$ G, unless otherwise noted.

Parameter	Test Conditions / Comments	Min	Typ	Max	Unit
VELOCITY					
Sensitivity					
Output Range	f = 10 Hz ~ 1000 Hz			± 100	mm/s
Scale Factor	2^{-22} m/s/LSB		2.38×10^{-4}		mm/s/LSB
Sensitivity Error	25 °C, ≤ 1 G	-1550		1550	$\times 10^{-6}$ (ppm)
Nonlinearity	≤ 1 G, Best fit straight line, RT	-0.15		0.15	% of FS
Cross Axis Sensitivity	No alignment correction		± 0.9 *3		%
Noise					
Noise Density	25 °C, Avg, f = 200 Hz ~ 1000 Hz		1.4×10^{-4}		mm/s/ $\sqrt{\text{Hz}}$, rms
Cantilever Resonance Frequency	25 °C, VCC 3.3 V		4,460		Hz
Frequency Property					
Frequency Range	-3 dB at 25 °C		10~1,000		Hz
DISPLACEMENT					
Sensitivity					
Output Range	f = 1 Hz ~ 100 Hz			± 200	mm
Scale Factor	2^{-22} m/LSB		2.38×10^{-4}		mm/LSB
Nonlinearity	≤ 1 G, Best fit straight line, RT	-0.15		0.15	% of FS
Cross Axis Sensitivity			± 0.9 *3		%
Noise					
Noise Density	25 °C, Avg, f = 20 Hz ~ 100 Hz		0.7×10^{-5}		mm/ $\sqrt{\text{Hz}}$, rms
Frequency Property					
Frequency Range	-3 dB at 25 °C		1~100		Hz
TEMPERATURE SENSOR					
Output Range		-40		85	°C
16bit Scale Factor *1	Output=2634(0x0A4A) at 25 °C		-0.0037918		°C/LSB
8bit Scale Factor *1	Output=2634(0x0A4A) at 25 °C		-0.9707008		°C/LSB
RELIABILITY					
MTBF*2	JIS-C5003 TA = 25 °C	87,600			hour

*1) This is a reference value used for the internal temperature correction, and is not guaranteed to accurately output the interior temperature.

*2) The MTBF is an estimated value derived from the result of high temperature operation with a system requirement of TA=25°C and a 60% reliability level.

*3) When the alignment is corrected by the host, the other axis sensitivity is Typ. 0.1 %.

Note) The values in the specifications are based on the data calibrated at the factory. The values may change according to the way the product is used.

Note) The Max/Min value is the maximum/minimum value of the design or factory shipment examination, unless otherwise specified.

Note) The calibrated standard 1G gravitational acceleration value is 9.80665 m/s²

3 Axis Accelerometer M-A352AD10

- Ultra-low noise : 0.2 μ G/ $\sqrt{\text{Hz}}$ Typ.
- Improved shock resistance: 1,000G
- Selectable output format: Acceleration / Tilt Angle
- 3-axis digital output SPI / UART
- Programmable low-pass digital filters
- Low jitter external trigger function for synchronous sampling
- Solid Metallic Case (Size : 48 x 24 x 16 mm³, Weight: 25 g)



Product number
M-A352AD10 : X2F000011000100



Recommended Application

- MHM (Machine Health Monitoring) • Earthquake detection • Environmental vibration measurement
- Industrial equipment monitoring • Unmanned vehicles • Measurement of the vibration and path of industrial equipment and vehicles.

Recommended Operating Condition

Parameter	Condition	Min	Typ	Max	Unit
VCC to GND		3.15	3.3	3.45	V
Digital Input Voltage to GND		GND		VCC	V
Digital Output Voltage to GND		-0.3		VCC +0.3	V
Operating Temperature Range		-30		85	°C
Start up Time	Power-on to start output.			900	ms.

Specifications

T_A=-30 °C to +85 °C, VCC=3.15 V~3.45 V, $\leq \pm 1$ G, unless otherwise noted.

Parameter	Test Conditions / Comments	Min	Typ	Max	Unit
SPECIFICATIONS					
Output Range	f = DC ~ 460 Hz			± 15	G
Scale Factor	2 ⁻²⁴ G/LSB		0.06		μ G/LSB
Sensitivity Error	25 °C, ≤ 1 G		± 500		$\times 10^{-6}$ (ppm)
Nonlinearity	≤ 1 G, Best fit straight line, RT			± 0.03	% of FS
Misalignment	25 °C			± 0.1	Deg
Initial Error	25 °C			± 2	mG
Bias Repeatability	T _A =25 °C and VCC=3.3 V for one year after shipment		3		mG
Bias Temperature Error	25 °C			± 2	mG
Noise Density	25 °C, Avg, f = 0.5 Hz ~ 6 Hz		0.2	0.7	μ G/ $\sqrt{\text{Hz}}$, rms
Cantilever Resonance frequency	25 °C, VCC3.3 V		850		Hz
VRC	at 50 Hz, 25 °C, VCC3.3 V			± 50	μ G/G ²
Power Supply Current	Standard noise floor condition, 200 Sps, Average		13.2	18.0	mA
	Reduced noise floor condition, 200 Sps, Average		16.2	20.0	mA
	Sleep mode		1.3	2.0	mA
FUNCTION					
Built-in LPF Cut off	-6 dB at 25 °C, selectable	9		460	Hz
User LPF			4, 64, 128, 512		Tap
Output Data Rate		50		1,000	Sps
Ext.Trigger Input Cycle		1		20	ms
Ext.Trigger Jitter	ADC's completion to Ext.Trigger input	0		5	μ s
RECOMMENDED OPERATING CONDITION					
VCC to GND		3.15	3.3	3.45	V
Operating temperature range	No condensation	-30		85	°C
ABSOLUTE MAXIMUM RATINGS					
Acceleration/Shock	Half-sine 0.2 msec		1,000		G
MTBF	JIS-C5003, 60 % reliability leve		87,600		Hour
Storage Temperature Range	No condensation	-40		85	°C

*1) This is a reference value used for the internal temperature correction, and is not guaranteed to accurately output the interior temperature.

*2) The MTBF is an estimated value derived from the result of high temperature operation with a system requirement of T_A=25 °C and a 60 % reliability level.

*3) When the alignment is corrected by the host, the other axis sensitivity is Typ. 0.1 %.

Note) The values in the specifications are based on the data calibrated at the factory. The values may change according to the way the product is used.

Note) The Max. /Min. value is the maximum/minimum value of the design or factory shipment examination, unless otherwise specified.

Note) The calibrated standard 1 G gravitational acceleration value is 9.80665 m/s²

Sensing Device

Product Grade	Automotive	<div style="border: 1px solid black; padding: 2px; width: fit-content;"> XC1011SD Vo: ±3 dps DRa.: ±30 G </div>	
	Automotive	<div style="border: 1px solid black; padding: 2px; width: fit-content;"> XV4001Bx ZRLt: ±3 dps </div>	<div style="border: 1px solid black; padding: 2px; width: fit-content;"> XV4001Kx ZRLt: ±3 dps (20 °) </div>
	Consumer & Industrial	<div style="border: 1px solid black; padding: 2px; width: fit-content;"> XV7081BB ZRLt: ±3.0 dps 1.5 mdps/√Hz </div>	<div style="border: 1px solid black; padding: 2px; width: fit-content;"> XV7021BB ZRLt: ±1.0 dps 1.5 mdps/√Hz </div>
		<div style="border: 1px solid black; padding: 2px; width: fit-content;"> XV7001BB ZRLt: ±5.0 dps 3 mdps/√Hz </div>	<div style="border: 1px solid black; padding: 2px; width: fit-content;"> XV7011BB ZRLt: ±1.0 dps 3 mdps/√Hz </div>
		Z axis	others

► Gyro Sensor

P	Model	Size [mm]	Supply Voltage [V]	Interface Type	Bias	Rate Range [°/s]	Scale Factor [mV/(°/s)]	Non Linearity [%FS]	Operating Temperature [°C]	Recommended Application
107	XV7021BB	5.0×3.2×1.3t	2.7 to 3.6	Digital (SPI / I ² C)	0 [LSB] Typ. ± 1 [°/s]	±400	24bit: 17920 [LSB/(°/s)] ± 5 [%]	± 0.5	-20 to +80 (Please contact us about -40 °C to +85 °C)	Anti vibration and attitude control for industrial applications etc. Motion detection for Man machine interface
108	XV7081BB						24bit: 71680 [LSB/(°/s)] ± 5 [%]			
109	XV7011BB					±100	16bit: 280 [LSB/(°/s)] ± 5 [%]			
110	XV7001BB					±300	3.0	-20 to +80		
111	XV-3510CB		2.7 to 3.3	Analog Voltage	1430 [mV]	±300	3.0	± 0.5	-20 to +80	Detection picture stabilization

► Gyro sensor for automotive

P	Model	Size [mm]	Supply Voltage [V]	Interface Type	Bias	Rate Range [°/s]	Scale Factor [mV/(°/s)]	Non Linearity [%FS]	Operating Temperature [°C]	Recommended Application
Web site	XV-9100CD (AEC-Q100)	5.0×5.0×1.3t	4.75 to 5.25	Analog Voltage	0.5 × V _{DD} [V]	±100	0.004 × V _{DD}	± 0.5	-40 to +105	Electric Stability Control System, Rollover Protection System
	XV-9300LP (AEC-Q100)	9.5×5.0×7.2t				±300	0.0012 × V _{DD}		-40 to +125	
136	XV4001KC (AEC-Q200)	6.0×4.8×3.3t Inclined	3.0 to 3.6	Digital (I ² C-Bus)	0 [LSB] Typ. ± 2 [°/s]	±70	370 [LSB/(°/s)] ±1.5 [%]	± 0.5	-40 to +85	Car navigation system
	XV4001KD (AEC-Q200)			Digital (SPI-Bus)						
	XV4001BC (AEC-Q200)	Digital (I ² C-Bus)								
	XV4001BD (AEC-Q200)	Digital (SPI-Bus)								

► Combined sensor for automotive

P	Model	Size [mm]	Supply Voltage [V]	Interface Type	Gyro Bias	Rate Range [°/s]	Acceleration 0G Output [mG]	Acceleration Range [G]	Operating Temperature [°C]	用途
137	XC1011SD (AEC-Q100)	6.5×5.2×1.9t	3.135 to 3.465	Digital (SPI-Bus)	0 [LSB] Typ. ± 3 [°/s]	±160	± 57	± 30	-40 to +105	Electric Stability Control System

GYRO SENSOR (Digital Output)

XV7021BB



Product number
XV7021BB: X2A000311xxxx00

- Excellent bias temperature coefficient 0.0016 (°/s)/°C Typ.
- Low angle random walk 0.065 °/√h Typ.
- Integrated user-selectable digital filter and detuning frequency eliminate filter
- SPI or I²C serial interface
- Angular rate output (16 bits or 24 bits resolution)
- Operating temperature -20 °C to +80 °C
- Embedded temperature sensor
- Low current consumption 900 μA Typ.



Recommended Application

- Anti-vibration and attitude control for industrial applications etc.
- Motion detection for human machine interface

*The I²C-Bus is a trademark of NXP Semiconductors

Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
Supply voltage	V _{DDM}	2.7 V to 3.6 V	
Supply voltage for interface	V _{DDI}	1.65 V to 3.6 V	
Storage temperature	T _{STG}	-40 °C to +85 °C	
Operating temperature	T _{OPR}	-20 °C to +80 °C	
Scale factor	S _o	70 LSB/(°/s) ±2 %	16 bits, T _a = +25 °C
		17920 LSB/(°/s) ±2 %	24 bits, T _a = +25 °C
Scale factor variation over temperature	S _{pt}	±3.0 %	V _{DDM} = 3 V, T _a = +25 °C reference
Bias	ZRL	±1 °/s (0 LSB Typ.)	T _a = +25 °C
Bias variation over temperature A	ZRL _{ta}	±0.25 °/s	-10 °C to +50 °C, T _a = +25 °C reference
Bias variation over temperature B	ZRL _{tb}	±1 °/s	-20 °C to +80 °C, T _a = +25 °C reference
Bias temperature coefficient	ZRL _s	0.0016 (°/s)/°C Typ.	V _{DDM} = 3 V, Average of absolute value, ΔT = 1 °C
Rate range	I	±400 °/s	
Non-linearity	NI	±0.5 %FS	T _a = +25 °C
Cross-axis sensitivity	CS	±5 %	T _a = +25 °C
Current consumption	I _{op1}	900 μA Typ.	
Sleep current	I _{op3}	3 μA Typ.	
Noise density	N _d	0.0015 (°/s)/√Hz	@ 10Hz, LPF default setting
Angle random walk	N	0.065 °/√h	

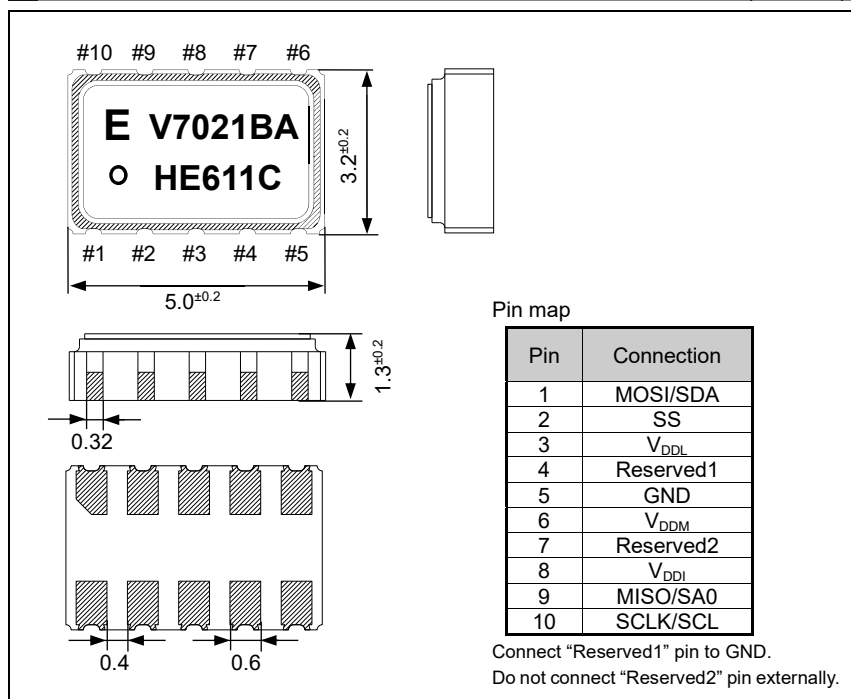
Product Name
(Standard form)

XV7021BB * *
① ②③④ ⑤ ⑥

- ① Model ② Detection axis (1: Z axis) ③ Package type (B: Ceramics 5032 size)
④ Output (B: SPI/I²C) ⑤ Frequency ⑥ Custom recognition (not necessary to specify)

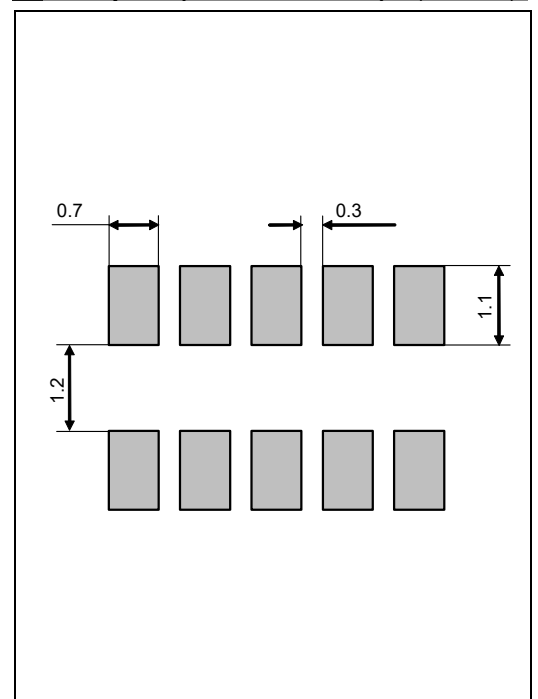
External Dimensions

(Unit: mm)



Footprint (Recommended)

(Unit: mm)





TCXO / VC-TCXO HIGH STABILITY, CMOS OUTPUT



Product Number
 TG3225CEN : X1G005101xxxxxx
 TG2520CEN : X1G005161xxxxxx

TG3225CEN / TG2520CEN

- Output frequency : 12 MHz to 52 MHz
- Supply voltage : 2.8 V Typ. / 3.0 V Typ. / 3.3 V Typ.
- Frequency / temperature characteristics : $\pm 2.0 \times 10^{-6}$ Max.
- External dimensions: 3.2 x 2.5 x 0.9 mm / 2.5 x 2.0 x 0.8 mm
- Applications : Reference clock for measurement machine
Wireless communication devices
(Smart meter, Telemeter, other)
- Features : High stability, CMOS output



TG3225CEN
(3.2 x 2.5 x 0.9 mm)



TG2520CEN
(2.5 x 2.0 x 0.8 mm)

Specifications (characteristics)

Item	Symbol	TCXO	VC-TCXO	Conditions / Remarks
Output frequency range	fo	12 MHz to 52 MHz 12 MHz, 20 MHz, 24 MHz, 25 MHz, 26 MHz, 27 MHz, 32 MHz, 36 MHz, 38.4 MHz, 39 MHz and 40 MHz		Standard frequency
Supply voltage	V _{CC}	2.8 V \pm 5 % / 3.0 V \pm 5 % / 3.3 V \pm 5 %		Supply voltage range: 2.375 V to 3.63 V
Storage temperature range	T _{stg}	-40 °C to +90 °C		Storage as single product.
Operating temperature range	T _{use}	G: -40 °C to +85 °C		
Frequency tolerance	f _{tol}	$\pm 2.0 \times 10^{-6}$ Max.		After reflow, +25 °C
Frequency/temperature characteristics	fo-Tc	F: $\pm 2.0 \times 10^{-6}$ Max. / -40 °C to +85 °C		Standard stability version
Frequency/load coefficient	fo-Load	$\pm 0.2 \times 10^{-6}$ Max.		15 pF \pm 10 %
Frequency/voltage coefficient	fo-V _{CC}	$\pm 0.3 \times 10^{-6}$ Max.		V _{CC} \pm 5 %
Frequency aging	f _{age}	$\pm 1.0 \times 10^{-6}$ Max.		+25 °C, First year, 12 MHz \leq fo \leq 20 MHz, 24 MHz \leq fo \leq 40 MHz
		$\pm 1.5 \times 10^{-6}$ Max.		+25 °C, First year, 20 MHz < fo < 24 MHz, 40 MHz < fo \leq 52 MHz
Current consumption	I _{CC}	4.0 mA Max.		12 MHz \leq fo \leq 26 MHz
		6.0 mA Max.		26 MHz < fo \leq 39 MHz
		6.5 mA Max.		39 MHz < fo \leq 52 MHz
Input impedance	Z _{in}	-	500 k Ω Min.	V _c - GND (DC)
Frequency control range	f _{cont}	-	$\pm 5.0 \times 10^{-6}$ Min.	C: V _c = 1.4 V \pm 1.0 V (V _{CC} = 2.8 V) or D: V _c = 1.5 V \pm 1.0 V (V _{CC} = 3.0 V) or E: V _c = 1.65 V \pm 1.0 V (V _{CC} = 3.3 V)
Frequency change polarity	f _{cp}	-	Positive polarity	
Symmetry	SYM	45 % to 55 %		50 % V _{CC} level, L _{CMOS} \leq 15 pF
Output voltage	V _{OH}	90 % V _{CC} Min.		
	V _{OL}	10 % V _{CC} Max.		
Start-up time	t _{str}	2.0 ms Max.		t = 0 at 90 % V _{CC}
Rise time / Fall time	tr/tf	8.0 ns Max.		10 % V _{CC} to 90 % V _{CC} level, Load: 15 pF
CMOS load condition	L _{CMOS}	15 pF		15 pF \pm 10 %

* Note : Please contact us for requirements not listed in this specification.

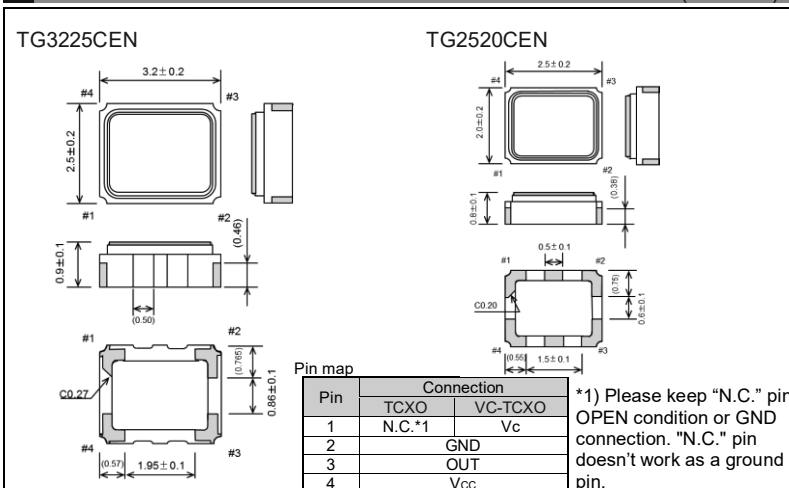
Product Name TG3225 CEN 39.000000MHz K F G N N M
 (Standard form) ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨

- ① Model ② Output (C: CMOS)
 ③ Frequency ④ Supply voltage (Refer to symbol table)
 ⑤ Frequency / temperature characteristics (F: $\pm 2.0 \times 10^{-6}$ Max.)
 ⑥ Operating temperature (G: -40 °C to +85 °C)
 ⑦ OE function (N: Non) ⑧ V_c function (Refer to symbol table, A: V_c = any)
 ⑨ Internal identification code ("M" is default)

Voltage [V]	④ Supply voltage [V _{CC}] , ⑧ V _c function [V _c] (Symbol table)			
	TCXO	VC-TCXO		
④ V _{CC} (Typ.)	K: 2.5 to 3.3	K: 2.5 to 3.3	P: 2.6 to 3.3	M: 2.8 to 3.3
⑧ V _c (Typ.)	N: Non	C: 1.4	D: 1.5	E: 1.65

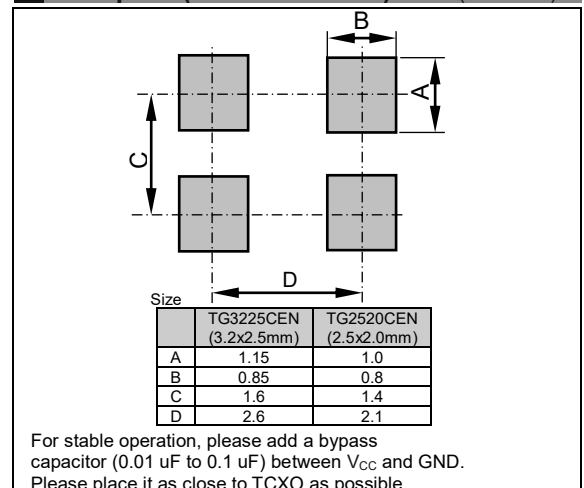
External dimensions

(Unit: mm)



Footprint (Recommended)

(Unit: mm)





GYRO SENSOR (Digital Output)

XV7081BB



Product number
XV7081BB: X2A000351xxxx00

- Excellent bias temperature coefficient 0.0024 (°/s)/°C Typ.
- Low angle random walk 0.065 °/√h Typ.
- SPI or I²C serial interface
- Integrated user-selectable digital filter
- Angular rate output (16 bits or 24 bits resolution)
- Operating temperature -20 °C to +80 °C
- Embedded temperature sensor
- Low current consumption 900 μA Typ.



Recommended Application

- Anti-vibration, attitude control for industrial applications.
- Autonomous machines

*The I²C-Bus is a trademark of NXP Semiconductors

Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
Supply voltage	V _{DDM}	2.7 V to 3.6 V	
Supply voltage for interface	V _{DDI}	1.65 V to 3.6 V	
Storage temperature	T _{STG}	-40 °C to +85 °C	
Operating temperature	T _{OPR}	-20 °C to +80 °C	
Scale factor	S _o	70 LSB/(°/s) ±2 %	16 bits, T _a = +25 °C
		17920 LSB/(°/s) ±2 %	24 bits, T _a = +25 °C
Scale factor variation over temperature	S _p	±3.0 %	V _{DDM} = 3 V, T _a = +25 °C reference
Bias	ZRL	±1 °/s (0 LSB Typ.)	T _a = +25 °C
Bias variation over temperature	ZRL _t	±3.0 °/s	V _{DDM} = 3 V, T _a = +25 °C reference
Bias temperature coefficient	ZRL _s	0.0024 (°/s)/°C Typ.	V _{DDM} = 3 V, Average of absolute value, ΔT = 1 °C.
Rate range	I	±400 °/s	
Non-linearity	NI	±0.5 %FS	T _a = +25 °C
Cross-axis sensitivity	CS	±5 %	T _a = +25 °C
Current consumption	I _{op1}	900 μA Typ.	
Stand-by current	I _{op2}	160 μA Typ.	
Sleep current	I _{op3}	3 μA Typ.	
Noise density	N _d	0.0015 (°/s)/√Hz	@ 10Hz, LPF default setting
Angle random walk	N	0.065 °/√h	

Product Name
(Standard form)

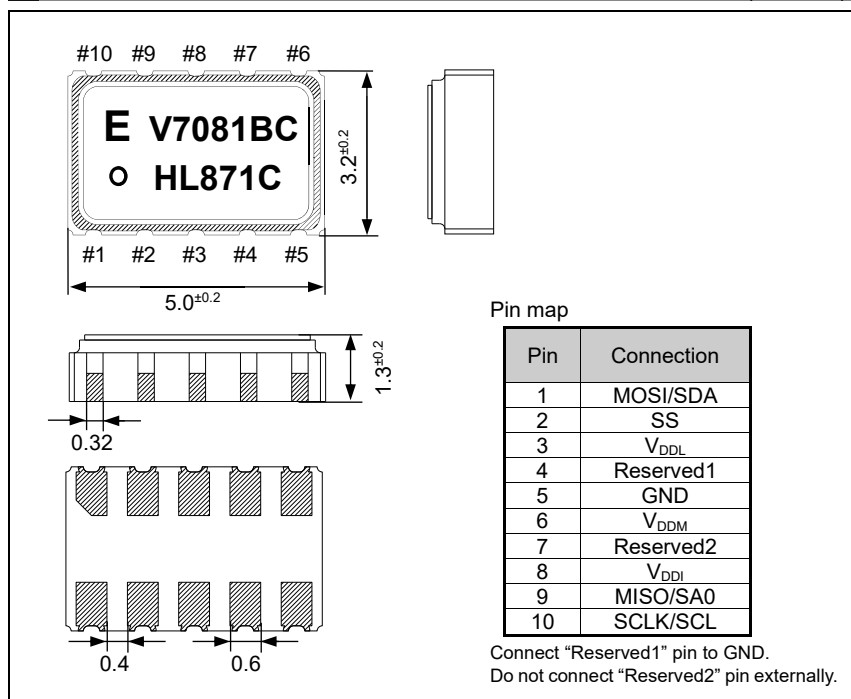
XV708 1 B B * *

① ②③④ ⑤ ⑥

- ① Model ② Detection axis (1: Z axis) ③ Package type (B: Ceramics 5032 size)
④ Output (B: SPI/I²C) ⑤ Frequency ⑥ Custom recognition (not necessary to specify)

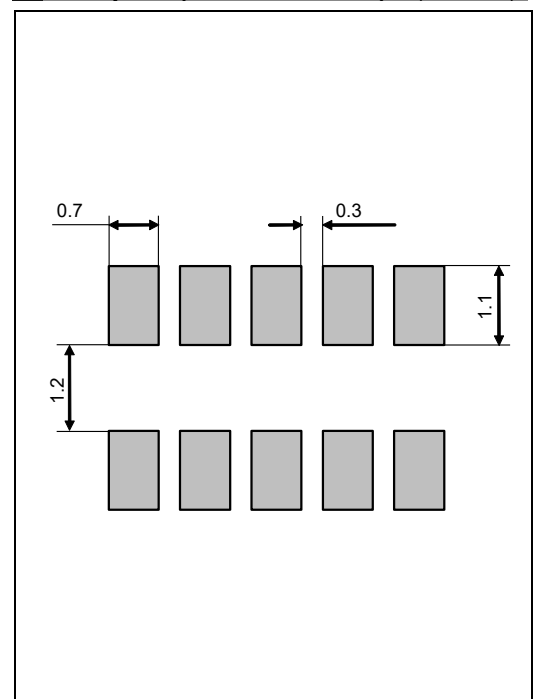
External Dimensions

(Unit: mm)



Footprint (Recommended)

(Unit: mm)



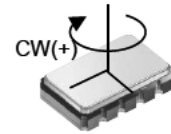
GYRO SENSOR (Digital Output)

XV7011BB



Product number
XV7011BB: X2A000271xxxx00

- SPI or I²C serial interface
 - Angular rate output (16/24bit)
 - Excellent bias stability over temperature
 - Operating temperature range -20 °C to +80 °C
(Option: -40 °C to +85 °C)
 - Built-in temperature sensor
 - Built-in selectable digital filter
 - Low power consumption
- Recommended Application**
- Anti vibration and attitude control for industrial applications etc.
 - Motion detection for man machine interface



*The I2C-Bus is a trademark of NXP Semiconductors

Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
Supply Voltage	VDDM	2.7 to 3.6 V	
Supply Voltage for interface	VDDI	1.65 V to 3.60 V	
Temperature range	Storage Temperature	TSTG	-40 C to +85 C
	Operating Temperature	TOPR	-20 C to +80 C Option: -40 C to +85 C
Scale factor	So	280 LSB/(°/s) ±5 %	16bit, Ta=+25 C
		71680 LSB/(°/s) ±5 %	24bit, Ta=+25 C
Bias	ZRL	±1 °/s (0 LSB Typ.)	Ta=+25 C
Bias over temperature	ZRL _t	±1 °/s	Variation from Ta = +25 °C
Rate range	I	±100 °/s	
Non linearity	NI	±0.5 %FS	Ta=+25 C
Cross axes	CS	±5 %	Ta=+25 C
Current consumption	Iop1	0.9 mA Typ.	Not communicating
Stand-by current	Iop2	160 µA Typ.	
Sleep current	Iop3	3 µA Typ.	
Noise	Nd	0.003 (°/s)/√Hz	at 10Hz

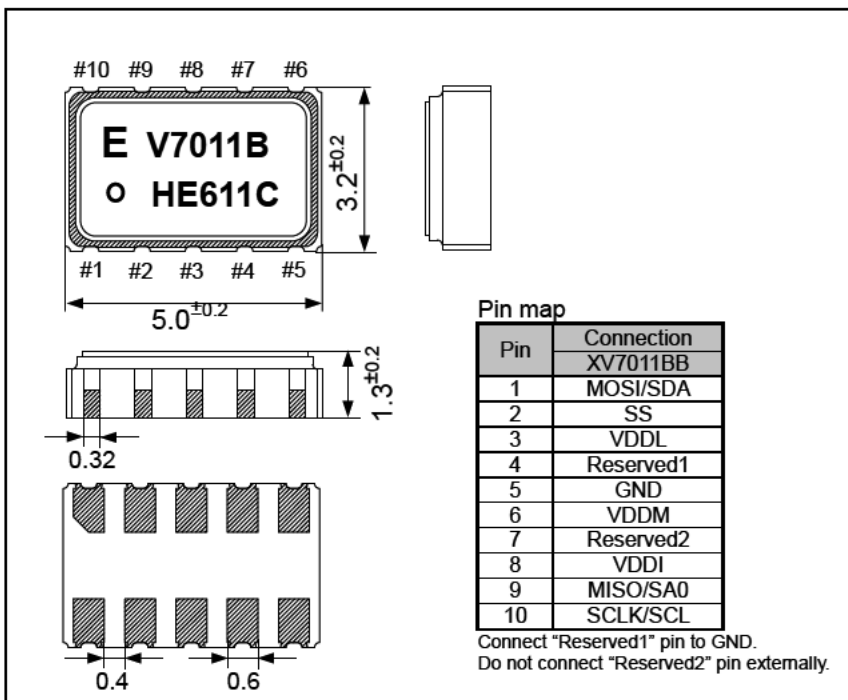
Product Name
(Standard form)

XV7011BB * *
① ②③④ ⑤ ⑥

①Model ②Detection axis (1:Z-axis) ③Package type(B: Ceramic 5032size)
④Output (B: SPI/I²C) ⑤Frequency ⑥Custom recognition(not necessary to specify)

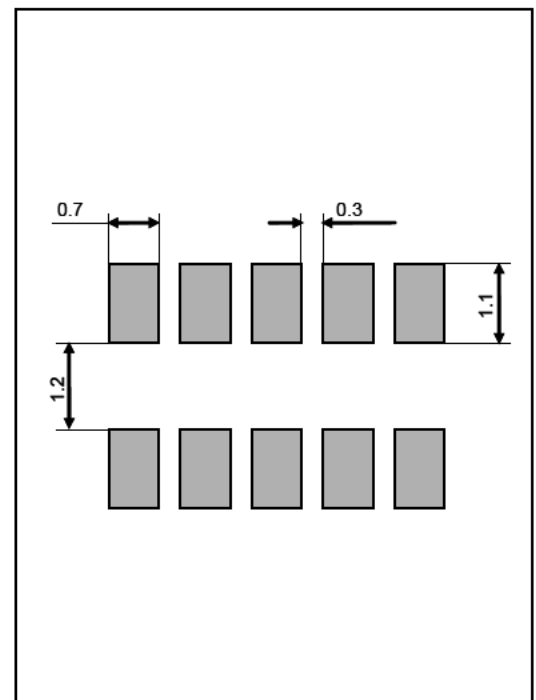
External Dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



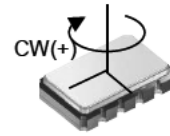


GYRO SENSOR (Digital Output)

XV7001BB

Product number
XV7001BB: X2A000261xxxx00

- SPI or I²C serial interface
- Angular rate output (16bit)
- Built-in temperature sensor
- Built-in selectable digital filter
- Low power consumption



Recommended Application

- Anti vibration and attitude control for industrial applications etc.
- Motion detection for man machine interface

*The I2C-Bus is a trademark of NXP Semiconductors

Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
Supply Voltage	VDDM	2.7 to 3.6 V	
Supply Voltage for interface	VDDI	1.65 V to 3.60 V	
Temperature range	Storage Temperature	TSTG	-40 C to +85 C
	Operating Temperature	TOPR	-20 C to +80 C
Scale factor	So	280 LSB/(°/s) ±5 %	16bit, Ta=+25 C
Bias	ZRL	±1 °/s (0 LSB Typ.)	Ta=+25 C
Bias over temperature	ZRL _t	±5 °/s	Variation from Ta = +25 °C
Rate range	I	±100 °/s	
Non linearity	NI	±0.5 %FS	Ta=+25 C
Cross axes	CS	±5 %	Ta=+25 C
Current consumption	Iop1	0.9 mA Typ.	Not communicating
Stand-by current	Iop2	160 µA Typ.	
Sleep current	Iop3	3 µA Typ.	
Noise	Nd	0.003 (°/s)/√Hz	at 10Hz

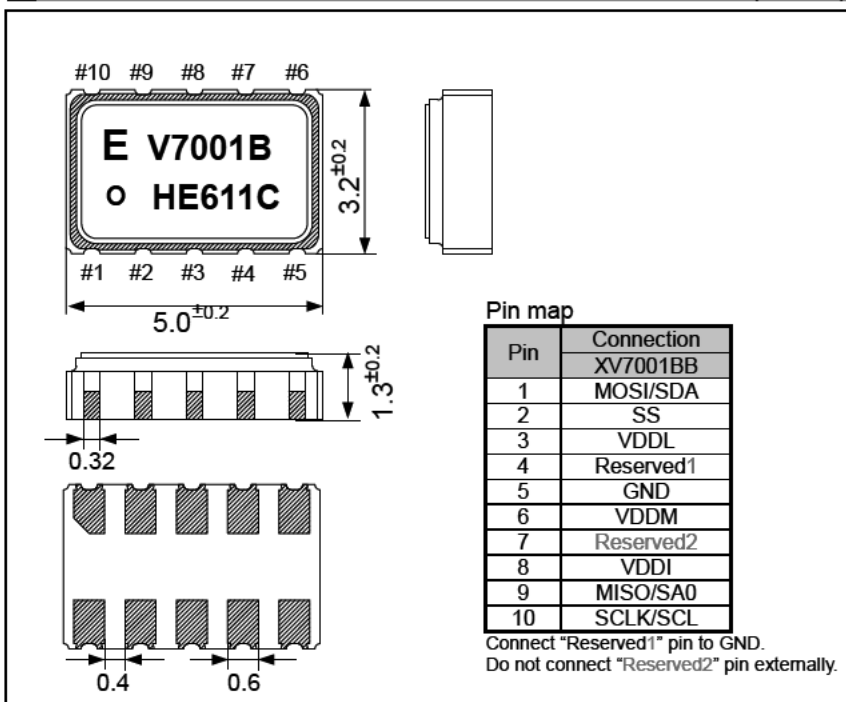
Product Name
(Standard form)

XV7001BB * *

- ①Model ②Detection axis (1:Z-axis) ③Package type(B: Ceramic 5032size)
④Output (B: SPI/I²C) ⑤Frequency ⑥Custom recognition(not necessary to specify)

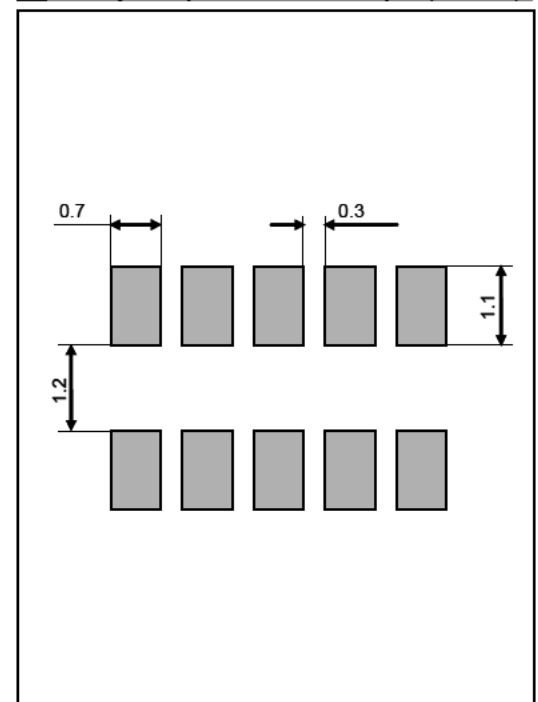
External Dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



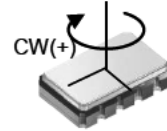


GYRO SENSOR

XV-3510CB



Product number
XV-3510CB:X2A000121xxxxxx



- Small Package size SMD(5.0 × 3.2 × 1.3 mm)
- Hermetic sealing provides excellent sustainable environmental capability
- High stability using vibration crystal

Recommended Application

- Detection picture stabilization of DVC and DSC
- Detection of moving with man machine interface

Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
		XV-3510CB	
Supply voltage	VDD	3.0 V ±0.3 V	
Temperature range	Storage Temperature	TSTG	-40 C to +85 C
	Operating Temperature	TOPR	-20 C to +80 C
Scale Factor	So	3.0 mV/(°/s)	
Bias	V0	Vr ± 30 mV	Ta=+25 C
Reference Voltage	Vr	1430 ± 20 mV	Ta=+25 C
Rate Range	I	±300 °/s	
Non linearity	NL	±0.5 % FS	Ta=+25 C
Phase Delay	φ20	9 deg.	at 20Hz phase delay angle
Current consumption	Iop	2.2 mA Typ.	V0,Vr : Output No load condition

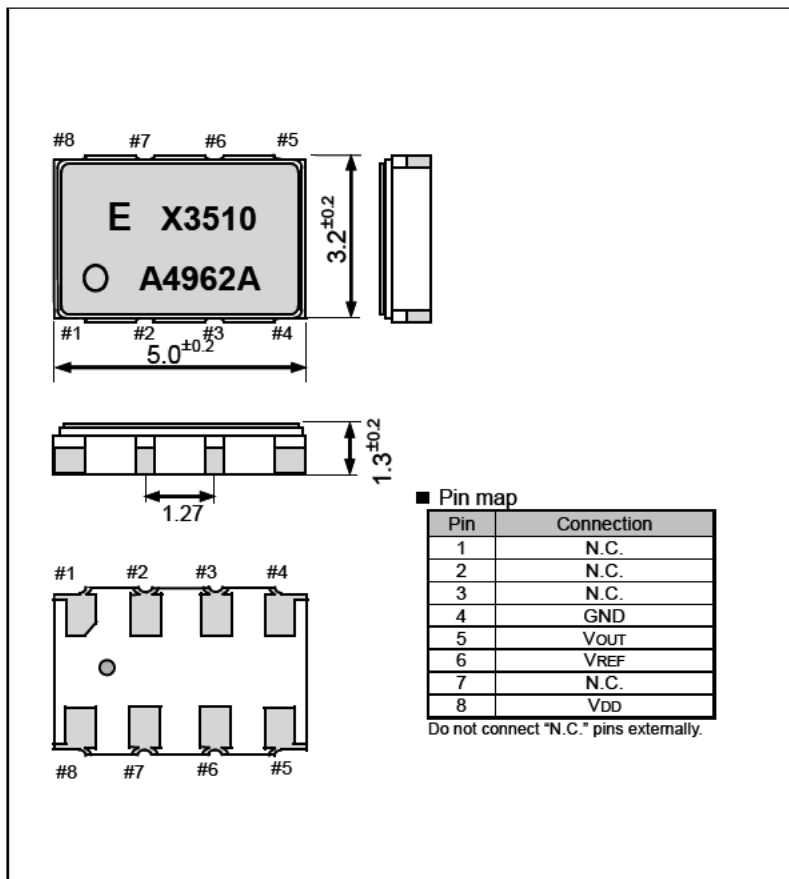
Product Name
(Standard form)

XV-3510 CB * ** ***** kHz

① Model ② Package type ③ Spec segment ④ Frequency (not necessary to specify)

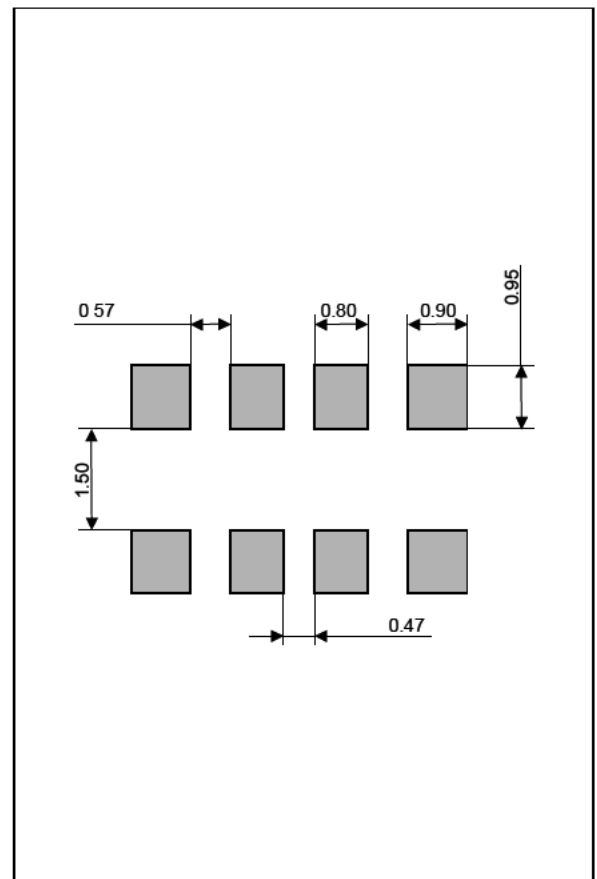
External Dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



Automotive Solutions
► kHz Crystal Unit

P	Model	Size [mm]	Nominal frequency range	Frequency Tolerance (+25 °C) [x 10 ⁻⁶]	Motional resistance Max. [kΩ]	Load capacitance [pF]	Operating temperature [°C]	Frequency vs temperature characteristics (Turnover Temperature: +25 °C Typ.)
Web site	FC-13A (AEC-Q200)	3.2 x 1.5 x 0.9t	● 32.768 kHz	±20 ±30 ±50	70	9 12.5	-40 to +125	Parabolic coefficient (B): -0.04 x 10 ⁻⁶ / °C ² Max. f _{tem} = B (Ti - θx) ²
104	FC2012AA (AEC-Q200)	2.05 x 1.2 x 0.6t	● 32.768 kHz	±20	40	7	+25	
					70	9	-40 to +105	
					75	12.5	-40 to +125	

► kHz Crystal Oscillator
► SPXO

P	Model	Size [mm]	Nominal frequency range	Frequency Tolerance (+25 °C) [x 10 ⁻⁶]	Operating temperature (T _{use}) [°C]	Supply voltage [V]	Current consumption Max. [μA] (V _{CC} = 3.3 V, No load, T _{use})	Output load condition [pF]	Output control
115	SG-3031CMA (AEC-Q100)	3.2 x 1.5 x 0.9t	● 32.768 kHz	5 ± 23	-40 to +85	1.1 to 5.5	1.3	15	V _{IO}

► TCXO

P	Model	Size [mm]	Nominal frequency range	Frequency tolerance [x 10 ⁻⁶] / Operating temperature (T _{use}) [°C]	Supply voltage [V]	Current consumption Max. [μA] (V _{CC} = 3.3 V, No load, T _{use})	Output load condition [pF]	Output control
116	TG-3541CEA (AEC-Q100)	3.2 x 2.5 x 1.0t	● 32.768 kHz	±3.4 / -40 to +85 ±5.0 / -40 to +85 ±8.0 / +85 to +105	1.5 to 5.5	3.0	30	OE

► Real Time Clock Module
3.2 x 2.5 x 1.0t (CE package) Recommendation package

P	Model	Interface	Specifications								Functions					
			Operating temperature Ta [°C]		Frequency Tolerance [x 10 ⁻⁶]				Backup current consumption [μA] 3.0V		Time Stamp	Power Switch	EVIN pin	User Memory	Timer	Others
			Min.	Max.	+25 °C	-40 °C to +85 °C	+85 °C to +105 °C	+105 °C to +125 °C	Typ. (25 °C)	Max. (Ta = Max.)						
117	RA8000CE (AEC-Q100)	I ² C	-40	+125	-	±5.0	±8.0	±50.0	0.3	1.7	2	-	0 or 2	-	24 bit x 1 ch. to 32 years	Reset output with Delay, SOUT pin
119	RA4000CE (AEC-Q100)	SPI	-40	+125	-	±5.0	±8.0	±50.0	0.3	1.7	2	-	0 to 2	-	24 bit x 1 ch. to 32 years	Reset output with Delay, SOUT pin
121	RA8804CE (AEC-Q100)	I ² C	-40	+105	-	±3.4	±8.0	-	0.35	1.5	1	-	1	-	16 bit x 1 ch. to 7.5 years	SOUT pin
122	RA8900CE (AEC-Q200)	I ² C	-40	+85	-	±5.0	-	-	0.7	1.4	-	✓	-	-	12 bit x 1 ch. to 2.8 days	Built-in Temp. Sensor

10.1 x 7.4 x 3.3t (SA package / SOP-14 pin)

P	Model	Interface	Specifications								Functions					
			Operating temperature Ta [°C]		Frequency Tolerance [x 10 ⁻⁶]				Backup current consumption [μA] 3.0 V		Time Stamp	Power Switch	EVIN pin	User Memory	Timer	Others
			Min.	Max.	+25 °C	-40 °C to +85 °C	-40 °C to +105 °C	Typ. (25 °C)	Max. (Ta = Max.)							
Web Site	RA8803SA (AEC-Q200)	I ² C	-40	+85	-	±3.4 ±5.0	-	-	0.75	2.1	-	-	1	-	to 2.8 days	Time sync. with 1 PPS
	RA4803SA (AEC-Q200)	SPI							0.75	2.1	-	-	1	-	to 2.8 days	Time sync. with 1 PPS
	RA-4565SA (AEC-Q200)	SPI							5 ± 23	-	-	0.8	1.6	-	-	-

► MHz Crystal Unit

P	Model	Size [mm]	Nominal frequency range	Frequency tolerance (+25 °C) [x 10 ⁻⁶]	Frequency vs. temperature characteristics [x10 ⁻⁶] / Operating temperature [°C]	Motional resistance Max. [Ω]	Load Capacitance [pF]	Operating temperature [°C]
123	FA2016AA (AEC-Q200)	2.0 x 1.6 x 0.5t	▬ 19.2 MHz 54 MHz	±10	±20 / -40 to +85 ±50 / -40 to +125	150 (19.2 ≤ f < 20 MHz) 100 (20 ≤ f < 24 MHz) 80 (24 ≤ f < 26 MHz) 60 (26 ≤ f ≤ 54 MHz)	6 to ∞	-40 to +125
124	FA-238A (AEC-Q200)	3.2 x 2.5 x 0.7t	▬ 12 MHz 50 MHz	±15	±30 / -40 to +85	120 (12 ≤ f ≤ 13 MHz) 80 (13 < f < 20 MHz) 60 (20 ≤ f < 25 MHz) 50 (25 ≤ f ≤ 50 MHz)	7 to ∞	-40 to +125

► MHz Crystal Unit (Built-in Thermistor)

P	Model	Size [mm]	Nominal frequency range	Frequency tolerance (+25 °C) [x 10 ⁻⁶]	Frequency vs. temperature characteristics [x10 ⁻⁶] / Operating temperature [°C]	Motional resistance Max. [Ω]	Load Capacitance [pF]	Operating temperature [°C]
125	FA2016ASA (AEC-Q200)	2.0 × 1.6 × 0.68t	● 38.4 MHz	±10	±12 / -30 to +85 ±30 / -40 to +105	50	6 to ∞	-40 to +105

*1 Please contact us about reference temperature.

► Fixed-Frequency SPXO

P	Model	Size [mm]	Nominal frequency range	Frequency Tolerance [x 10 ⁻⁶]	Operating temperature [°C]	Supply voltage [V]	Current consumption Max. [mA]	Output load condition [pF]	Output control
126	SG2016CAA (AEC-Q200)	2.0 × 1.6 × 0.7t 2.5 × 2.0 × 0.8t	█ 1 MHz 75 MHz 1.2 MHz (SG2016CAA)	±50, ±100 ±50, ±100 ±100, ±150	-40 to +85 -40 to +105 -40 to +125	1.6 to 2.2	2.9	15	ST
	2.2 to 2.7					3.3			
	2.7 to 3.6					3.5			

► Programmable SPXO

P	Model	Size [mm]	Nominal frequency range	Frequency Tolerance [x 10 ⁻⁶]	Operating temperature [°C]	Supply voltage [V]	Current consumption Max. [mA]	Output load condition [pF]	Output control
127	SG-8201CJA (AEC-Q100)	2.0 × 1.6 × 0.6t	█ 1.2 MHz 170 MHz	±15 ±25 / ±50	-40 to +105 -40 to +125	1.62 to 1.98 2.25 to 2.75 2.97 to 3.63	10.4 12.4 15.0	15	OE or ST
129	SG-8101CGA (AEC-Q100)	2.5 × 2.0 × 0.7t	█ 0.67 MHz 170 MHz	±15 ±20 ±50 / ±100	-40 to +85 -40 to +105 -40 to +125	1.62 to 1.98 1.98 to 2.20 2.20 to 2.80 2.70 to 3.63	5.5 5.8 6.7 8.1	15	OE or ST
131	SG-9101CGA (AEC-Q100)	2.5 × 2.0 × 0.7t	█ 0.67 MHz 170 MHz	±0.25 to ±2.0 -0.5 to -4.0	-40 to +125	1.62 to 1.98 1.98 to 2.20 2.20 to 2.80 2.70 to 3.63	5.8 6.1 7.0 8.4	15	OE or ST

► High Precision Oscillator (TCXO / VC-TCXO)

P	Model	Size [mm]	Nominal frequency range	Frequency tolerance (× 10 ⁻⁶)	Frequency / Temperature Characteristics (× 10 ⁻⁶)	Operating temperature [°C]	Supply voltage [V]	Current consumption Max. [mA]	Output load condition	Output control
134	TG2016SKA (AEC-Q100)	2.0 × 1.6 × 0.7t	█ 13 MHz 55 MHz	±2.0	±0.5	-40 to +105	1.8 ± 0.1 3.3 ± 5 %	2.0 (≤ 40 MHz) 2.5 (≤ 55 MHz)	10 kΩ/10 pF	ST
135	TG2016SLA (AEC-Q100)	2.0 × 1.6 × 0.7t	█ 13 MHz 55 MHz	±2.0	±0.5	-40 to +85	1.8 ± 0.1 3.3 ± 5 %	2.0 (≤ 40 MHz) 2.5 (≤ 55 MHz)	10 kΩ/10 pF	ST

► Gyro sensor for automotive

P	Model	Size [mm]	Supply Voltage [V]	Interface Type	Bias	Rate Range [°/s]	Scale Factor [mV/(°/s)]	Non Linearity [%FS]	Operating Temperature [°C]	Recommended Application
136	XV4001KC (AEC-Q200)	6.0×4.8×3.3t Inclined	3.0 to 3.6	Digital (I ² C-Bus)	0 [LSB] Typ. ± 2 [°/s]	±70	370 [LSB/(°/s)] ± 1.5 [%]	± 0.5	-40 to +85	Car navigation system
	Digital (SPI-Bus)									
	Digital (I ² C-Bus)									
	Digital (SPI-Bus)									

► Combined sensor for automotive

P	Model	Size [mm]	Supply Voltage [V]	Interface Type	Gyro Bias	Rate Range [°/s]	Acceleration 0G Output [mG]	Acceleration Range [G]	Operating Temperature [°C]	用途
137	XC1011SD (AEC-Q100)	6.5×5.2×1.9t	3.135 to 3.465	Digital (SPI-Bus)	0 [LSB] Typ. ± 3 [°/s]	±160	± 57	± 30	-40 to +105	Electric Stability Control System

► RF Transmitter Module

P	Model	Size [mm]	Feature	Operating Temperature [°C]	Supply voltage [V]	Current consumption Max. [mA]
133	SR3225SAA (AEC-Q100)	3.2×2.5×1.0t	UHF range wireless transmitter module. 300 ~ 400 MHz (0.25 kHz step) 600 ~ 930 MHz (0.49 kHz step) Modulation types: ASK / OOK / FSK SPI interface	-40 to +85	1.8 to 3.6	16

kHz RANGE CRYSTAL UNIT

FC2012AA

Low ESR 75 kΩ Max. (-40 °C to +125 °C)

- Frequency range : 32.768 kHz
- External dimensions : 2.05 x 1.2 x 0.6 mm
- Overtone order : Fundamental
- Applications : Automotive applications
(Various modules, Electronic key, etc.)
Industrial equipment, etc.
- AEC-Q200 compliant



Product Number
FC2012AA: X1A000181xxxx18



Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
Nominal frequency range	f_nom	32.768 kHz	
Storage temperature range	T_stg	-55 °C to +125 °C	Storage as single product.
Operating temperature range	T_use	-40 °C to +125 °C	
Level of drive	DL	0.5 μW Max.	
Frequency tolerance (standard)	f_tol	±20 x 10 ⁻⁶	+25 °C, DL = 0.1 μW
Turnover temperature	Ti	+25 °C ± 5 °C	
Parabolic coefficient	B	-0.04 x 10 ⁻⁶ / °C ² Max.	
Load capacitance	CL	7 pF, 9 pF, 12.5 pF	Please contact us other CL values
Motional resistance (ESR)	R1	40 kΩ Typ.	+25 °C
		70 kΩ Max.	-40 °C to +105 °C
		75 kΩ Max.	-40 °C to +125 °C
Motional capacitance	C1	8.0 fF Typ.	
Shunt capacitance	C0	1.5 pF Typ.	
Frequency aging	f_age	±5 x 10 ⁻⁶ / year Max.	+25 °C, First year

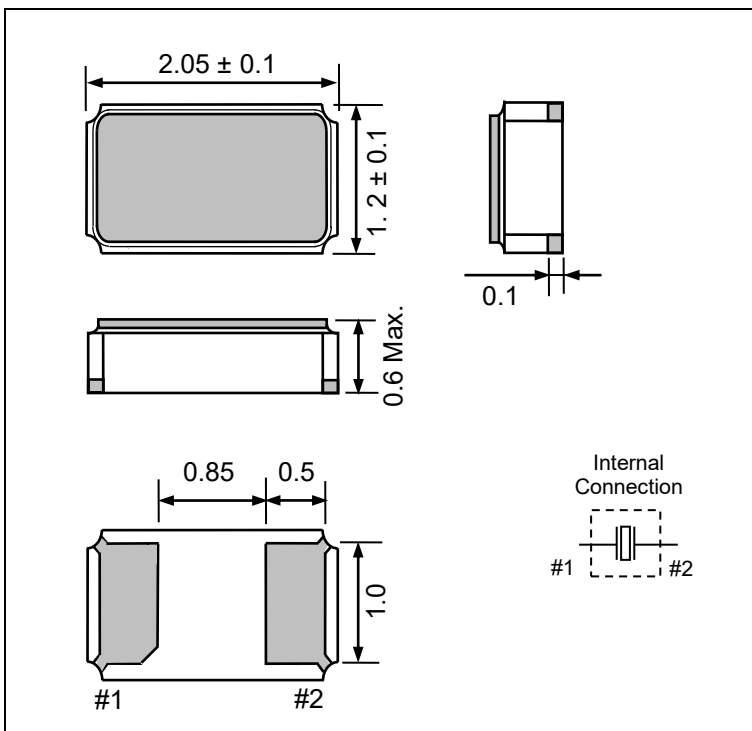
Product name

Product name FC2012AA 32.768000kHz 12.5 +20.0-20.0
(Standard form) a b c d

a: Model b: Frequency c: Load capacitance (pF) d: Frequency tolerance (x 10⁻⁶, +25 °C)

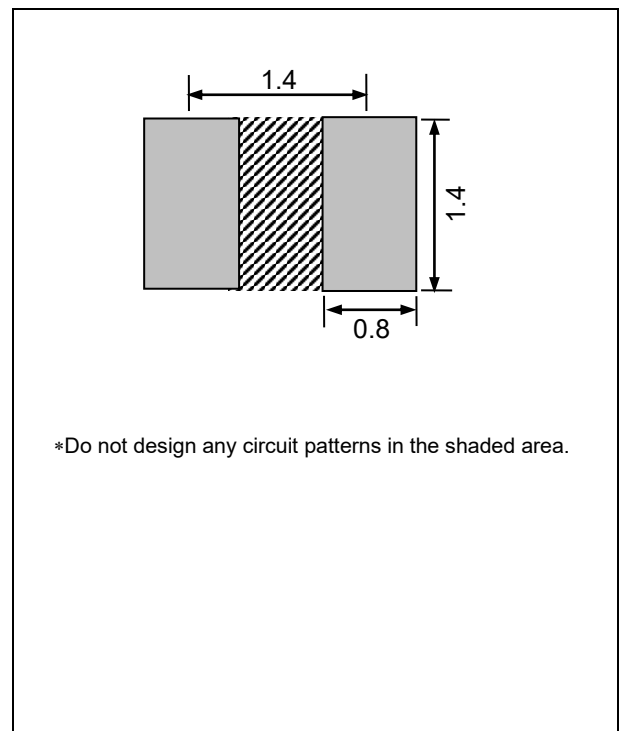
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



**CRYSTAL OSCILLATOR (SPXO)
FOR AUTOMOTIVE**
32.768 kHz



Product Number
SG-3031CMA : X1B000401A00116

SG-3031CMA



- Built-in 32.768 kHz crystal unit allows adjustment-free efficient operation.
- Operation temperature -40 °C to +105 °C
- Use of CMOS IC enables reduction of current consumption.
- V_{IO} controls swing amplitude.
- AEC-Q100 compliant

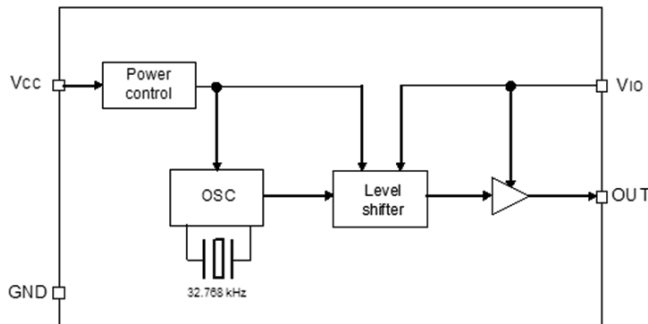
- Applications
Infotainment and communication devices, Body (ECU*)
Clock for Time counting and Sleep function. *ECU: Electronic control unit

Specifications (characteristics)

Item	Symbol	Specifications	Remarks
Output frequency range	f_o	32.768 kHz	
Supply voltage	V_{CC}	1.2 V to 5.5 V	$V_{CC} < 1.5 V, V_{IO} = V_{CC}$
Interface power supply voltage	V_{IO}	1.2 V to 5.5 V	
Storage temperature range	T_{stg}	-55 °C to +125 °C	Store as bare product after unpacking
Operating temperature range	T_{use}	-40 °C to +105 °C	
Frequency tolerance	f_{tol}	$+5 \pm 23 \times 10^{-6}$	+25 °C, $V_{CC} = 3.3 V$
Frequency temperature coefficient	f_o -Tc	-120×10^{-6} to $+10 \times 10^{-6}$	-20 °C to +70 °C ($V_{CC} = 3.3 V$ +25 °C is reference)
		-240×10^{-6} to $+10 \times 10^{-6}$	-40 °C to +85 °C ($V_{CC} = 3.3 V$ +25 °C is reference)
		-420×10^{-6} to $+10 \times 10^{-6}$	-40 °C to +105 °C ($V_{CC} = 3.3 V$ +25 °C is reference)
Frequency voltage coefficient	f_o - V_{CC}	$\pm 1 \times 10^{-6}$ / V Max.	$V_{CC} = 1.5 V$ to 5.5 V
		$\pm 5 \times 10^{-6}$ / V Max.	$V_{CC} = 1.2 V$ to 1.5 V
Current consumption (V_{CC} Pin)	I_{CC}	0.30 μA Typ. / 0.65 μA Max.	$V_{CC} = 1.2 V$ to 5.5 V
Current consumption (V_{CC} + V_{IO} Pin)	$I_{CC}+I_{IO}$	0.38 μA Typ.	$V_{CC} = V_{IO} = 1.2 V$, No load condition
		0.65 μA Typ. / 1.3 μA Max. (+105 °C)	$V_{CC} = V_{IO} = 3.3 V$, No load condition
Symmetry	SYM	45 % to 55 %	$1/2V_{CC}(V_{IO})$ level, 1.5 V to 5.5 V
		40 % to 60 %	$1/2V_{CC}(V_{IO})$ level, $V_{CC} < 1.5 V$
Output voltage	V_{OH} / V_{OL}	$V_{IO} - 0.4V$ Min. / 0.4V Max.	$I_{OH} = -0.4mA / I_{OL} = 0.4mA, V_{IO} = 1.5 V$ to 5.5 V
		$V_{IO} - 0.2V$ Min. / 0.2V Max.	$I_{OH} = -0.1mA / I_{OL} = 0.1mA, V_{IO} = 1.2 V$ to 1.5 V
Output load condition (CMOS)	L_{CMOS}	15 pF Max.	CMOS load
Rise time / Fall time	t_r/t_f	200 ns Max.	20 % V_{IO} to 80 % V_{IO} level, $V_{IO} = 1.2 V$ to 5.5 V
		100 ns Max.	20 % V_{IO} to 80 % V_{IO} level, $V_{IO} = 1.8 V$ to 5.5 V
Start-up time	t_{str}	0.15 s Typ. / 0.45 s Max.	$V_{CC} = 1.5 V$ to 5.5 V
		1.0 s Max.	$V_{CC} = 1.2 V$ to 1.5 V
Frequency aging	f_{age}	$\pm 5 \times 10^{-6}$ / year Max.	+25 °C, $V_{CC} = 3.3 V$, First year

Unless otherwise stated, characteristics (specifications) shown in the above table are based on the rated operating temperature and voltage condition.

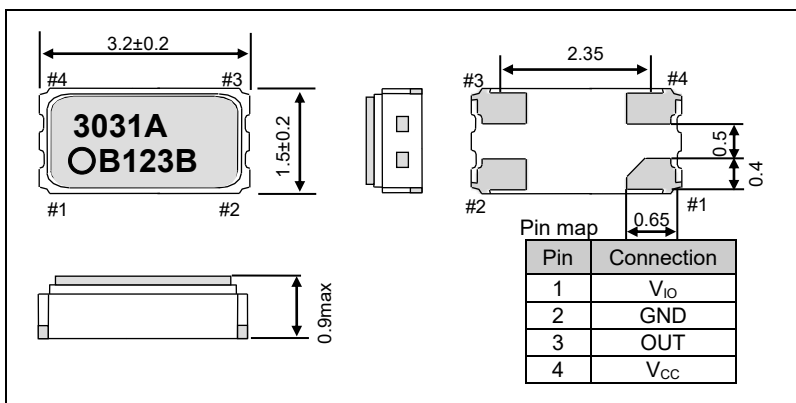
Block diagram



V_{IO} is a power supply pin for OUT output and can also be used as an OE pin. Set V_{IO} to 0 V when setting Disable.
 V_{CC} is a power supply pin for operating the 32.768 kHz oscillation.
 Power consumption can be minimized by minimizing the applied voltage of both power supplies. However, when $V_{CC} < 1.5 V, V_{IO} = V_{CC}$.

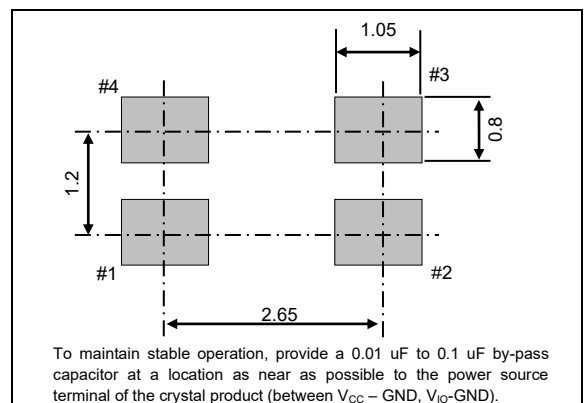
External dimension

(Unit:mm)



Footprint(ference)

(Unit:mm)



TCXO
For Automotive



Product Number
TG-3541CEA XA : X1B000361A00100
TG-3541CEA XB : X1B000361A00200

TG-3541CEA



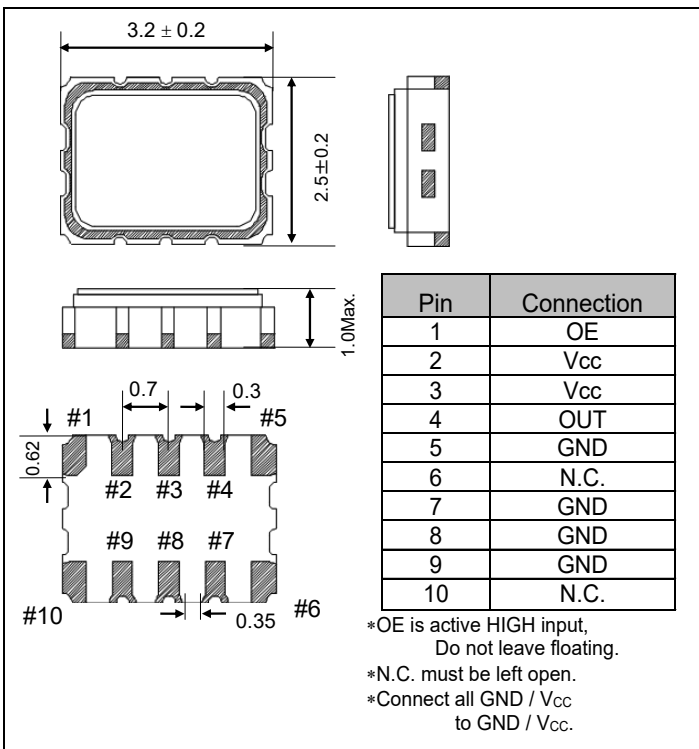
- Output frequency : 32.768 kHz
- Built-in 32.768 kHz-DTCXO
- Supply voltage : 1.5 V to 5.5 V
- Frequency / Temperature Characteristics
 - XA $\pm 1.9 \times 10^{-6}$ (0 °C to +50 °C)
 - $\pm 3.4 \times 10^{-6}$ (-40 °C to +85 °C)
 - $\pm 8.0 \times 10^{-6}$ (+85 °C to +105 °C)
 - XB $\pm 3.8 \times 10^{-6}$ (0 °C to +50 °C)
 - $\pm 5.0 \times 10^{-6}$ (-40 °C to +85 °C)
 - $\pm 8.0 \times 10^{-6}$ (+85 °C to +105 °C)
- AEC-Q100 compliant

Specifications (characteristics)

Item	Symbol	Specifications	Conditions	
Output frequency range	fo	32.768 kHz		
Supply voltage	V _{CC}	1.5 V to 5.5 V		
Storage temperature range	T _{stg}	-55 °C to +125 °C		
Operating temperature range	T _{use}	-40 °C to +105 °C		
Frequency / Temperature Characteristics	fo_Tc	XA	$\pm 1.9 \times 10^{-6}$	Ta = 0 °C ~ +50 °C
			$\pm 3.4 \times 10^{-6}$	Ta = -40 °C ~ +85 °C
			$\pm 8.0 \times 10^{-6}$	Ta = +85 °C ~ +105 °C
		XB	$\pm 3.8 \times 10^{-6}$	Ta = 0 °C ~ +50 °C
			$\pm 5.0 \times 10^{-6}$	Ta = -40 °C ~ +85 °C
			$\pm 8.0 \times 10^{-6}$	Ta = +85 °C ~ +105 °C
Current consumption	I _{CC}	1.0 μ A Typ./3.0 μ A Max.	No load condition, V _{CC} = 3.0 V	
Disable current	I _{dis}	1.5 μ A Max.	OE=GND, V _{CC} = 3.0 V	
Symmetry	SYM	40 % to 60 %	50 % V _{CC} level	
Output voltage	V _{OH}	80 % V _{CC} Min.	IOH=-0.5 mA, V _{CC} =3.0 V	
	V _{OL}	20% V _{CC} Max.	IOL=0.5 mA, V _{CC} =3.0 V	
Output load condition(CMOS)	L _{CMOS}	30 pF Max.		
Start-up time	t _{str}	1 s Max.	Ta= +25 °C, V _{CC} =1.5 V to 5.5 V	
Frequency aging	f _{aging}	$\pm 3 \times 10^{-6}$	Ta= +25 °C, V _{CC} =3.0 V, first year	

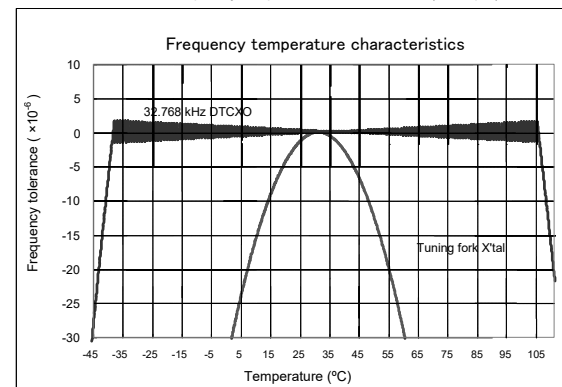
"CE" PKG – Rev.01

External dimensions (Unit:mm)



Frequency temperature characteristics

■ 32.768 kHz-DTCXO Frequency temperature characteristics (Example)



REAL TIME CLOCK MODULE (I²C)

For Automotive, Built-in 32.768 kHz DTCXO,
High Stability, +125 °C



Product Number (2,000 pcs / Reel)
RA8000CE YB A0 : X1B000501A00115
RA8000CE YB B8 : X1B000501A00915
RA8000CE YB C0 : X1B000501A01015

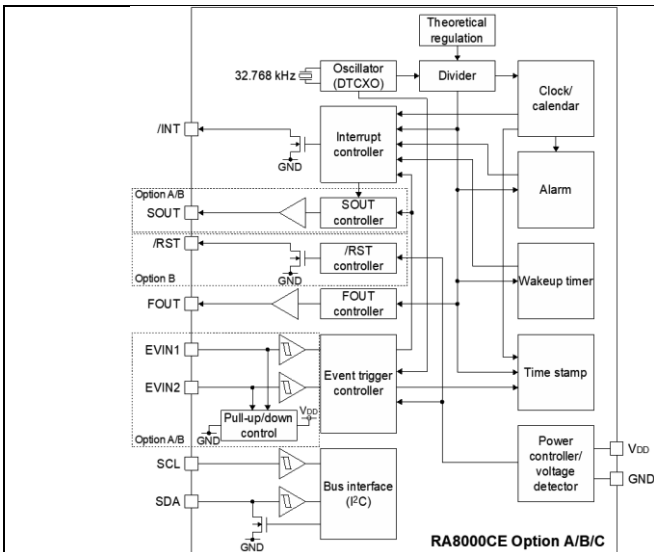
RA8000CE

- Built in frequency adjusted 32.768 kHz crystal unit and DTCXO
- Interface Type : I²C-Bus
- Time stamp function : 2 times stamped from year to second
- Reset functions with a delay : Detect a main power supply and remove the reset
- Interrupt output : Wake up every minute or every second
- Alarm interruption : Day, date, hour, minute, second
- Auto repeat wakeup timer interruption
- Self-monitoring interruption : Crystal oscillation stop, V_{DD} low
- AEC-Q100 compliant



RA8000CE
(3.2 × 2.5 mm, t = 1.0 mm Max.)

Block diagram



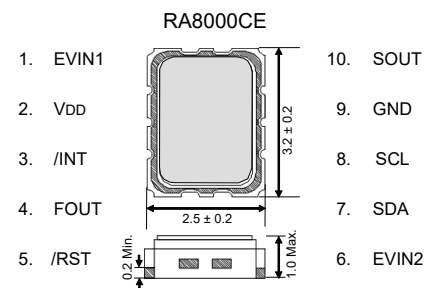
Overview

- Interface type
I²C-Bus interface Fast-Mode 400 kHz
- High stability
YB : ±5.0 × 10⁻⁶ / -40 °C to +85 °C (Monthly rate: ±13.2 seconds)
 ±8.0 × 10⁻⁶ / +85 °C to +105 °C (Monthly rate: ±21 seconds)
 ±50.0 × 10⁻⁶ / +105 °C to +125 °C (Monthly rate: ±132 seconds)
- Clock output function
Output frequency is selectable from 32.768 kHz, 1024 Hz, 1 Hz
- Wakeup timer function
Can generate an interrupt in 976.56 μs to 32-year cycle.
Can be used as a time integration meter.
Can be used as a watchdog timer.
- Time stamp function
Record data: 1/1024 seconds to 1 second, seconds, minutes, hours, days, months, years.
Number of recordable events: 2 events
Trigger source: External event (EVIN) input, voltage drop/oscillation stop status detected, command input from the host.
EVIN pin has function of chattering-cancel.
- Reset function with a delay
Can output a reset signal when a V_{DD} voltage drop status is detected.
- Status output (SOUT)
Can output the selected internal flag (interrupt flag, voltage drop detection flag) status.

Pin Function

Signal Name	I / O	Function
EVIN1, 2	Input	External event input pins Pull-up and pull-down is configurable by the resistors
SCL	Input	Serial clock input pin
SDA	Input / Output	Serial data input and output pin
FOUT	Output	Frequency output pin (CMOS). 32.768 kHz (default), 1024 Hz or 1 Hz clock output is selectable. This pin can be switched to the wakeup timer interrupt output (CMOS)
/INT	Output	Interrupt output pin (N-ch. open drain). The wakeup timer, time update, alarm, and/or event detection interrupt signals can be selected to output from this pin. When two or more signals are selected, they are NORed before being output.
/RST	Output	Reset output pin (N-ch. open drain)
SOUT	Output	Status output pin
V _{DD}	-	Power-supply pin
GND	-	Ground pin

Terminal connection / External dimensions (Unit: mm)



*The above diagram is the terminal layout for Option B.
For other options, please refer to the Pin Option section.

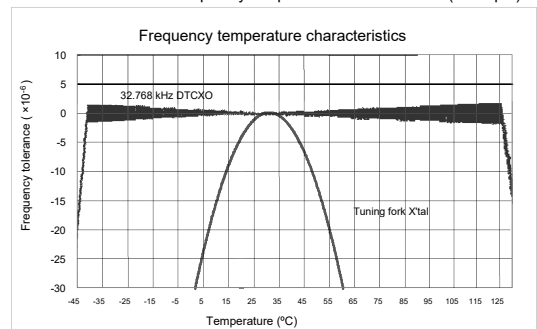
Specifications (characteristics)

* Refer to application manual for details

Electrical Characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit		
Operating voltage	V _{DD}	-	1.6	3.0	5.5	V		
Temp. compensated Voltage	V _{TEM}	-	1.6	3.0	5.5	V		
Clock supply voltage	V _{CLK}	-	1.3	3.0	5.5	V		
Operating temperature	T _a	-	-40	+25	+125	°C		
Frequency tolerance	Δf/f	YB	T _a = -40 °C to +85 °C	±5.0		x 10 ⁻⁶		
			T _a = +85 °C to +105 °C	±8.0				
			T _a = +105 °C to +125 °C	±50.0				
Current consumption	I _{DD1}	/INT = Hi-Z, FOUT: Output OFF (Hi-Z), Temp. Compensation interval 2.0 s, SCL = SDA = H	No /RST pin	V _{DD} = 5 V	-	0.35	1.8	μA
	I _{DD2}			V _{DD} = 3 V	-	0.3	1.7	
	I _{DD11}		With /RST pin	V _{DD} = 5 V	-	1.5	3.7	
	I _{DD12}			V _{DD} = 2 V	-	0.6	2.25	

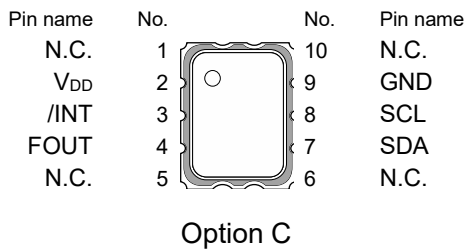
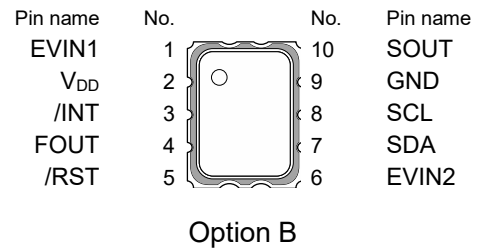
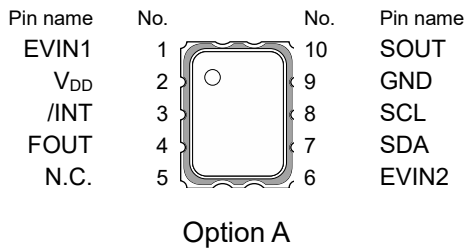
32.768 kHz-DTCXO Frequency temperature characteristics (Example)





Pin Option

Pin No.	Pin name		
	Option A	Option B	Option C
1	EVIN1		N.C.
2	V _{DD}		
3	/INT		
4	FOUT		
5	N.C.	/RST	N.C.
6	EVIN2		N.C.
7	SDA		
8	SCL		
9	GND		
10	SOUT		N.C.



Product name

RA8000CE YB A 0
 ① ② ③④

- ① Model CE type package 3.2 x 2.5 x 1.0 mm
- ② Frequency tolerance
 YB: $\pm 5.0 \times 10^{-6}$ / -40 °C to +85 °C (Monthly rate: ± 13.2 seconds)
 $\pm 8.0 \times 10^{-6}$ / +85 °C to +105 °C (Monthly rate: ± 21 seconds)
 $\pm 50.0 \times 10^{-6}$ / +105 °C to +125 °C (Monthly rate: ± 132 seconds)
- ③ Pin Option
 A: Option A
 B: Option B
 C: Option C
- ④ Reset output function
 0: No /RST pin
 8: With /RST pin (V_{DD} drop detection voltage: +2.4 V Typ.)

REAL TIME CLOCK MODULE (SPI)

For Automotive, Built-in 32.768 kHz DTCXO,
High Stability, +125 °C

RA4000CE

- Built in frequency adjusted 32.768 kHz crystal unit and DTCXO
- Interface Type : 3 wire / 4 wire SPI-Bus
- Time stamp function : 2 time stamps from year to second
- Reset functions with a delay : Output a reset signal when a V_{DD} voltage drop status is detected.
- Interrupt output : Wake up every minute or every second
- Alarm interruption : Day, date, hour, minute, second
- Auto repeat wakeup timer interruption
- Self-monitoring interruption : Crystal oscillation stop, V_{DD} low
- AEC-Q100 compliant

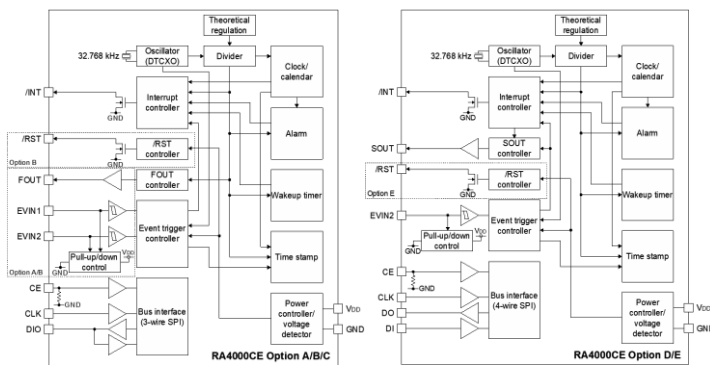


Product Number (2,000 pcs / Reel)
RA4000CE YB A0 : X1B000491A00115
RA4000CE YB B8 : X1B000491A00915
RA4000CE YB C0 : X1B000491A01015
RA4000CE YB D0 : X1B000491A01115
RA4000CE YB E8 : X1B000491A01915



RA4000CE
(3.2 × 2.5 mm, t = 1.0 mm Max.)

Block diagram



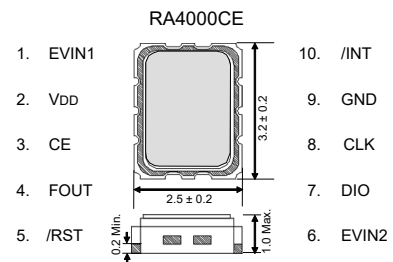
Overview

- Interface type
3 wire / 4 wire SPI-Bus
- High stability
± 5.0 × 10⁻⁶ / -40 °C to +85 °C (Monthly rate: ±13.2 seconds)
± 8.0 × 10⁻⁶ / +85 °C to +105 °C (Monthly rate: ±21 seconds)
± 50.0 × 10⁻⁶ / +105 °C to +125 °C (Monthly rate: ±132 seconds)
- Clock output function
Selectable from 32.768 kHz, 1024 Hz and 1 Hz outputs
- Wakeup timer function
Selectable from 976.56 μs to 32 years cycle
Can be used as a time integration meter
Can be used like a watchdog timer
- Time stamp function
Record data: 1/1024 seconds to 1 second, seconds, minutes, hours, days, months, years
Number of recordable events: 2 events
Trigger source: External event (EVIN) input, voltage drop/oscillation stop status detected, command input from the host
EVIN pin has function of chattering-cancel
- Reset function with a delay
Can output a reset signal when a V_{DD} voltage drop status is detected
- Status output (SOUT)
Can output the selected internal flag (interrupt flag, voltage drop detection flag) status.

Pin Function

Signal Name	I / O	Function
EVIN1, 2	Input	External event input pins Pull-up and pull-down is configurable by the registers
CE	Input	Slave select input pin A pull-down resistor (Typ. 300 kΩ) is included
CLK	Input	Serial clock input pin
DI	Input	Serial data input pin (4 wire)
DO	Output	Serial data Output pin (4 wire)
DIO	Input / Output	Serial data input/output pin (3 wire)
FOUT	Output	Frequency output pin (CMOS). 32.768 kHz (default), 1024 Hz or 1 Hz clock output is selectable. This pin can be switched to the wakeup timer interrupt output (CMOS)
/INT	Output	Interrupt output pin (N-ch. open drain). The wakeup timer, time update, alarm, and/or event detection interrupt signals can be selected to output from this pin. When two or more signals are selected, they are NORed before being output.
/RST	Output	Reset output pin (N-ch. open drain)
SOUT	Output	Status output pin
V _{DD}	-	Power-supply pin
GND	-	Ground pin

Terminal connection / External dimensions (Unit: mm)



*The above diagram is the terminal layout for Option B.
For other options, please refer to the Pin Option section.

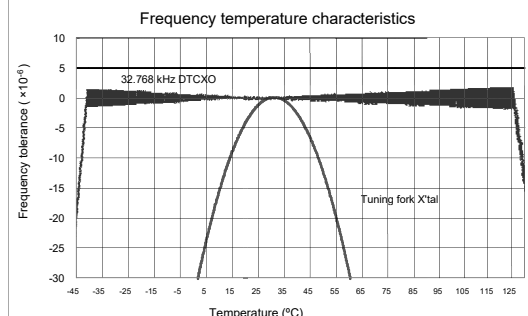
Specifications (characteristics)

* Refer to application manual for details

Electrical Characteristics

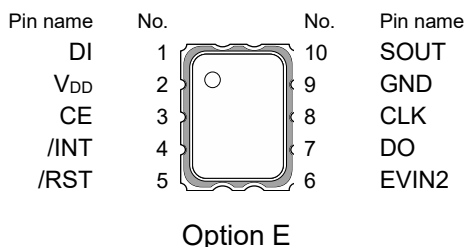
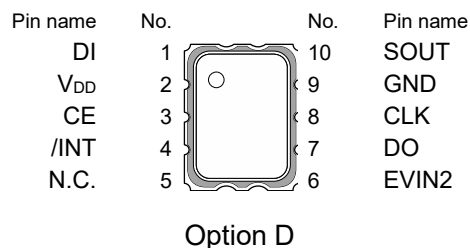
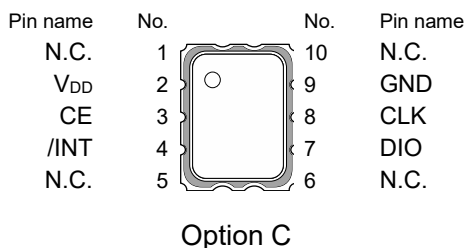
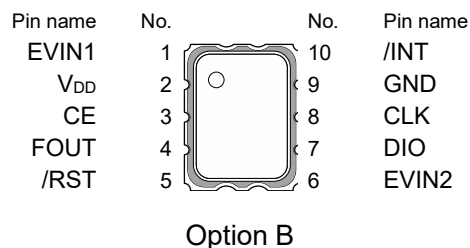
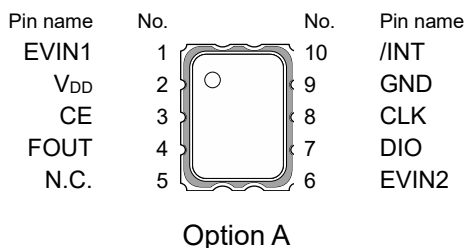
Item	Symbol	Conditions	Min.	Typ.	Max.	Unit		
Operating voltage	V _{DD}	-	1.6	3.0	5.5	V		
Temp. compensated Voltage	V _{TEM}	-	1.6	3.0	5.5	V		
Clock supply voltage	V _{CLK}	-	1.3	3.0	5.5	V		
Operating temperature	T _a	-	-40	+25	+125	°C		
Frequency tolerance	Δ f / f	YB T _a = -40 °C to +85 °C T _a = +85 °C to +105 °C T _a = +105 °C to +125 °C	±5.0		x 10 ⁻⁶			
			±8.0					
			±50.0					
Current consumption	I _{DD1}	/INT = Hi-Z, FOUT: Output OFF (Hi-Z), Temp. Compensation interval 2.0 s, CE = L	No /RST pin	V _{DD} = 5 V	-	0.35	1.8	μA
	I _{DD2}		/RST pin	V _{DD} = 3 V	-	0.3	1.7	
	I _{DD11}		With /RST pin	V _{DD} = 5 V	-	1.5	3.7	
	I _{DD12}			V _{DD} = 2 V	-	0.6	2.25	

32.768 kHz-DTCXO Frequency temperature characteristics (Example)



Pin Option

Pin No.	Pin name				
	Option A	Option B	Option C	Option D	Option E
	3 wire			4 wire	
1	EVIN1		N.C.	DI	
2	V _{DD}				
3	CE				
4	FOUT		/INT		
5	N.C.	/RST	N.C.		/RST
6	EVIN2		N.C.	EVIN2	
7	DIO			DO	
8	CLK				
9	GND				
10	/INT		N.C.	SOUT	



Product name

RA4000CE YB A 0
 ① ② ③④

- ① Model CE type package 3.2 x 2.5 x 1.0 mm
- ② Frequency tolerance
 YB: $\pm 5.0 \times 10^{-6}$ / -40 °C to +85 °C (Monthly rate: ± 13.2 seconds)
 $\pm 8.0 \times 10^{-6}$ / +85 °C to +105 °C (Monthly rate: ± 21 seconds)
 $\pm 50.0 \times 10^{-6}$ / +105 °C to +125 °C (Monthly rate: ± 132 seconds)
- ③ Pin Option
 A: Option A
 B: Option B
 C: Option C
 D: Option D
 E: Option E
- ④ Reset output function
 0: No /RST pin
 8: With /RST pin (V_{DD} drop detection voltage: +2.4 V Typ.)

REAL TIME CLOCK MODULE (I²C-Bus)

For Automotive
Built-in 32.768 kHz DTCXO, High Stability



Product Number (2,000 pcs / Reel)
RA8804CE XA: X1B000381A00100
RA8804CE XB: X1B000381A00200

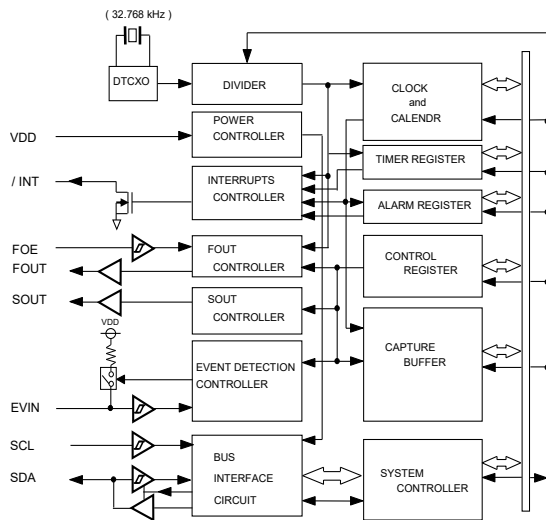
RA8804CE

- Built in frequency adjusted 32.768 kHz crystal unit and DTCXO
- Interface Type : I²C-Bus
- Selectable clock output : 32.768 kHz, 1024 Hz, 1 Hz
- Time stamp function : 1 time stamped from year to second
- Interrupt output : Wake up every minute or every second
- Alarm interruption : Day, date, hour, minute
- Auto repeat wakeup timer interruption
- Self-monitoring interruption : Crystal oscillation stop, V_{BAT} low, V_{DD} low
- SOUT pin outputs that selected flag bit value
- AEC-Q100 compliant



RA8804CE
(3.2 × 2.5 mm, t = 1.0 mm Max.)

Block diagram



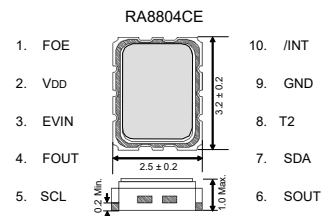
Overview

- Interface type
I²C-Bus interface Fast-Mode 400 kHz
- High stability
XA: ± 3.4 × 10⁻⁶ / -40 °C to +85 °C (equivalent to ±9 s of mo. deviation)
± 8.0 × 10⁻⁶ / +85 °C to +105 °C (equivalent to ±21 s of mo. deviation)
XB: ± 5.0 × 10⁻⁶ / -40 °C to +85 °C (equivalent to ±13 s of mo. deviation)
± 8.0 × 10⁻⁶ / +85 °C to +105 °C (equivalent to ±21 s of mo. deviation)
- Clock output function
Output frequency is selectable from 32.768 kHz, 1024 Hz, 1 Hz
- Wakeup timer function
Selectable from 244 μs to 32 years (24 bit × 1 ch.)
Timer source clock selectable from 1/60 Hz, 1 Hz, 64 Hz, 4096 Hz
Auto release after interrupt output from /INT pin at timer completes
This operation is auto repeat with a selected cycle, it can be used like a watchdog timer
- Time stamp function
1 time stamped from year to second
The time stamp trigger inputs from EVIN pin, self-monitoring and I²C software command
EVIN pin has function of chattering-cancel
- Alarm function
It is possible program from day to minute
- Internal state output function
SOUT pin outputs selected flag-bit value or specified value (H or L)

Pin Function

Signal Name	I / O	Function
SOUT	Output	Internal state output pin
SCL	Input	Serial clock input pin
FOUT	Output	Frequency output pin (CMOS) (frequency selection: 32.768 kHz, 1024 Hz, 1 Hz)
EVIN	Input	Event input pin
V _{DD}	-	Power-supply pin
FOE	Input	The FOUT output control pin
/INT	Output	Interrupts output by Alarm and Timer events (N-ch. open drain)
GND	-	Ground pin
T2	-	Test pin in the factory (Do not connect externally)
SDA	Input / Output	Serial data input and output pin.

Terminal connection / External dimensions (Unit: mm)



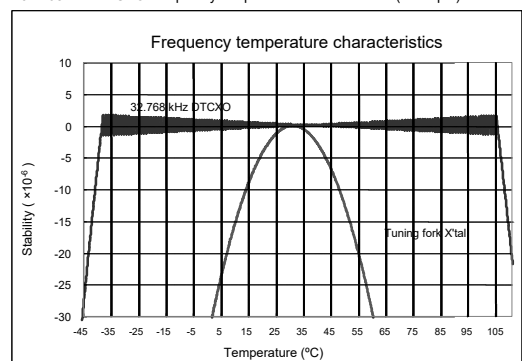
Specifications (characteristics)

* Refer to application manual for details

Electrical Characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit	
Operating voltage	V _{DD}	-	1.6	3.0	5.5	V	
Temp. compensated Voltage	V _{TEM}	-	1.5	3.0	5.5	V	
Clock supply voltage	V _{CLK}	-	1.5	3.0	5.5	V	
Operating temperature	T _a	-	-40	+25	+105	°C	
Stability	Δf / f	XA	T _a = -40 °C to +85 °C	±3.4		x 10 ⁻⁶	
			T _a = +85 °C to +105 °C	±8.0			
		XB	T _a = -40 °C to +85 °C	±5.0			
			T _a = +85 °C to +105 °C	±8.0			
Current consumption (1)	I _{DD1}	fSCL = 0 Hz, /INT = V _{DD} , FOE = GND, FOUT: OFF, Temp. Compensation interval 2.0 s	V _{DD} = 5 V	-	0.4	1.6	μA
Current consumption (2)	I _{DD2}		V _{DD} = 3 V	-	0.35	1.5	μA

32.768 kHz DTCXO Frequency temperature characteristics (Example)



REAL TIME CLOCK MODULE (I²C-Bus)

For Automotive, Power switching,
Built-in 32.768 kHz DTCXO, High Stability



Product Number (2,000 pcs / Reel)
RA8900CE UA: X1B000271A00400
RA8900CE UB: X1B000271A00500

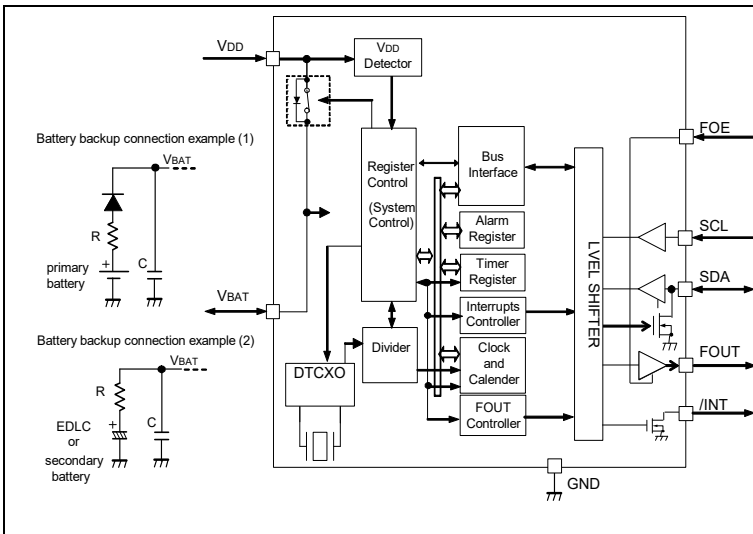
RA8900CE

- Built-in frequency adjusted 32.768 kHz crystal unit and DTCXO
- Interface Type : I²C-Bus
- Interface voltage range : 2.5 V to 5.5 V
- Temp. compensated voltage range : 2.0 V to 5.5 V
- Timekeeping voltage range : 1.6 V to 5.5 V
- Auto power switching function : Automatically switches to backup power supply by monitoring the V_{DD} voltage
- Interrupt output : Wake up every minute or every second
- Alarm interruption : Day, date, hour, minute
- Auto repeat wakeup timer interruption
- AEC-Q200 compliant



RA8900CE
(3.2 x 2.5 mm, t = 1.0 mm Max.)

Block diagram



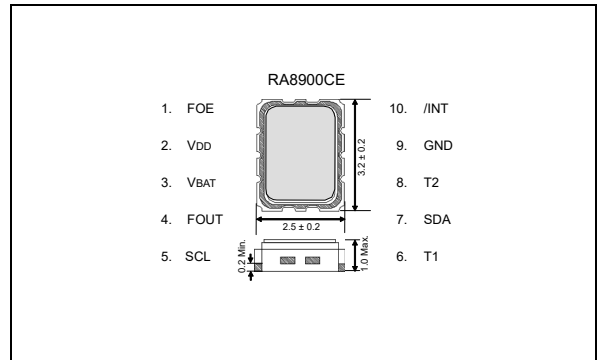
Overview

- Interface type
I²C-Bus interface Fast-Mode 400 kHz
- High stability
UA: $\pm 3.4 \times 10^{-6}$ / -40 °C to +85 °C (equiv. to ± 9 s of mo. deviation)
UB: $\pm 5.0 \times 10^{-6}$ / -40 °C to +85 °C (equiv. to ± 13 s of mo. deviation)
- Auto power switch function
The V_{DD} voltage is monitored and it switches to the backup power supply by the automatic operation
Backup power supply switching voltage 1.9 V Min.
- Clock output function
Output frequency is selectable from 32.768 kHz, 1024 Hz, 1 Hz
- Wakeup timer function
Selectable from 244 μ s to 2.8 days (12 bit x 1 ch.)
Timer source clock selectable from 1/60 Hz, 1 Hz, 64 Hz, 4096 Hz
Auto release after interrupt output from /INT pin at timer completes
This operation is auto repeat with a selected cycle, it can be used like a watchdog timer
- Alarm function
It is possible program from day to minute
- Temp. sensor function
Available readout temperature data from embedded temp sensor

Pin Function

Signal Name	I / O	Function
T1	-	Test pin in the factory (Do not connect externally)
SCL	Input	Serial clock input pin
FOUT	Output	Frequency output pin (CMOS) (frequency selection: 32.768 kHz, 1024 Hz, 1 Hz)
VBAT	-	This is a power supply pin for backup battery Connect an EDLC, a secondary battery, a primary battery In the backup voltage range, supplied to IC, from this pin
V _{DD}	-	Power-supply pin
FOE	Input	The FOUT output control pin
/INT	Output	Interrupt output (N-ch. open drain).
GND	-	Ground pin
T2	-	Test pin in the factory (Do not connect externally)
SDA	Input / Output	Serial data input and output pin

Terminal connection / External dimensions (Unit: mm)



Specifications (characteristics)

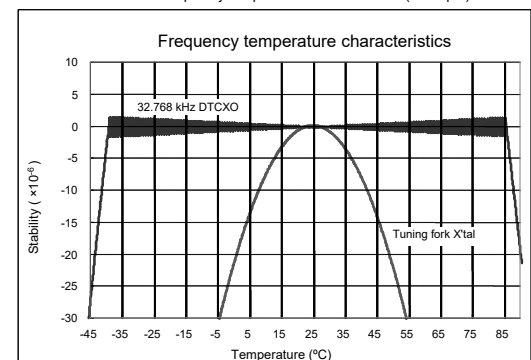
* Refer to application manual for details

Electrical Characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit	
Operating voltage	V _{DD}	-	2.5	3.0	5.5	V	
Temp. compensated Voltage	V _{TEM}	-	2.0	3.0	5.5	V	
Clock supply voltage	V _{CLK}	-	1.6	3.0	5.5	V	
V _{DD} detect voltage (3)	V _{DET3}	-	2.3	2.4	2.5	V	
Operating temperature	T _a	-	-40	+25	+85 ^{*1}	°C	
Stability	$\Delta f / f$	UA	T _a = -40 °C to +85 °C		± 3.4	$\times 10^{-6}$	
		UB	T _a = -40 °C to +85 °C		± 5.0		
		UC	T _a = -30 °C to +70 °C				
Current consumption (1)	I _{DD1}	fSCL = 0 Hz, /INT = V _{DD} , FOE = GND, V _{DD} = V _{BAT} , FOUT: OFF,	V _{DD} = 5 V	-	0.72	1.5	μ A
Current consumption (2)	I _{DD2}	Temp. Compensation interval 2.0 s	V _{DD} = 3 V	-	0.70	1.4	

*1) Please contact us about +85 °C < T_a

32.768 kHz-DTCXO Frequency temperature characteristics (Example)





MHz RANGE CRYSTAL UNIT

For Automotive



Product Number
X1E000381Axxx18

FA2016AA

- Nominal frequency range : 19.2 MHz to 54 MHz
- External dimensions : 2.0 × 1.6 × 0.5 mm
- Overtone order : Fundamental
- Applications : Car audio, Vehicle camera system,
Car navigation system, ECU clock,
Clock, Meter, Remote keyless entry
- AEC-Q200 compliant



Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
		For Automotive	
Nominal frequency range	f_nom	19.2 MHz to 54 MHz	Fundamental Please contact us about available frequencies.
Storage temperature range	T_stg	-40 °C to +125 °C	Storage as single product.
Operating temperature range	T_use	-40 °C to +125 °C	
Level of drive	DL	200 μW Max.	Recommended: 1 μW to 100 μW
Frequency tolerance	f_tol	±10 × 10 ⁻⁶	+25 °C Please contact us for requirements not listed in this specifications.
Frequency versus temperature characteristics	f_tem	±20 × 10 ⁻⁶ / -40 °C to +85 °C ±50 × 10 ⁻⁶ / -40 °C to +125 °C	Please contact us for requirements not listed in this specifications.
Load capacitance	CL	6 pF to ∞	Please specify.
Motional resistance (ESR)	R1	As per below table 1.	-40 °C to +125 °C, DL=100 μW
Frequency aging	f_age	±3 × 10 ⁻⁶ / year Max.	+25 °C, First year

Table 1. Motional resistance (ESR) R1

Frequency	Motional resistance
19.2 MHz ≤ f_nom < 20 MHz	150 Ω Max.
20 MHz ≤ f_nom < 24 MHz	100 Ω Max.
24 MHz ≤ f_nom < 26 MHz	80 Ω Max.
26 MHz ≤ f_nom ≤ 54 MHz	60 Ω Max.

Product name

Product name (Standard form) **FA2016AA** 24.000000MHz 12.0 +10.0-10.0

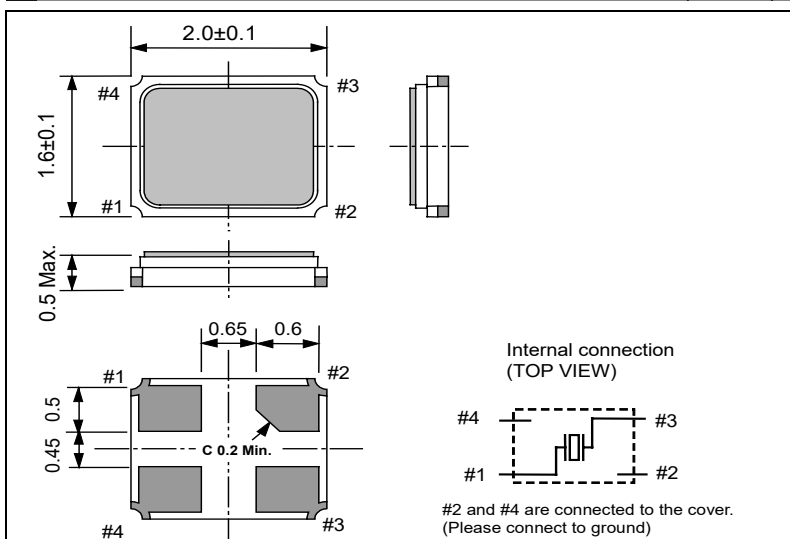
① Model ② Frequency ③ Load capacitance(pF) ④ Frequency tolerance(× 10⁻⁶, +25 °C)

①Model ②Frequency ③Load capacitance(pF) ④Frequency tolerance(× 10⁻⁶, +25 °C)

In addition to the above mentioned specification item, please specify frequency temperature characteristics and operating temperature range in case of inquiry.

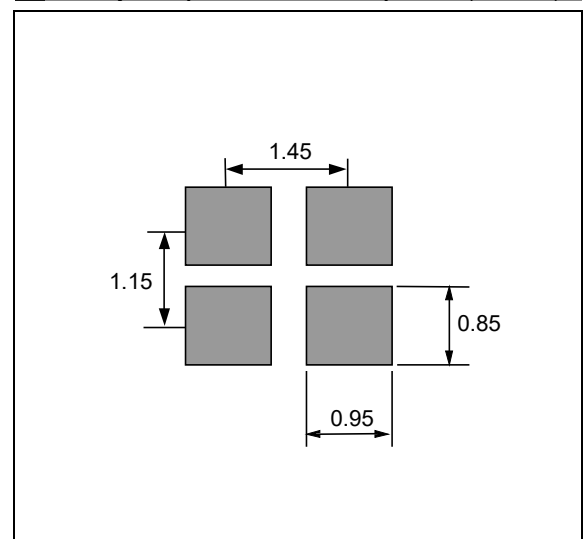
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



**MHz RANGE CRYSTAL UNIT**

For Automotive

Product Number
X1E000341xxxx00**FA-238A**

- Nominal frequency range : 12 MHz to 50 MHz
- External dimensions : 3.2× 2.5×0.7 mm
- Overtone order : Fundamental
- Applications : Car audio, ECU clock, Clock,
Car navigation system,
Meter, Remote keyless entry
- AEC-Q200 compliant

**Specifications (characteristics)**

Item	Symbol	Specifications	Conditions / Remarks
		For Automotive	
Nominal frequency range	f_nom	12 MHz to 50 MHz	Fundamental Please contact us about available frequencies.
Storage temperature range	T_stg	-40 °C to +125 °C	Storage as single product.
Operating temperature range	T_use	-40 °C to +125 °C	
Level of drive	DL	200 μW Max.	Recommended: 1 μW to 100 μW
Frequency tolerance	f_tol	±15 × 10 ⁻⁶ to ±50 × 10 ⁻⁶	+25 °C Please contact us for requirements not listed in this specifications.
Frequency versus temperature characteristics	f_tem	±30 × 10 ⁻⁶ / -40 °C to +85 °C ±50 × 10 ⁻⁶ / -40 °C to +125 °C	Please contact us for requirements not listed in this specifications.
Load capacitance	CL	7 pF to ∞	Please specify.
Motional resistance (ESR)	R1	As per below table1.	-40 °C to +125 °C, DL=100 μW
Frequency aging	f_age	±5 × 10 ⁻⁶ / year Max.	+25 °C, First year

Table 1. Motional resistance (ESR) R1

Frequency	Motional resistance
12 MHz ≤ f_nom ≤ 13 MHz	120 Ω Max.
13 MHz < f_nom < 20 MHz	80 Ω Max.
20 MHz ≤ f_nom < 25 MHz	60 Ω Max.
25 MHz ≤ f_nom ≤ 50 MHz	50 Ω Max.

Product name

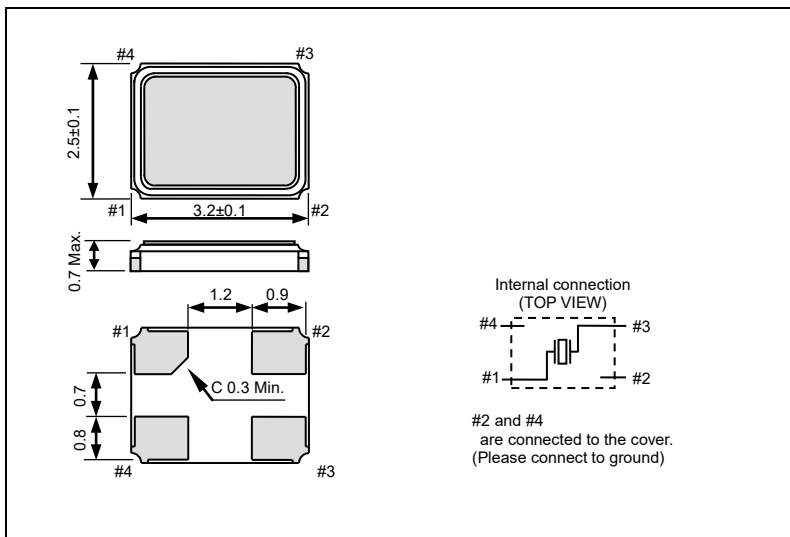
Product name FA-238A 28.636360MHz 18.0 +15.0-15.0
(Standard form) ① ② ③ ④

①Model ②Frequency ③Load capacitance(pF) ④Frequency tolerance(× 10⁻⁶, +25 °C)

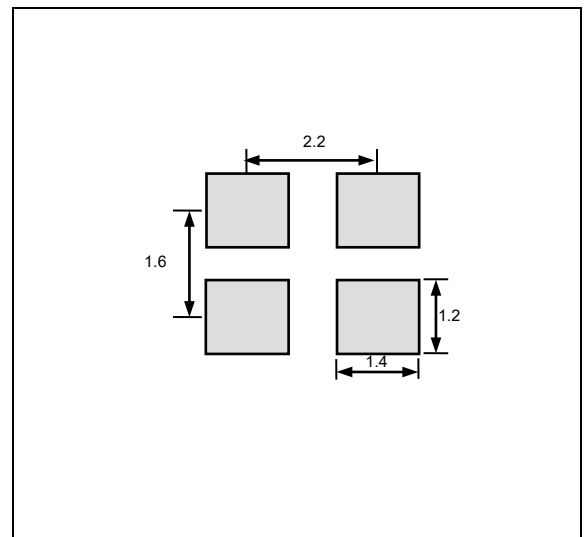
In addition to the above mentioned specification item, please specify frequency temperature characteristics and operating temperature range in case of inquiry.

External dimensions

(Unit:mm)

**Footprint (Recommended)**

(Unit:mm)



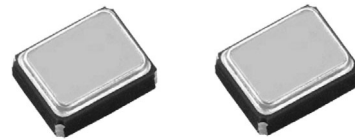
**MHz RANGE CRYSTAL UNIT**

Built-in thermistor

For Automotive

Product Number
FA2016ASA: X1E000431xxxx16**FA2016ASA**

- Nominal frequency range : 38.4 MHz
- External dimensions : 2.0 × 1.6 × 0.68 mm
- Overtone order : Fundamental
- Applications : GPS module, Telematics module
Car navigation system, etc.
- AEC-Q200 compliant

**Specifications (characteristics)**

Item	Symbol	Specifications	Conditions / Remarks
Nominal frequency range	f_nom	38.4 MHz	Fundamental Please contact us about available frequencies.
Storage temperature range	T_stg	-40 °C to +125 °C	Storage as single product.
Operating temperature range	T_use	-40 °C to +105 °C	
Level of drive	DL	200 μW Max.	Recommended: 1 μW to 100 μW
Frequency tolerance	f_tol	±10 × 10 ⁻⁶	Please contact us about reference temperature. Please contact us for inquiries.
Frequency versus temperature characteristics	f_tem	±12 × 10 ⁻⁶	-30 °C to +85 °C Please contact us for inquiries.
		±30 × 10 ⁻⁶	-40 °C to +105 °C Please contact us for inquiries.
Load capacitance	CL	6 pF to ∞	Please specify.
Motional resistance (ESR)	R1	50 Ω Max.	-40 °C to +105 °C
Frequency aging	f_age	±1 × 10 ⁻⁶ / year Max.	+25 °C, First year
Thermistor resistance	—	Please contact us about specifications.	
Thermistor B constant	—		

Product name

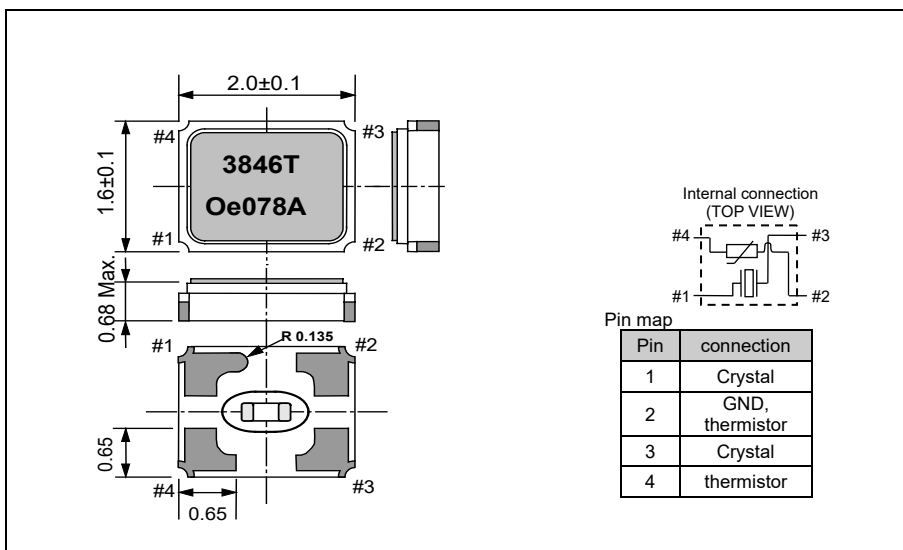
Product name FA2016ASA 38.400000MHz 7.0 +10.0-10.0
 (Standard form) ① ② ③ ④

①Model ②Frequency ③Load capacitance(pF) ④Frequency tolerance(× 10⁻⁶, +25 °C)

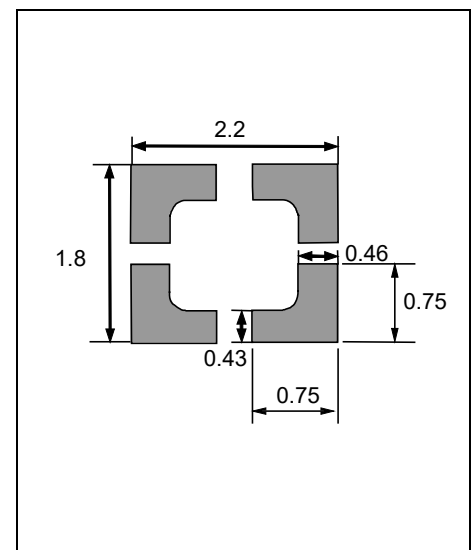
In addition to the above mentioned specification item, please specify frequency temperature characteristics and operating temperature range in case of inquiry.

External dimensions

(Unit:mm)

**Footprint (Recommended)**

(Unit:mm)



CRYSTAL OSCILLATOR (SPXO)

OUTPUT : CMOS



Product Number
 SG2016CAA: X1G005341xxxx00
 SG2520CAA: X1G005951xxxx16

SG2016CAA
 SG2520CAA

- Frequency : 19 standard frequencies
- Supply voltage : 1.8 V to 3.3 V Typ.
- Function : Standby (\overline{ST})
- Operation temperature : -40 °C to +125 °C
- AEC-Q200 compliant



Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks	
Output frequency	f_o	8 MHz 10 MHz 11.2896 MHz 12 MHz 12.288 MHz 14.7456 MHz 16.6666 MHz 20 MHz 22.5792 MHz 24 MHz 24.576 MHz 25 MHz 27 MHz 33 MHz 33.3333 MHz 40 MHz 48 MHz 50 MHz 54 MHz		
Supply voltage	V_{CC}	T: 1.60 V to 3.63 V		
Storage temperature range	T_{stg}	-55 °C to +125 °C	Storage as single product.	
Operating temperature range	T_{use}	H: -40 °C to +105 °C J: -40 °C to +125 °C		
Frequency tolerance	f_{tol}	J: $\pm 50 \times 10^{-6}$ L: $\pm 100 \times 10^{-6}$		
Current consumption	I_{cc}	$V_{CC} = 1.8 V \pm 10 \%$	No load condition, 8 MHz $\leq f_o \leq 20$ MHz	
		$V_{CC} = 2.5 V \pm 10 \%$		No load condition, 20 MHz $< f_o \leq 40$ MHz
		$V_{CC} = 3.3 V \pm 10 \%$		No load condition, 40 MHz $< f_o \leq 54$ MHz
Stand-by current	I_{std}	2.7 μA Max. 3.1 μA Max. 3.3 μA Max.	$\overline{ST} = GND$	
Symmetry	SYM	45 % to 55 %	50 % V_{CC} level, $L_{CMOS} \leq 15$ pF	
Output voltage	V_{OH}	90 % V_{CC} Min.	I_{OH} 1.8 V $\pm 10 \%$ -1.5 mA 2.5 V $\pm 10 \%$ -3 mA 3.3 V $\pm 10 \%$ -4 mA	
	V_{OL}	10 % V_{CC} Max.	I_{OL} 1.5 mA 3 mA 4 mA	
	V_{OH}	$V_{CC} - 0.4$ V Min.	I_{OH} 1.8 V $\pm 10 \%$ -3 mA 2.5 V $\pm 10 \%$ -4 mA 3.3 V $\pm 10 \%$ -6 mA	
	V_{OL}	0.4 V Max.	I_{OL} 3 mA 4 mA 6 mA	
Output load condition	L_{CMOS}	15 pF Max.		
Input voltage	V_{IH}	80 % V_{CC} Min.	\overline{ST} terminal	
	V_{IL}	20 % V_{CC} Max.		
Rise time and Fall time	t_r / t_f	3 ns Max. 3.5 ns Max. (@1.8 V $\pm 10 \%$)	20 % V_{CC} to 80 % V_{CC} level, $L_{CMOS} = 15$ pF	
Start-up time	t_{str}	5 ms Max.	$t = 0$ at 90 % V_{CC}	
Frequency aging	f_{age}	$\pm 3 \times 10^{-6}$ / year Max.	+25 °C, First year	

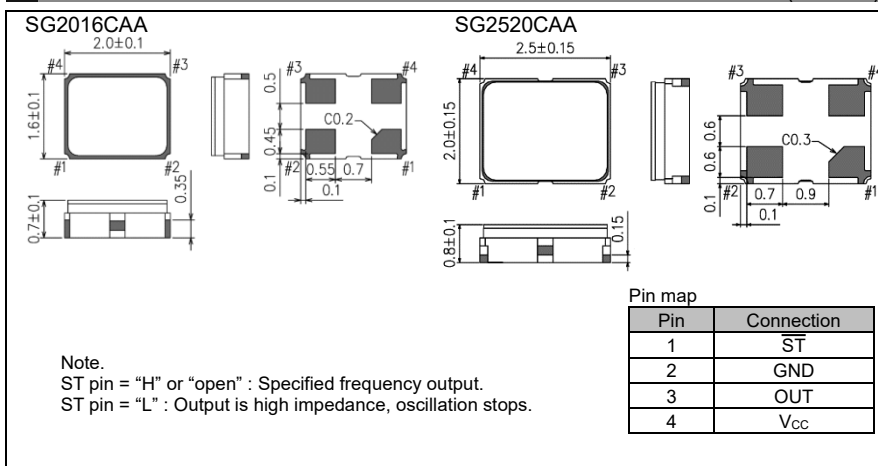
Product Name SG2016CAA 25.000000MHz I J H A
 (Standard form) Model Name Frequency ①②③ — Standard Specification A

①Supply voltage	
T	1.8 V to 3.3 V Typ.

②Frequency tolerance / ③Operating temperature	
JH	$\pm 50 \times 10^{-6}$ / -40 °C to +105 °C
LJ	$\pm 100 \times 10^{-6}$ / -40 °C to +125 °C

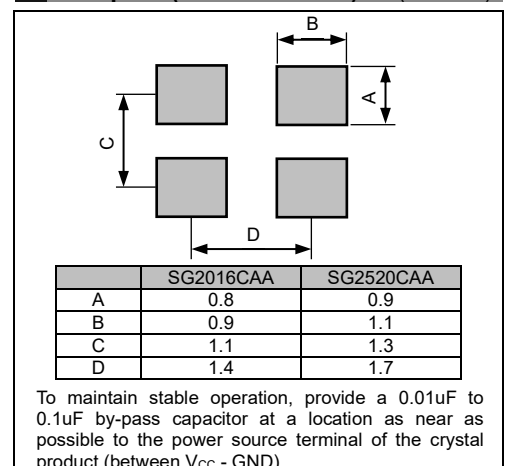
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)





CRYSTAL OSCILLATOR (Programmable)
OUTPUT: CMOS



Product Number
X1G005991xxxx16

SG-8201CJA

- Frequency range : 1.2 MHz to 170 MHz
- Supply voltage : 1.62 V to 3.63 V
- Function : Output enable (OE/ \overline{OE}) or Standby (\overline{ST} / \overline{ST})
- Frequency tolerance, operating temperature:
 - $\pm 15 \times 10^{-6}$ (-40 °C to +105 °C)
 - $\pm 25 \times 10^{-6}$, $\pm 50 \times 10^{-6}$ (-40 °C to +125 °C)
- External dimensions : 2.0 × 1.6 × 0.6 (mm)
- PLL technology to enable short lead time
- AEC-Q100 compliant



Specifications (characteristics)

Item	Symbol	Specifications			Conditions/Remarks			
Supply voltage	V _{CC}	1.80 V Typ.	2.50 V Typ.	3.30 V Typ.				
		1.62 V to 1.98 V	2.25 V to 2.75 V	2.97 V to 3.63 V				
Output frequency range	f _o	1.2 MHz to 170 MHz						
Storage temperature	T _{stg}	-55 °C to +150 °C			Storage as single product.			
Operating temperature range	T _{use}	H: -40 °C to +105 °C						
		J: -40 °C to +125 °C						
Frequency tolerance range ¹	f _{tol}	B: $\pm 15 \times 10^{-6}$			T _{use} = -40 °C to +105 °C			
		D: $\pm 25 \times 10^{-6}$			T _{use} = -40 °C to +125 °C			
		J: $\pm 50 \times 10^{-6}$			T _{use} = -40 °C to +125 °C			
Current consumption	I _{CC}	5.2 mA Typ.	5.4 mA Typ.	5.6 mA Typ.	1.2 MHz ≤ f _o ≤ 25 MHz	No load, Rise/Fall time: Default		
		7.0 mA Max.	7.2 mA Max.	7.5 mA Max.				
		5.4 mA Typ.	5.7 mA Typ.	6.1 mA Typ.	25 MHz < f _o ≤ 50 MHz			
		7.3 mA Max.	7.6 mA Max.	8.1 mA Max.				
		5.7 mA Typ.	6.3 mA Typ.	7.0 mA Typ.	50 MHz < f _o ≤ 75 MHz			
		7.7 mA Max.	8.2 mA Max.	9.1 mA Max.				
		6.2 mA Typ.	6.9 mA Typ.	7.9 mA Typ.	75 MHz < f _o ≤ 100 MHz			
		8.2 mA Max.	9.1 mA Max.	10.4 mA Max.				
		6.9 mA Typ.	7.9 mA Typ.	9.1 mA Typ.	100 MHz < f _o ≤ 125 MHz			
		9.4 mA Max.	10.7 mA Max.	12.4 mA Max.				
7.8 mA Typ.	9.2 mA Typ.	11.2 mA Typ.	125 MHz < f _o ≤ 170 MHz					
10.4 mA Max.	12.4 mA Max.	15.0 mA Max.						
Output disable current	I _{dis}	5.0 mA Typ.	5.0 mA Typ.	5.1 mA Typ.	OE = GND, \overline{OE} = V _{CC}			
		7.2 mA Max.	7.3 mA Max.	7.4 mA Max.				
Standby current	I _{std}	0.3 μA Typ.	0.3 μA Typ.	0.5 μA Typ.	\overline{ST} = GND, \overline{ST} = V _{CC}			
		15.0 μA Max.	15.0 μA Max.	15.0 μA Max.				
Symmetry	SYM	45 % to 55 %			50 % V _{CC} Level, L _{CMOS} ≤ 15 pF			
Output voltage (DC characteristics)	V _{OH}	90 % V _{CC} Min.			Rise/Fall time			
					Default 'A' Option ²	Other Options	I _{OH}	I _{OL}
	V _{OL}	10 % V _{CC} Max.			fo > 125 MHz	B: Faster	-2.0 mA	2.0 mA
					75 MHz < fo ≤ 125 MHz	C: Fast	-1.0 mA	1.0 mA
				50 MHz < fo ≤ 75 MHz	D: Slow	-0.5 mA	0.5 mA	
				fo ≤ 50 MHz	E: Slower	-0.2 mA	0.2 mA	
Output load condition	L _{CMOS}	15 pF Max.						
Input voltage	V _{IH}	70 % V _{CC} Min.			Pin 1			
	V _{IL}	30 % V _{CC} Max.						
Rise/Fall time	tr/tf	-			Default 'A' Option ²	Other Options	20 % - 80 % V _{CC} , L _{CMOS} = 15 pF	
		2.0 ns Max.			fo > 125 MHz	B: Faster		
		2.5 ns Max.			75 MHz < fo ≤ 125 MHz	C: Fast		
		4.0 ns Max.			50 MHz < fo ≤ 75 MHz	D: Slow		
				fo ≤ 50 MHz	E: Slower			
Output disable time (OE)	tstp_oe	1 μs Max.			Measured from the time OE or \overline{ST} pin crosses 30 % V _{CC} or measured from the time \overline{OE} or \overline{ST} pin crosses 70 % V _{CC}			
Output disable time (ST)	tstp_st	1 μs Max.			Measured from the time OE pin crosses 70 % V _{CC} or measured from the time \overline{OE} pin crosses 30 % V _{CC}			
Output enable time (OE)	tsta_oe	100 ns + 2 clock cycle Max.			Measured from the time OE pin crosses 70 % V _{CC} or measured from the time \overline{OE} pin crosses 30 % V _{CC}			
Output enable time (ST)	tsta_st	3 ms Max.			Measured from the time \overline{ST} pin crosses 70 % V _{CC} or measured from the time \overline{ST} pin crosses 30 % V _{CC}			
Start-up time	t _{str}	3 ms Max.			Measured from the time V _{CC} reaches its rated minimum value, 1.62 V			
Phase Jitter	t _{pj}	1.2 ps Typ.			fo = 25 MHz, Offset frequency: 12 kHz to 5 MHz			
		1.2 ps Typ.			fo = 50 MHz, Offset frequency: 12 kHz to 20 MHz			
		1.2 ps Typ.			fo = 75 MHz, Offset frequency: 12 kHz to 20 MHz			
		1.2 ps Typ.			fo = 100 MHz, Offset frequency: 12 kHz to 20 MHz			
		1.1 ps Typ.			fo = 125 MHz, Offset frequency: 12 kHz to 20 MHz			
		1.4 ps Typ.			fo = 150 MHz, Offset frequency: 12 kHz to 20 MHz			
				fo = 170 MHz, Offset frequency: 12 kHz to 20 MHz				
Frequency aging	f _{age}	This is included in frequency tolerance specification.			+25 °C, first year			

¹ Frequency tolerance includes initial frequency tolerance, temperature variation, supply voltage variation, reflow drift, load drift and aging (+25 °C, 1 year).

² Default 'A' Rise/Fall time and I_{OH}/I_{OL} are dependent on programmed frequency.

Pin description

Pin	Name	I/O type	Function	
1	OE	Input	Output Enable	High ^{*1} or Open: Specified frequency output from OUT pin Low: OUT pin is low (pull down with 500 kΩ), only output driver is disabled.
	\overline{OE}	Input	Output Enable	Low ^{*2} or Open: Specified frequency output from OUT pin High: OUT pin is low (pull down with 500 kΩ), only output driver is disabled.
	\overline{ST}	Input	Standby	High ^{*1*3} : Specified frequency output from OUT pin Low: OUT pin is low (pull down with 500 kΩ), Device goes to standby mode. Supply current reduces to the least as I _{std} .
	ST	Input	Standby	Low ^{*2*3} : Specified frequency output from OUT pin High: OUT pin is low (pull down with 500 kΩ), Device goes to standby mode. Supply current reduces to the least as I _{std} .
2	GND	Power	Ground	
3	OUT	Output	Clock output	
4	V _{CC}	Power	Power supply	

*1 If fixing it at High, please connect to V_{CC} directly.

*2 If fixing it at Low, please connect to GND directly.

*3 If necessary to use Open, please select Output Enable function.

Product Name

SG-8201CJA 170.000000MHz T D J P A
① ② ③ ④ ⑤ ⑥ ⑦ ⑧

- ① Model ② Package type (CJ: 2.0 mm × 1.6 mm)
③ Frequency ④ Supply voltage (T: 1.8 V to 3.3 V Typ.)
⑤ Frequency tolerance ⑥ Operating temperature
⑦ Function ⑧ Rise/Fall time

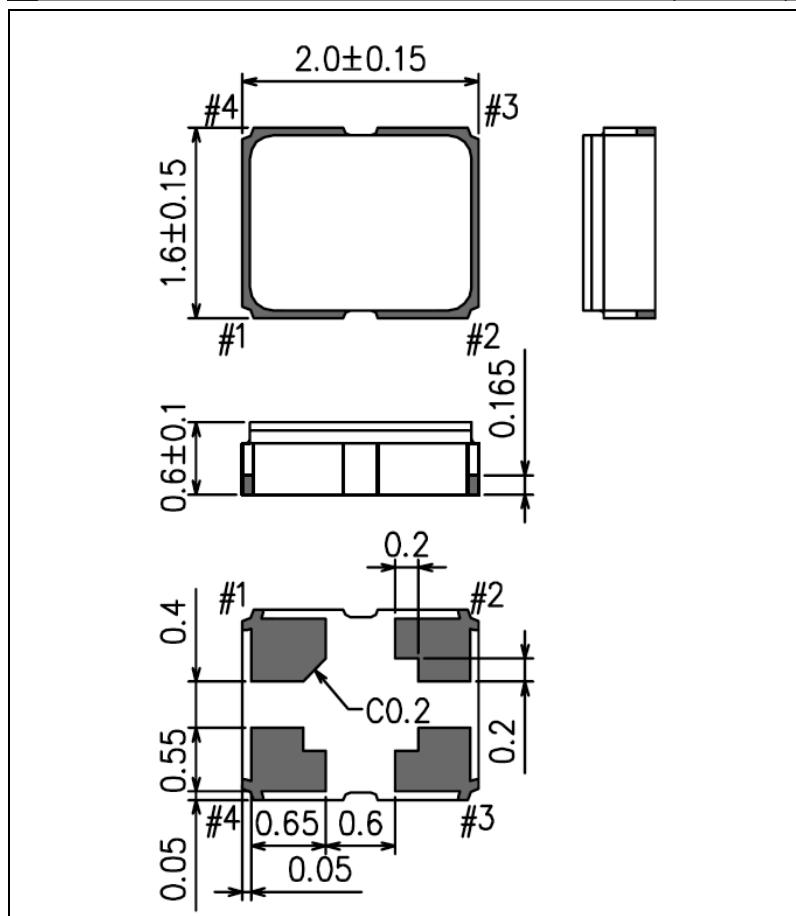
⑤ Frequency tolerance / ⑥ Operating temperature	
BH	±15 × 10 ⁻⁶ / -40 °C to +105 °C
DJ	±25 × 10 ⁻⁶ / -40 °C to +125 °C
JJ	±50 × 10 ⁻⁶ / -40 °C to +125 °C

⑧ Rise/Fall time	
A	Default
B	Faster
C	Fast
D	Slow
E	Slower

⑦ Function	
P	Output Enable (OE)
Q	Output Enable (\overline{OE})
S	Standby (\overline{ST})
T	Standby (ST)

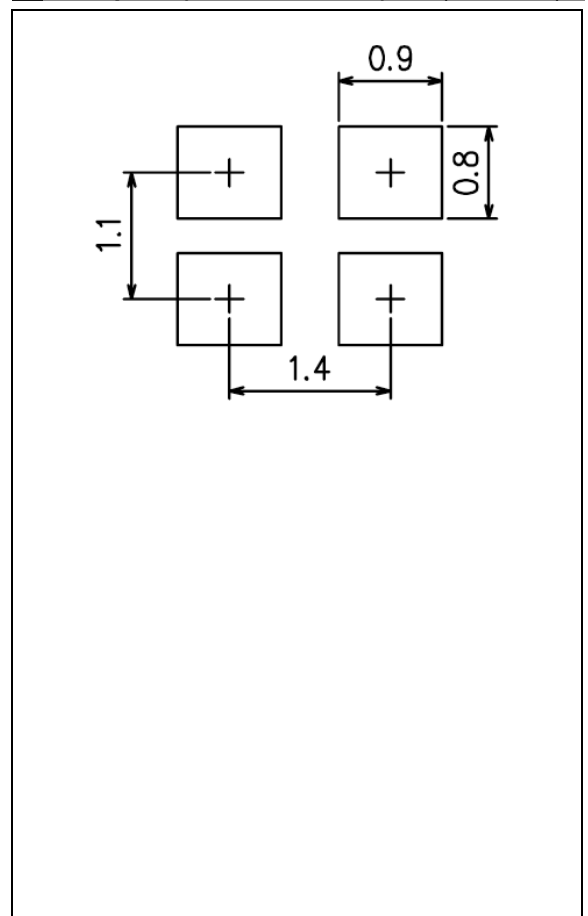
External dimensions

(Unit: mm)



Footprint (Recommended)

(Unit: mm)



Notes:

In order to achieve optimum jitter performance, the 0.01 μF to 0.1 μF capacitor between V_{CC} and GND should be placed. It is also recommended that the capacitors are placed on the device side of the PCB, as close to the device as possible and connected together with short wiring pattern.

CRYSTAL OSCILLATOR (Programmable)
OUTPUT: CMOS



Product Number
X1G005171xxxx00

SG-8101CGA

- Frequency range : 0.67 MHz to 170 MHz (1 ppm Step)
- Supply voltage : 1.62 V to 3.63 V
- Function : Output enable (OE) or Standby (ST)
- Frequency tolerance, operating temperature:
 - ±15 ppm (-40 °C to +85 °C)
 - ±20 ppm (-40 °C to +105 °C)
 - ±50 ppm (-40 °C to +125 °C)
 - ±100 ppm (-40 °C to +125 °C)
- Package : 2.5 x 2.0 (mm)
- PLL technology to enable short lead time
- AEC-Q100 compliant



Specifications (characteristics)

Item	Symbol	Specifications	Conditions/Remarks																																													
Supply voltage	V_{CC}	1.80 V Typ. 1.62 V to 1.98 V 1.98 V to 2.20 V 2.50 V Typ. 2.20 V to 2.80 V 3.30 V Typ. 2.70 V to 3.63 V																																														
Output frequency range	f_o	0.67 MHz to 170 MHz																																														
Storage temperature range	T_{stg}	-40 °C to +125 °C	Storage as single product.																																													
Operating temperature range	T_{use}	-40 °C to +85 °C -40 °C to +105 °C -40 °C to +125 °C																																														
Frequency tolerance*1	f_{tol}	B : ±15 × 10 ⁻⁶ C : ±20 × 10 ⁻⁶ J : ±50 × 10 ⁻⁶ L : ±100 × 10 ⁻⁶	T_{use} = -40 °C to +85 °C T_{use} = -40 °C to +105 °C T_{use} = -40 °C to +125 °C T_{use} = -40 °C to +125 °C																																													
Current consumption	I_{CC}	3.3 mA Max. 3.4 mA Max. 3.5 mA Max. 3.6 mA Max. 3.2 mA Max. 3.3 mA Max. 3.4 mA Max. 3.5 mA Max. 2.7 mA Typ. 2.9 mA Typ. 3.0 mA Typ. 3.1 mA Typ. 5.6 mA Max. 5.9 mA Max. 6.8 mA Max. 8.2 mA Max. 5.5 mA Max. 5.8 mA Max. 6.7 mA Max. 8.1 mA Max.	T_{use} = +125 °C T_{use} = +105 °C T_{use} = +25 °C T_{use} = +125 °C T_{use} = +105 °C No load, f_o = 20 MHz T_{use} = +25 °C No load, f_o = 170 MHz T_{use} = +125 °C T_{use} = +105 °C																																													
Output disable current	I_{dis}	3.3 mA Max. 3.4 mA Max. 3.4 mA Max. 3.6 mA Max. 3.2 mA Max. 3.3 mA Max. 3.3 mA Max. 3.5 mA Max.	T_{use} = +125 °C T_{use} = +105 °C OE = GND, f_o = 170 MHz																																													
Standby current	I_{std}	2.3 µA Max. 2.5 µA Max. 3.0 µA Max. 4.2 µA Max. 0.9 µA Max. 1.0 µA Max. 1.5 µA Max. 2.5 µA Max. 0.3 µA Typ. 0.4 µA Typ. 0.5 µA Typ. 1.1 µA Typ.	T_{use} = +125 °C T_{use} = +105 °C T_{use} = +25 °C ST = GND																																													
Symmetry	SYM	45 % to 55 %	50 % V_{CC} Level																																													
Output voltage (DC characteristics)	V_{OH} V_{OL}	90 % V_{CC} Min. 10 % V_{CC} Max.	<table border="1"> <thead> <tr> <th colspan="6">IOH/IOL Conditions [mA]</th> </tr> <tr> <th>Rise/Fall time selection</th> <th>V_{CC}</th> <th>*A</th> <th>*B</th> <th>*C</th> <th>*D</th> </tr> </thead> <tbody> <tr> <td rowspan="2">Default ($f_o > 40$ MHz), Fast</td> <td>I_{OH}</td> <td>-2.5</td> <td>-3.5</td> <td>-4.0</td> <td>-5.0</td> </tr> <tr> <td>I_{OL}</td> <td>2.5</td> <td>3.5</td> <td>4.0</td> <td>5.0</td> </tr> <tr> <td rowspan="2">Default ($f_o \leq 40$ MHz)</td> <td>I_{OH}</td> <td>-1.5</td> <td>-2.0</td> <td>-2.5</td> <td>-3.0</td> </tr> <tr> <td>I_{OL}</td> <td>1.5</td> <td>2.0</td> <td>2.5</td> <td>3.0</td> </tr> <tr> <td rowspan="2">Slow</td> <td>I_{OH}</td> <td>-1.0</td> <td>-1.5</td> <td>-2.0</td> <td>-2.5</td> </tr> <tr> <td>I_{OL}</td> <td>1.0</td> <td>1.5</td> <td>2.0</td> <td>2.5</td> </tr> </tbody> </table> <p>*A: 1.62 V to 1.98 V, *B: 1.98 V to 2.20 V, *C: 2.20 V to 2.80 V, *D: 2.70 V to 3.63 V</p>	IOH/IOL Conditions [mA]						Rise/Fall time selection	V_{CC}	*A	*B	*C	*D	Default ($f_o > 40$ MHz), Fast	I_{OH}	-2.5	-3.5	-4.0	-5.0	I_{OL}	2.5	3.5	4.0	5.0	Default ($f_o \leq 40$ MHz)	I_{OH}	-1.5	-2.0	-2.5	-3.0	I_{OL}	1.5	2.0	2.5	3.0	Slow	I_{OH}	-1.0	-1.5	-2.0	-2.5	I_{OL}	1.0	1.5	2.0	2.5
IOH/IOL Conditions [mA]																																																
Rise/Fall time selection	V_{CC}	*A	*B	*C	*D																																											
Default ($f_o > 40$ MHz), Fast	I_{OH}	-2.5	-3.5	-4.0	-5.0																																											
	I_{OL}	2.5	3.5	4.0	5.0																																											
Default ($f_o \leq 40$ MHz)	I_{OH}	-1.5	-2.0	-2.5	-3.0																																											
	I_{OL}	1.5	2.0	2.5	3.0																																											
Slow	I_{OH}	-1.0	-1.5	-2.0	-2.5																																											
	I_{OL}	1.0	1.5	2.0	2.5																																											
Output load condition	L_{CMOS}	15 pF Max.																																														
Input voltage	V_{IH} V_{IL}	70 % V_{CC} Min. 30 % V_{CC} Max.	OE or ST																																													
Rise and Fall time	Default Fast Slow	t_r/t_f	<table border="1"> <thead> <tr> <th>Condition</th> <th>t_r/t_f</th> </tr> </thead> <tbody> <tr> <td>$f_o > 40$ MHz</td> <td>3.0 ns Max.</td> </tr> <tr> <td>$f_o \leq 40$ MHz</td> <td>6.0 ns Max.</td> </tr> <tr> <td>$f_o = 0.67$ MHz to 170 MHz</td> <td>3.0 ns Max.</td> </tr> <tr> <td>$f_o = 0.67$ MHz to 20 MHz</td> <td>10.0 ns Max.</td> </tr> </tbody> </table>	Condition	t_r/t_f	$f_o > 40$ MHz	3.0 ns Max.	$f_o \leq 40$ MHz	6.0 ns Max.	$f_o = 0.67$ MHz to 170 MHz	3.0 ns Max.	$f_o = 0.67$ MHz to 20 MHz	10.0 ns Max.																																			
Condition	t_r/t_f																																															
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$f_o = 0.67$ MHz to 170 MHz	3.0 ns Max.																																															
$f_o = 0.67$ MHz to 20 MHz	10.0 ns Max.																																															
Disable Time	t_{stp}	1 µs Max.	Measured from the time OE or ST pin crosses 30 % V_{CC}																																													
Enable Time	t_{sta}	1 µs Max.	Measured from the time OE pin crosses 70 % V_{CC}																																													
Resume Time	t_{res}	3 ms Max.	Measured from the time ST pin crosses 70 % V_{CC}																																													
Start-up time	t_{str}	3 ms Max.	Measured from the time V_{CC} reaches its rated minimum value, 1.62 V																																													
Frequency aging	f_{aging}	This is included in frequency tolerance specification.	+25 °C, first year																																													

*1 Frequency tolerance includes initial frequency tolerance, temperature variation, supply voltage variation, reflow drift, load drift and aging (+25 °C, 1 year).

Pin description

Pin	Name	I/O type	Function
1	OE	Input	Output enable High: Specified frequency output from OUT pin Low: Out pin is low (weak pull down), only output driver is disabled.
	ST	Input	Standby High: Specified frequency output from OUT pin Low: Out pin is low (weak pull down), Device goes to standby mode. Supply current reduces to the least as I_{std} .
2	GND	Power	Ground
3	OUT	Output	Clock output
4	V_{CC}	Power	Power supply



Product Name

SG-8101CGA 170.000000MHz T C H P A
① ② ③ ④⑤⑥⑦⑧

② Package type
CG 2.5 mm x 2.0 mm

⑤⑥	⑤ Frequency tolerance	⑥ Operating temperature
BG	B: 15×10^{-6}	G: -40 °C to +85 °C
CH	C: 20×10^{-6}	H: -40 °C to +105 °C
JJ	J: 50×10^{-6}	J: -40 °C to +125 °C
LJ	L: 100×10^{-6}	

⑦ Function
P Output Enable
S Standby

④ Supply voltage
T 1.8 V to 3.3 V Typ.

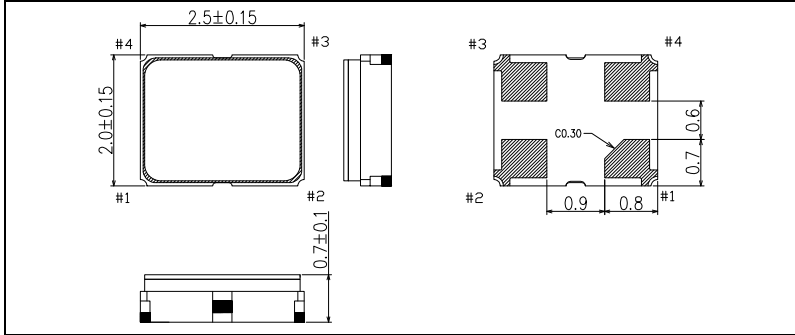
⑧ Rise/Fall time
A Default
B Fast
C* Slow

- ① Model, ② Package type,
- ③ Frequency, ④ Supply voltage,
- ⑤ Frequency tolerance, ⑥ Operating temperature,
- ⑦ Function, ⑧ Rise/Fall time

* Available only when fo ≤ 20 MHz

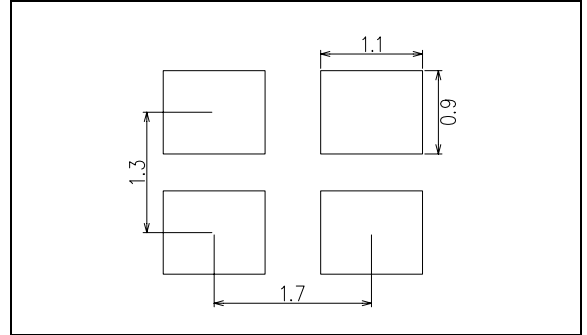
External dimensions

(Unit: mm)



Footprint (Recommended)

(Unit: mm)



Notes:

In order to achieve optimum jitter performance, the 0.1 μF capacitor between V_{CC} and GND should be placed. It is also recommended that the capacitors are placed on the device side of the PCB, as close to the device as possible and connected together with short wiring pattern.



Product Name

SG-9101CGA 170.000000MHz C 20 P J A A A

① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩

- ① Model, ② Package type, ③ Frequency,
 ④ Spread type, ⑤ Spread percentage code,
 ⑥ Function, ⑦ Operating temperature,
 ⑧ Modulation frequency, ⑨ Modulation profile, ⑩ Rise/Fall time

② Package Type	CG 2.5 mm x 2.0 mm
----------------	--------------------

⑦ Operating temperature	J -40 °C to +125 °C
-------------------------	---------------------

⑨ Modulation profile	A Hershey-kiss (default)
	B Sine-wave
	C Triangle

④ Spread type	C Center spread
	D Down spread

⑧ Modulation frequency	A 25.4 kHz (default)
	B 12.7 kHz
	C 8.5 kHz
	D 6.3 kHz

⑥ Function	P Output enable
	S Standby

⑩ Rise/Fall time	A Default
	B Fast
	C* Slow

* Available only when $f \leq 20$ MHz

Spread spectrum configuration

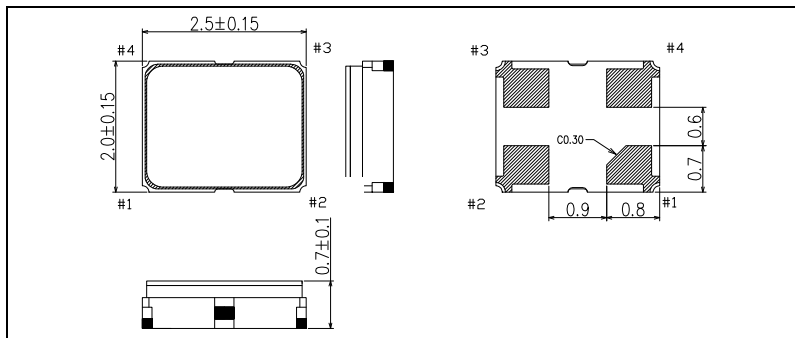
④	C: Center spread modulation	⑤ Code	02	05	07	10	15	20
		Spread percentage	±0.25 %	±0.5 %	±0.75 %	±1.0 %	±1.5 %	±2.0 %
④	D: Down spread modulation	⑤ Code	05	10	15	20	30	40
		Spread percentage	-0.5 %	-1.0 %	-1.5 %	-2.0 %	-3.0 %	-4.0 %

Modulation frequency: 25.4 kHz (default), 6.3 kHz, 8.5 kHz, 12.7 kHz

Modulation profile: Hershey-kiss (default), Sine-wave, Triangle

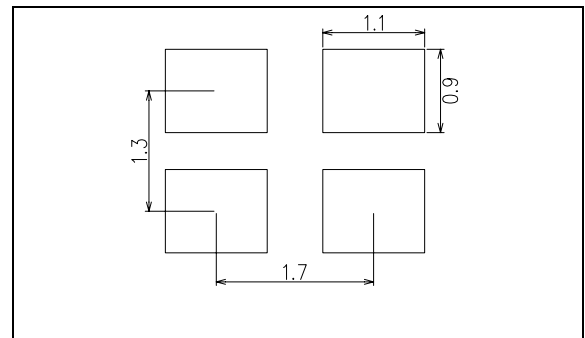
External dimensions

(Unit: mm)



Footprint (Recommended)

(Unit: mm)



Notes:

In order to achieve optimum jitter performance, the 0.1 μ F capacitor between V_{CC} and GND should be placed. It is also recommended that the capacitors are placed on the device side of the PCB, as close to the device as possible and connected together with short wiring pattern.

RF Transmitter Module
AEC-Q100 compliant
SR3225SAA

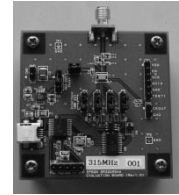


Product Number
SR3225SAA: X1G00479xxxxx00

SR3225SAA is a wireless transmitter module for UHF range. Crystal resonator, oscillator, PLL and Power Amp are integrated in 3.2 mm x 2.5 mm ceramic package. The wireless transmission function can be configured by connect to the external control devices. It is suitable for small wireless transmitter. The evaluation kit is available.

Application

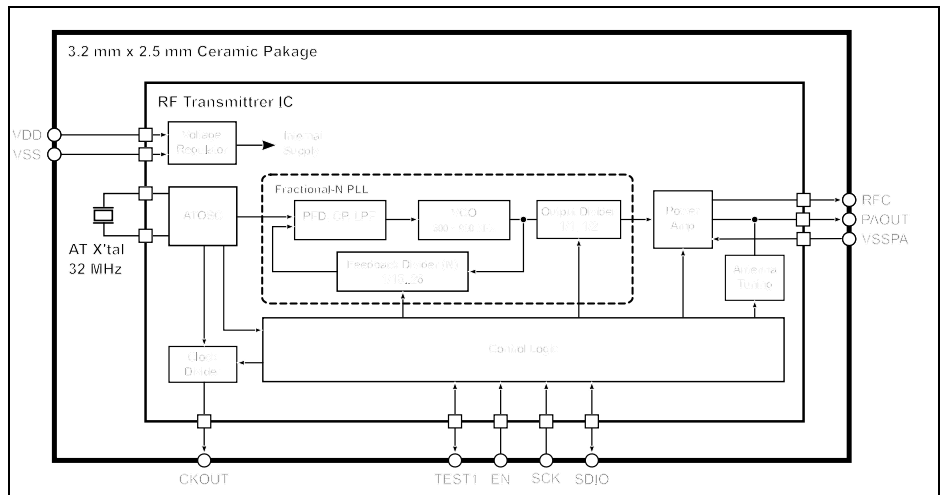
- Remote Keyless Entry, Passive Entry
- Short range radio data transmitter
- Garage door opener
- Transmitter for RFID tag



Overview

- Carrier frequency bands:
300 MHz ~ 465 MHz (0.25 kHz Step),
600 MHz ~ 930 MHz (0.49 kHz Step)
- $\Delta\Sigma$ fractional-N based PLL
- Programmable Power Amp output power:
-15 ~ 11 dBm, 128 steps
- Modulation types: ASK/OOK/FSK with Soft-ASK and/or Soft FSK shaping
- 3-wire/4-wire SPI interface
- SFR (Special Function Register)
- Embedded 32 MHz crystal resonator and oscillation circuit
- Programmable clock output via CKOUT
- Programmable voltage threshold of Under Voltage Detection: 4 steps (1.8 V ~ 2.4 V)
- Fail-Safe mechanism (PLL Loss of Lock, VCO auto-calibration error, Under Voltage Detection)

Block diagram

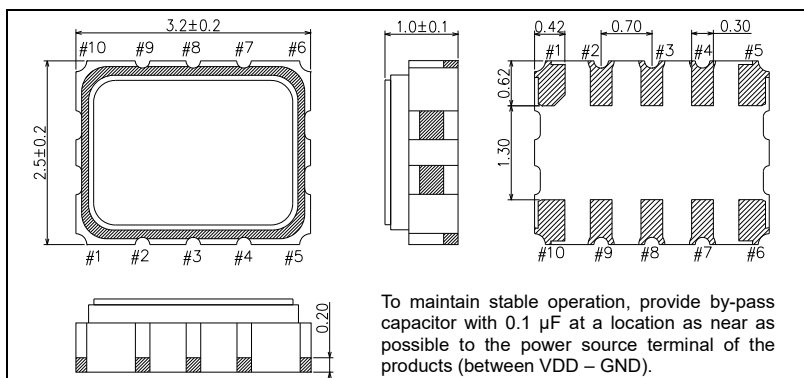


Specification (characteristics)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage	VDD	-	1.8	3.0	3.6	V
Operation temperature range	Ta	-	-40	-	+85	°C
Storage temperature range	Tstg	-	-40	-	+125	°C
Supply current Powerdown Mode	I _{DDPD}	VDD = 3.0 V, Ta = +25 °C	-	20	100	nA
Supply current Transmitter-Active Mode	I _{DDTMA}	F _{TX} = 315 MHz, P _{out} = 5 dBm,	-	10.0	11.0	mA
		F _{TX} = 315 MHz, P _{out} = 8 dBm,	-	12.7	13.7	
		F _{TX} = 315 MHz, P _{out} = 10 dBm,	-	15.0	16.0	
Carrier frequency bands	F _{TX}	-	300	-	465	MHz
		-	600	-	930	
ASK Bit rate	R _{ASK}	NRZ	-	-	100	kbps
FSK Bit rate	R _{FSK}	NRZ	-	-	50	kbps
Crystal frequency tolerance	F _{TOL}	Ta = +25 °C, without aging	-2	-	2	ppm
Crystal temperature variation	F _{TC}	-40 °C ~ +85 °C	-20	-	20	ppm
Nominal output power	P _{OUT}	Ta = +25 °C, VDD = 3.0 V, F _{TX} = 315 MHz, HPWR = 1, AM* = 0x3F	10	11	12	dBm
		Ta = +25 °C, VDD = 3.0 V, F _{TX} = 315 MHz, HPWR = 0, AM* = 0x01	-16	-15	-14	

External dimensions

(Unit: mm)



Pin descriptions

Pin No.	Pin Name	Function
1	TEST1	Test, Transmission data input or SPI interface data output
2	EN	Enable inp or SPI interface chip select
3	SCK	SPI interface clock input
4	SDIO	SPI data input / output or Transmission data input
5	CKOUT	Clock output
6	VSSPA	GND for Power Amp
7	PAOUT	Power Amp output
8	RFC	RF choke coil connect pin
9	VDD	Positive power supply
10	VSS	GND



TCXO / VC-TCXO / TCXO-Standby For Automotive 105 °C High temperature range



Product Number (Please contact us)
TG2016SKA : X1G005371xxxx16

TG2016SKA

- Output frequency : 13 MHz to 55 MHz
- Supply voltage : 1.8 V Typ. / 3.3 V Typ.
- Frequency / temperature characteristics : $\pm 0.5 \times 10^{-6}$ Max. (-40 °C to +105 °C)
- External dimensions: 2.0 × 1.6 × 0.7 mm Max.
- Applications : GNSS for Automotive, V2X (TCU, DSRC)*
- Features : Low noise, 105 °C High temp, Stand-by function (\overline{ST})
- AEC-Q100 compliant



TG2016SKA

(2.0 × 1.6 × 0.7 mm)

* GNSS: Global Navigation Satellite System V2X: Vehicle to Everything TCU: Telematics control unit DSRC: Dedicated Short Range Communication

Specifications (characteristics)

Item	Symbol	TCXO	VC-TCXO	TCXO-Standby	Conditions / Remarks
Output frequency range	f_o	13 MHz to 55 MHz			
		26 MHz, 49.58 MHz			Standard frequency
Supply voltage	V_{CC}	1.8 V \pm 0.1 V / 3.3 V \pm 5 %			Supply voltage range: 1.7 V to 3.63 V
Storage temperature range	T_{stg}	-55 °C to +125 °C			Storage as single product.
Operating temperature range	T_{use}	H: -40 °C to +105 °C			Standard
Frequency tolerance	f_{tol}	$\pm 2.0 \times 10^{-6}$ Max.			After 3 times reflow, +25 °C
Frequency/temperature Characteristics	f_o -Tc	C: $\pm 0.5 \times 10^{-6}$ Max.			Standard stability version
Frequency/load coefficient	f_o -Load	$\pm 0.2 \times 10^{-6}$ Max.			10 k Ω // 10 pF \pm 10 %
Frequency/voltage coefficient	f_o - V_{CC}	$\pm 0.2 \times 10^{-6}$ Max.			$V_{CC} \pm 5$ %
Frequency aging	f_{age}	$\pm 1.0 \times 10^{-6}$ Max.			+25 °C, First year, 13 MHz $\leq f_o \leq$ 20 MHz, 26 MHz $\leq f_o \leq$ 40 MHz
		$\pm 1.5 \times 10^{-6}$ Max.			+25 °C, First year, 20 MHz < f_o < 26 MHz 40 MHz < $f_o \leq$ 55 MHz
Current consumption	I_{CC}	2.0 mA Max. 2.5 mA Max.			13 MHz $\leq f_o \leq$ 40 MHz 40 MHz < $f_o \leq$ 55 MHz
Input resistance	Z_{in}	-	500 k Ω Min.	-	V_C - GND (DC)
Frequency control range	f_{cont}	-	$\pm 5.0 \times 10^{-6}$ Min.	-	B: $V_C = 0.9 V \pm 0.6 V$ ($V_{CC} = 1.8 V$) or E: $V_C = 1.65 V \pm 1.0 V$ ($V_{CC} = 3.3 V$)
Frequency change polarity	f_{cp}	-	Positive polarity	-	
Stand-by current	I_{std}	-		10 μ A Max.	$\overline{ST} =$ GND
Input voltage	V_{IH}	-		80 % V_{CC} Min.	\overline{ST} terminal
	V_{IL}	-		20 % V_{CC} Max.	
Symmetry	SYM	40 % to 60 %			GND level (DC cut)
Output voltage	V_{pp}	0.8 V Min.			Peak to Peak
Start-up time	t_{str}	2.0 ms Max.			$t = 0$ at 90 % V_{CC}
Output load	Load_R	10 k Ω			DC cut capacitor = 0.01 μ F
	Load_C	10 pF			
G-sensitivity	G_s	1.5 $\times 10^{-9}$ / G Max.			30 Hz to 3 kHz, sinewave, 3axes

* Note : Please contact us for requirements not listed in this specification.

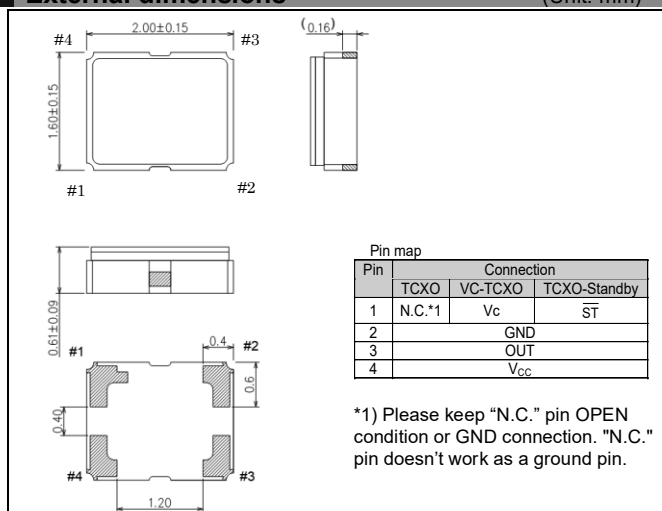
Product Name **TG2016 SKA 26.000000MHz** E C H N N M
(Standard form) ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨

- ① Model (TG2016) ② Output (S: Clipped sine wave)
③ Frequency ④ Supply voltage (Refer to symbol table)
⑤ Frequency / temperature characteristics (C: $\pm 0.5 \times 10^{-6}$ Max.)
⑥ Operating temperature (H: -40 °C to +105 °C) ⑦ ST function (N: Non, S: Standby)
⑧ V_C function (Refer to symbol table) ⑨ Internal identification code

④ Supply voltage [V_{CC}] ⑧ V_C function [V_C] (Symbol table)			
Voltage [V]	TCXO	VC-TCXO	
④ V_{CC} (Typ.)	E: 1.8 C: 3.3	E: 1.8	C: 3.3
⑧ V_C (Typ.)	N: Non	B: 0.9	E: 1.65

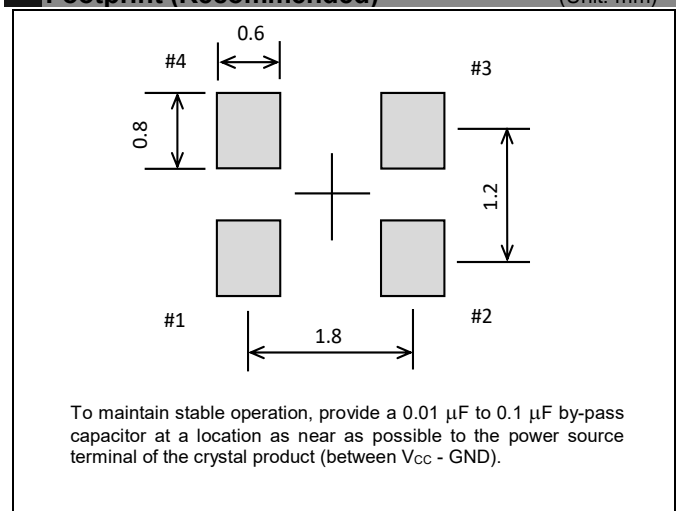
External dimensions

(Unit: mm)



Footprint (Recommended)

(Unit: mm)



TCXO / VC-TCXO / TCXO-Standby

For Automotive

85 °C High temperature range



Product Number (Please contact us)
TG2016SLA : X1G005741xxxx16

TG2016SLA



TG2016SLA

(2.0 × 1.6 × 0.7 mm)

- Output frequency : 13 MHz to 55 MHz
- Supply voltage : 1.8 V Typ. / 3.3 V Typ.
- Frequency / temperature characteristics : $\pm 0.5 \times 10^{-6}$ Max. (-40 °C to +85 °C)
- External dimensions: 2.0 × 1.6 × 0.7 mm Max.
- Applications : GNSS for Automotive, V2X (TCU, DSRC)*
- Features : Low noise, Stand-by function (\overline{ST})
- AEC-Q100 compliant

* GNSS: Global Navigation Satellite System V2X: Vehicle to Everything TCU: Telematics control unit DSRC: Dedicated Short Range Communication

Specifications (characteristics)

Item	Symbol	TCXO	VC-TCXO	TCXO-Standby	Conditions / Remarks
Output frequency range	f_o	13 MHz to 55 MHz			Standard frequency
		26 MHz, 48 MHz, 49.58 MHz			
Supply voltage	V_{CC}	1.8 V \pm 0.1 V / 3.3 V \pm 5 %			Supply voltage range: 1.7 V to 3.63 V
Storage temperature range	T_{stg}	-55 °C to +125 °C			Storage as single product.
Operating temperature range	T_{use}	G: -40 °C to +85 °C			Standard
Frequency tolerance	f_{tol}	$\pm 2.0 \times 10^{-6}$ Max.			After 3 times reflow, +25 °C
Frequency/temperature characteristics	f_o -Tc	C: $\pm 0.5 \times 10^{-6}$ Max.			Standard stability version
Frequency/load coefficient	f_o -Load	$\pm 0.2 \times 10^{-6}$ Max.			10 k Ω // 10 pF \pm 10 %
Frequency/voltage coefficient	f_o - V_{CC}	$\pm 0.2 \times 10^{-6}$ Max.			$V_{CC} \pm 5 \%$
Frequency aging	f_{age}	$\pm 1.0 \times 10^{-6}$ Max.			+25 °C, First year, 13 MHz $\leq f_o \leq$ 20 MHz, 26 MHz $\leq f_o \leq$ 40 MHz
		$\pm 1.5 \times 10^{-6}$ Max.			+25 °C, First year, 20 MHz $< f_o <$ 26 MHz, 40 MHz $< f_o \leq$ 55 MHz
Current consumption	I_{CC}	2.0 mA Max. 2.5 mA Max.			13 MHz $\leq f_o \leq$ 40 MHz 40 MHz $< f_o \leq$ 55 MHz
Input resistance	Z_{in}	-	500 k Ω Min.	-	V_c - GND (DC)
Frequency control range	f_{cont}	-	$\pm 5.0 \times 10^{-6}$ Min.	-	B: $V_c = 0.9 V \pm 0.6 V$ ($V_{CC} = 1.8 V$) or E: $V_c = 1.65 V \pm 1.0 V$ ($V_{CC} = 3.3 V$)
Frequency change polarity	f_{cp}	-	Positive polarity	-	
Stand-by current	I_{std}	-	-	10 μ A Max.	$\overline{ST} =$ GND
Input voltage	V_{IH}	-	-	80 % V_{CC} Min.	\overline{ST} terminal
	V_{IL}	-	-	20 % V_{CC} Max.	
Symmetry	SYM	40 % to 60 %			GND level (DC cut)
Output voltage	V_{pp}	0.8 V Min.			Peak to Peak
Start-up time	t_{str}	2.0 ms Max.			$t = 0$ at 90 % V_{CC}
Output load	Load_R	10 k Ω			DC cut capacitor = 0.01 μ F
	Load_C	10 pF			
G-sensitivity	G_s	1.5×10^{-9} / G Max.			30 Hz to 3 kHz, sinewave, 3axes

* Note : Please contact us for requirements not listed in this specification.

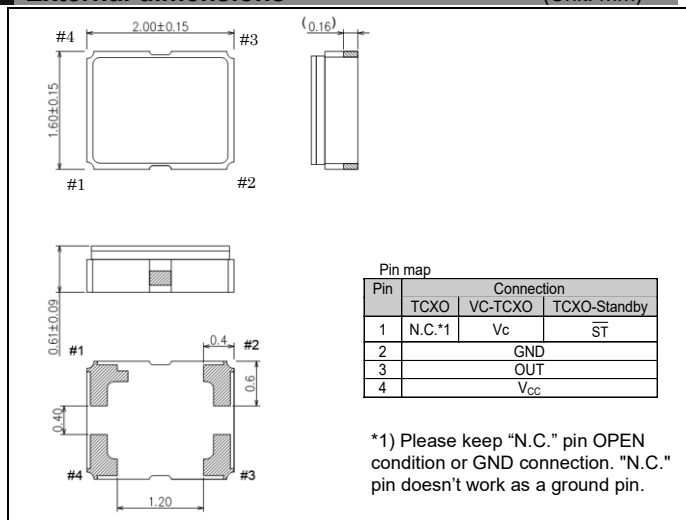
Product Name TG2016 SLA 26.000000MHz E C G N N M
(Standard form) ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨

- ① Model ② Output (S: Clipped sine wave)
- ③ Frequency ④ Supply voltage (Refer to symbol table)
- ⑤ Frequency / temperature characteristics (C: $\pm 0.5 \times 10^{-6}$ Max.)
- ⑥ Operating temperature (G: -40 °C to +85 °C) ⑦ ST function (N: Non, S: Standby)
- ⑧ Vc function (Refer to symbol table) ⑨ Internal identification code

Voltage [V]	④ Vc function [Vc] (Symbol table)		
	TCXO	VC-TCXO	
④ V_{CC} (Typ.)	E: 1.8 C: 3.3	E: 1.8	C: 3.3
⑧ Vc (Typ.)	N: Non	B: 0.9	E: 1.65

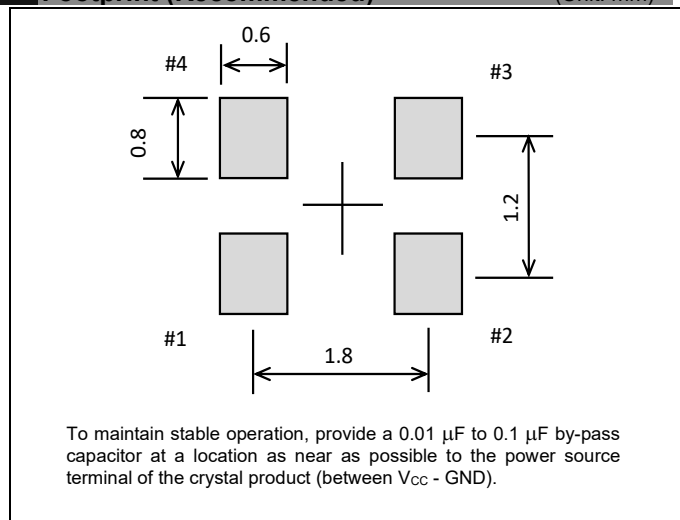
External dimensions

(Unit: mm)



Footprint (Recommended)

(Unit: mm)



GYRO SENSOR (Digital Output) FOR AUTOMOTIVE

XV4001BC / BD XV4001KC / KD

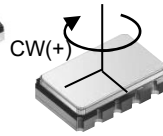


Product number
 XV4001BC: X2A000201xxxx00
 XV4001BD: X2A000161xxxx00
 XV4001KC: X2A000211xxxx00
 XV4001KD: X2A000171xxxx00

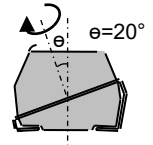
- SPI or I²C serial interface
 - Angular rate output (16bit), Temperature output (11bit)
 - Inclined angle: 20 degrees (XV4001KC/KD)
 - XV4001BC ... I²C / 5.0 x 3.2 x 1.3 mm
 - XV4001BD ... SPI / 5.0 x 3.2 x 1.3 mm
 - XV4001KC ... I²C / 6.0 x 4.8 x 3.3 mm
 - XV4001KD ... SPI / 6.0 x 4.8 x 3.3 mm
 - AEC-Q200 compliant
- Recommended Application**
- Car navigation system, Telematics



XV4001BC/BD



XV4001KC/KD



Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
Supply Voltage	V _{DD}	3.3 V ±0.3 V	
Temperature range	Storage Temperature	T _{STG}	-50 °C to +105 °C
	Operating Temperature	T _{OPR}	-40 °C to +85 °C
Scale factor	S _o	370 LSB/(°/s) ±1.5 %	T _a =+25 °C
Bias	V _o	±2 °/s (0 LSB Typ.)	T _a =+25 °C
Rate range	I	±70 °/s	
Non linearity	NL	±0.5 % FS	T _a =+25 °C
Frequency response	BW	10 Hz Typ.	LPF Gain -3dB bandwidth
Cross axes	OS	±5 %	T _a =+25 °C
Current consumption	I _{op}	3.5 mA Typ.	not rotation and not communicating
Noise	r _N	0.05 °/s RMS Typ.	

Product Name
(Standard form)

XV4001BC * * *

- ① Model ② Detection axis (1:Z-axis) ③ Package type(B: Ceramic 5032size, K: Lead frame K-Type)
 ④ Output (C: I²C, D: SPI) ⑤ Frequency ⑥, ⑦ Internal identification code (⑥⑦ are not necessary to specify)

External Dimensions

(Unit:mm)

Pin	Connection	
	XV4001BC/BD	XV4001KC/KD
1	N.C.	GND
2	SCLK SCL	MOSI
3	SS	MISO SDA
4	V _{DD}	N.C.
5	N.C.	N.C.
6	N.C.	SCLK SCL
7	GND	SS
8	MOSI	V _{DD}
9	MISO SDA	-
10	N.C.	-

Do not connect "N.C." pins externally.

Footprint (Recommended)

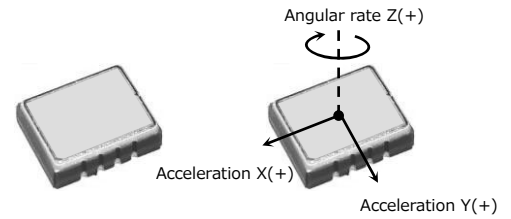
(Unit:mm)

XV4001BC/BD	XV4001KC/KD
<p>Dimensions: 5.10, 0.90, 0.70, 0.30, 1.30, 1.20, 3.80</p>	<p>Dimensions: 4.61, 0.80, 0.47, 2.20, 2.00, 6.40</p>

COMBINED SENSOR
FOR AUTOMOTIVEProduct number
XC1011SD: X2E0000210002xx

XC1011SD

- Combined sensor integrating gyro sensor and dual-axis G sensor in one package
- Ultra-small and low power consumption using the original Double-T structure quartz crystal element
- High reliability by installing the diagnosis function
- Excellent performance of shock-resistance and vibration-resistance
- Digital output: angular rate (Yaw) / acceleration (X, Y-axis) 16bit, Temperature 11bit with SPI
- Conforms to AEC-Q100, Support evaluation of hardware elements in ISO26262 (ASIL D)



Recommended Application

- Electric Stability Control System

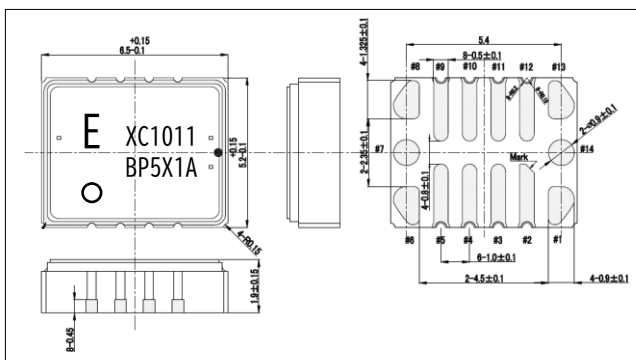
Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks	
Supply Voltage	V _{DD}	3.3 V ±0.165 V		
Storage Temperature	T _{STG}	-40 °C ~ +105 °C		
Operating Temperature	T _{OPR}	-40 °C ~ +105 °C		
Angular rate sensor	Sensitivity	S _y	175 ±5 LSB/(°/s)	T _a =+25°C
	Bias	V ₀	±525 LSB (±3 °/s)	T _a =+25°C, S ₀ =175 LSB/(°/s)
	Rate range	DR _y	±160 °/s	
	Non-linearity	NL _y	±1 % FS	FS= ±160 °/s
	Frequency characteristic	F _{cy}	52.6 ±2.6 Hz	-3dB bandwidth
	Cross axis sensitivity	CS _y	±5 %	
Acceleration sensor	Sensitivity	S _a	1092 ±22 LSB/G	T _a =+25°C, S _a =1092LSB/G, without PCB mount
	Zero G offset	0G	±57 mG	
	Acceleration range	DR _a	±30 G	Lo-range, Mid-range
	Non-linearity	NL _a	±43 LSB	±1G
		Frequency characteristic	F _{c1}	52.6 ±2.6 Hz
	F _{c2}	200 ±10 Hz	Mid-range, -3dB bandwidth	
Cross axis sensitivity	CS _a	±3 %		
Current consumption	I _{op}	20 mA Max.	Stationary and No-communication	
Start-up time	T _{ACT}	300 ms Max.	from V _{DD} rise-up 90%	

Product Name XC1011SD 50.300 kHz
 (Standard form) ① ② ③
 ① Model ② Package type ③ Frequency (not necessary to specify)

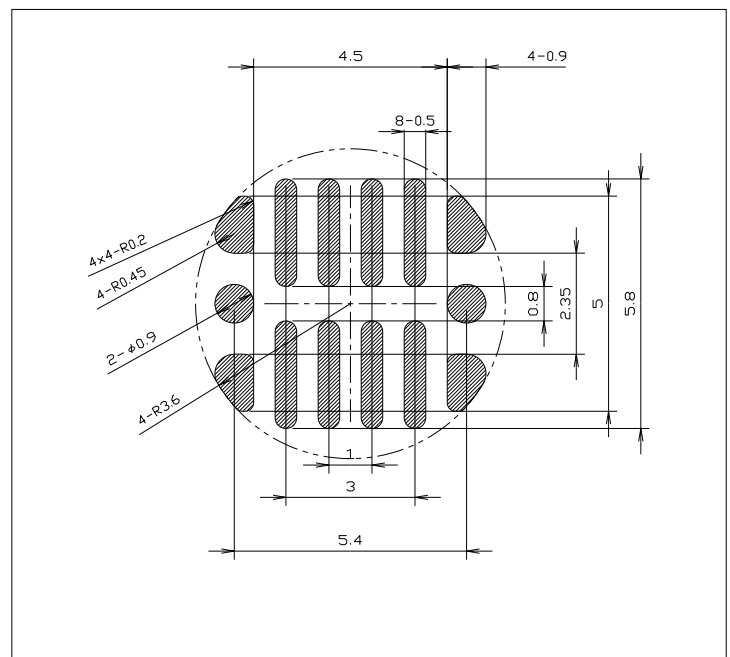
External Dimensions

(Unit: mm)



Footprint (Recommended)

(Unit: mm)



Pin Function

Pin	Connection	Pin	Connection
1	N.C.	8	N.C.
2	SS	9	V _{DDL}
3	MISO	10	V _{DD}
4	SCLK	11	VREFAD
5	MOSI	12	GND
6	N.C.	13	N.C.
7	N.C.	14	N.C.



Do not connect "N.C." pins externally devices.

WORKING FOR EU RoHS / Pb FREE

■ EU RoHS / Pb Free Products

The standard crystal products are compliant with EU RoHS directives.

The appropriate symbol is displayed on individual product pages as applicable.

	● Pb free.
	● Complies with EU RoHS directive. *About the products without the Pb-free mark. Contains Pb in products exempted by EU RoHS directive. (Contains Pb in sealing glass, high melting temperature type solder or other.)

■ DISTINCTIONS

Distinctions between the products with Pb plating terminals and the products that comply with EU RoHS directives.

● Plastic packaged type products.

Marking (year part lot No.) indicates as follows.

	Last digit of year	1	2	3	4	5	6	7	8	9	0
Products Complied with EU RoHS directive (Terminal Plating ; Sn-Bi or Sn-Ag)	Alphabet	A	B	C	D	E	F	G	H	J	K
Products Complied with EU RoHS directive (Terminal Plating ; Sn)	Alphabet	M	N	R	S	T	U	V	W	X	Z

■ Terminal materials of crystal products that comply with EU RoHS directive

We will deliver products that comply with EU RoHS directives for new orders.

Sn plating of terminals is our standard for the products that offer two types of plating.

Model	Terminal Material	Terminal Plating	Complies With EU RoHS	Pb Free	Remarks (Contains Pb in products exempted by RoHS directive.)	Reference weight (Typ.)
kHz Range Crystal unit						
FC1610AN	Mo	Au	○	○		3 mg
FC1610BN	Mo	Au	○	○		2.6 mg
FC2012AN	Mo	Au	○	○		4.3 mg
FC2012SN	Mo	Au	○	○		4.3 mg
FC-12M	Mo	Au	○	○		5 mg
FC3215AN	W	Au	○	○		13mg
FC-135 / FC-135R	W	Au	○		7(c) - I	11 mg
MC-146	42 Alloy	Sn or Sn-Bi	○		7(a)	29 mg
MC-306	42 Alloy	Sn or Sn-Bi	○		7(a)	126 mg
FC2012AA	Mo	Au	○	○		4.2 mg
FC-13A	Mo	Au	○	○		14 mg
MC-30A	42 Alloy	Sn or Sn-Bi	○		7(a)	126 mg
MHz Range Crystal unit						
FA1008AN	Mo	Au	○	○		0.8 mg
FA1210AN	Mo	Au	○	○		1 mg
FA-118T	Mo	Au	○	○		3 mg
FA-128	W	Au	○	○		7 mg
FA-20H	W	Au	○	○		11 mg
FA-238V	W	Au	○	○		16 mg
FA-238	W	Au	○	○		16 mg
TSX-3225	W	Au	○	○		17 mg
FA2016AA	W	Au	○	○		7 mg
FA-238A	W	Au	○	○		16 mg
FA1612AS	W	Au	○	○		5 mg
FA2016AS	W	Au	○	○		8 mg
FA2016ASA	W	Au	○	○		8 mg

Model	Terminal Material	Terminal Plating	Complies With EU RoHS	Pb Free	Remarks (Contains Pb in products exempted by RoHS directive.)	Reference weight (Typ.)
SPXO						
SG2016CAN / CAA	W	Au	O	O		9.9 mg
SG2016EGN / VGN	Mo	Au	O	O		7.6 mg
SG2016EHN / VHN	Mo	Au	O	O		7.6 mg
SG2016HGN / HHN	Mo	Au	O	O		7.6 mg
SG-210 STF	W	Au	O	O		12 mg
SG2520EGN / VGN	W	Au	O	O		12 mg
SG2520EHN / VHN	W	Au	O	O		12 mg
SG2520HGN / HHN	W	Au	O	O		12 mg
SG-3031CM	W	Au	O	O		13 mg
SG-3031CMA	W	Au	O	O		13 mg
SG3225CAN	W	Au	O	O		25 mg
SG3225EAN / VAN	W	Au	O	O		25 mg
SG3225EEN	W	Au	O	O		26 mg
SG3225HBN	W	Au	O	O		26 mg
SG3225VEN	W	Au	O	O		26 mg
SG5032CAN / CCN	W	Au	O	O		52 mg
SG5032EAN / VAN	W	Au	O	O		52 mg
SG5032EEN / VEN	W	Au	O	O		165 mg
SG7050CAN / CCN	W	Au	O	O		147 mg
SG7050EAN / VAN	W	Au	O	O		149 mg
SG7050EEN	W	Au	O	O		165 mg
SG7050VEN	W	Au	O	O		165 mg
Selectable SPXO						
SG-8503CA	W	Au	O	O		167mg
SG-8504CA	W	Au	O	O		168 mg
SG-8506CA	W	Au	O	O		167 mg
SPSO (Low-jitter SAW)						
EG-2102CB	W	Au	O	O		71 mg
EG-2121CB	W	Au	O	O		71 mg
XG-2102CA	W	Au	O	O		133 mg
XG-2103CA	W	Au	O	O		133 mg
XG-2121CA	W	Au	O	O		133 mg
XG5032HAN	W	Au	O	O		70 mg
Programmable SPXO						
SG-8018CA	W	Au	O	O		143 mg
SG-8018CB	W	Au	O	O		51 mg
SG-8018CE	W	Au	O	O		25 mg
SG-8018CG	W	Au	O	O		13 mg
SG-8101CA	W	Au	O	O		143 mg
SG-8101CB	W	Au	O	O		51 mg
SG-8101CE	W	Au	O	O		25 mg
SG-8101CG	W	Au	O	O		13 mg
SG-8200CG	W	Au	O	O		12 mg
SG-8201CG	W	Au	O	O		12 mg
SG-8101CGA	W	Au	O	O		13 mg
SG-8201CJA	Mo	Au	O	O		7.3 mg



Model	Terminal Material	Terminal Plating	Complies With EU RoHS	Pb Free	Remarks (Contains Pb in products exempted by RoHS directive.)	Reference weight (Typ.)
Spread Spectrum Programmable SPXO						
SG-9101CA	W	Au	O	O		143 mg
SG-9101CB	W	Au	O	O		51 mg
SG-9101CE	W	Au	O	O		25 mg
SG-9101CG	W	Au	O	O		13 mg
SG-9101CGA	W	Au	O	O		14 mg
VCXO / VCXO						
VG3225EFN	W	Au	O	O		26 mg
VG3225VFN	W	Au	O	O		26 mg
VG5032EDN	W	Au	O	O		64 mg
VG5032EFN	W	Au	O	O		66 mg
VG5032VDN	W	Au	O	O		64 mg
VG5032VFN	W	Au	O	O		66 mg
VG7050CDN	W	Au	O	O		168 mg
VG7050EAN	W	Au	O	O		167 mg
VG7050EBN	W	Au	O	O		166 mg
VG7050ECN	W	Au	O	O		168 mg
VG7050EFN	W	Au	O	O		172mg
VG7050VFN	W	Au	O	O		172mg
TCXO						
TG1612SLN	Mo	Au	O	O		4 mg
TG2016SKA	W	Au	O	O		8.2 mg
TG2016SLA	W	Au	O	O		8.2 mg
TG2016SLN	W	Au	O	O		8.1 mg
TG2016SMN	W	Au	O	O		8.7 mg
TG2520CEN	W	Au	O	O		16 mg
TG2520SMN	W	Au	O	O		16 mg
TG3225CEN	W	Au	O	O		26 mg
TG-3541CE	W	Au	O	O		24 mg
TG-3541CEA	W	Au	O	O		24 mg
TG-5006CE	W	Au	O	O		28 mg
TG-5006CG	W	Au	O	O		16 mg
TG-5006CJ	Mo	Au	O	O		9.1 mg
TG5032CFN / SFN	W	Au	O	O		71 mg
TG5032CGN / SGN	W	Au	O	O		72 mg
TG5032CKN / SKN	W	Au	O	O		72 mg
TG5032CMN / SMN	W	Au	O	O		71 mg
TG-5510CA	W	Au	O	O		165 mg
TG-5510CB	W	Au	O	O		72 mg
TG-5511CA	W	Au	O	O		167 mg
TG-5511CB	W	Au	O	O		71 mg
TG7050xKN	W	Au	O	O		165 mg
TG7050xMN	W	Au	O	O		167 mg
Multi Output Oscillator						
MG7050EAN / VAN / HAN	W	Au	O	O		163 mg
Module						
SR3225SAA	W	Au	O	O		24 mg

Model	Terminal Material	Terminal Plating	Complies With EU RoHS	Pb Free	Remarks (Contains Pb in products exempted by RoHS directive.)	Reference weight (Typ.)
Real Time Clock Module						
RA4000CE	W	Au	O	O		24 mg
RA-4565SA	42 Alloy	Sn	O		7(a)	309 mg
RA4803SA	42 Alloy	Sn	O		7(a)	314 mg
RA8000CE	W	Au	O	O		24 mg
RA8803SA	42Alloy	Sn	O		7(a)	314 mg
RA8804CE	W	Au	O	O		24 mg
RA8900CE	W	Au	O	O		24 mg
RTC-8564JE	42 Alloy	Sn-Ag	O		7(a)	119 mg
RTC-8564NB	Cu Alloy	Sn or Sn-Ag	O		7(a)	83 mg
RX-4035LC	42 Alloy	Sn-Ag	O		7(c) - I	25mg
RX-4035SA	42 Alloy	Sn	O		7(a)	314 mg
RX-4045NB	Cu Alloy	Sn or Sn-Ag	O		7(a)	84 mg
RX-4045SA	42 Alloy	Sn	O		7(a)	290 mg
RX4111CE	W	Au	O	O		24 mg
RX-4571LC	42 Alloy	Sn-Ag	O		7(c) - I	25 mg
RX-4571NB	Cu Alloy	Sn or Sn-Ag	O		7(a)	83 mg
RX-4571SA	42 Alloy	Sn	O		7(a)	309 mg
RX-4803LC	42 Alloy	Sn-Ag	O		7(c) - I	25 mg
RX-4803SA	42 Alloy	Sn	O		7(a)	314 mg
RX4901CE	W	Au	O	O		24 mg
RX6110SA	42 Alloy	Sn	O		7(a)	313 mg
RX8010SJ	Cu Alloy	Sn	O		7(a)	150 mg
RX-8025NB	Cu Alloy	Sn or Sn-Ag	O		7(a)	84 mg
RX-8025SA	42 Alloy	Sn	O		7(a)	292 mg
RX-8035LC	42 Alloy	Sn-Ag	O		7(c) - I	25 mg
RX-8035SA	42 Alloy	Sn	O		7(a)	314 mg
RX8111CE	W	Au	O	O		24 mg
RX8130CE	W	Au	O	O		24 mg
RX-8564LC	42 Alloy	Sn-Ag	O		7(c) - I	25 mg
RX-8571LC	42 Alloy	Sn-Ag	O		7(c) - I	25 mg
RX-8571SA	42 Alloy	Sn	O		7(a)	311 mg
RX-8731LC	42 Alloy	Sn-Ag	O		7(c) - I	25 mg
RX-8803LC	42 Alloy	Sn-Ag	O		7(c) - I	25 mg
RX-8803SA	42Alloy	Sn	O		7(a)	314 mg
RX8804CE	W	Au	O	O		24 mg
RX8900CE	W	Au	O	O		24 mg
RX8900SA	42Alloy	Sn	O		7(a)	313 mg
RX8901CE	W	Au	O	O		24 mg
Sensor						
XV-3510CB	W	Au	O	O		65 mg
XV4001Bx	W	Au	O	O		68 mg
XV4001Kx	Cu Alloy	Ni-Pd-Au	O	O		194 mg
XV7001BB	W	Au	O	O		65 mg
XV7011BB	W	Au	O	O		65 mg
XV7021BB	W	Au	O	O		67 mg
XV7081BB	W	Au	O	O		67 mg
XC1011SD	W	Au	O	O		173mg

*For IMU, Vibration sensor, Accelerometer, please contact an Epson sales representative in your region.

HANDLING PRECAUTIONS

Please use the each product under the conditions provided in respective specifications and catalogues.

The crystal products are designed and manufactured to satisfy its specification, and quality and reliability of the products are ensured through our stringent reliability tests before shipments. However, it is essential to store, mount, and use the products under proper conditions in order to keep the quality and reliability. Please pay careful attention to following precautions and use the products under the optimal conditions. We shall not be responsible for any deteriorated performance of the products caused by any application or usage of the products adopted at customer's own discretion.

Common points for all products

1. Shock resistance

Crystal products may be damaged under some conditions, such as dropping from desks or receiving shocks during mounting. Please do not use the products if products have received any excessive shocks.

2. Radiation

Exposure to radiation can cause deterioration in performance, so avoid irradiation.

3. Chemicals / pH

Do not use or store the product in a pH range that may cause corrosion or dissolution of the materials or packaging.

4. Adhesive

Do not use an adhesive that may cause corrosion of the packing materials, terminals, components, glass materials, and vapor deposited materials used in the products. (For example, a chlorine-based adhesive may corrode the metal parts "lid" of a crystal unit to diminish the hermetic qualities, lowering the performance.)

5. Halogen Compound

Do not use products in halogen gas. Even a slight amount of halogen gas, such as that found in chlorine gas in the air or in metal parts used in the package, may corrode. Also, do not use any resin that emits halogen gas.

6. Static electricity

Excessive levels of static electricity may damage the product, please treat it in anti-static condition. Choose conductive materials for containers and packing materials. Use a soldering gun and a measurement circuit free from high-voltage leakage and provide grounding conduction when working with them.

7. When designing

7.1 The affect of mechanical vibration

While there is any given shock or mechanical vibration periodically to crystal products, such as, a piezo sounder, a piezo buzzer, and speaker, to crystal products, output frequency and amplitude can be changed. Especially the quality of telecommunication equipment could be affected by this phenomenon. Although crystal products are designed to minimize the effect of mechanical vibration, we recommend to check them in advance and to follow the PCB design guidelines as below.

7.2 PCB design guidelines

- (1) Ideally, the mechanical buzzer source should be mounted on a separate PCB from the crystal device. It is advisable to use cushion or cutting PCB, if you mount on same PCB. Traveling mechanical vibration differs when applied to the PCB only vs. inside the body. It is advisable to confirm characteristics in the body.
- (2) Refer to the recommend footprint when designing.
- (3) In case of using solder flux, please use it on the basis of the flux of the JIS standard (JIS C 60068-2-20/IEC 60,068-2-20).
- (4) Please use the solder on the basis of the JIS standard (JIS Z 3282, Pb content 1000ppm, 0.1wt% or less) lead -free solder.

8. Notes on storage

- (1) Storing the crystal products under higher or lower temperature or high humidity for a long period may affect frequency stability or solderability. Please store the crystal products at the normal temperature and humidity, avoid storing them for a long period and mount them as soon as possible after unpacking.

Normal temperature and humidity:

Temp, +15 °C to +35 °C, humidity 25 % RH to 85 % RH

- (2) Please carefully handle the inner and outer boxes and reel. External pressure may cause deformation of reel and tape.

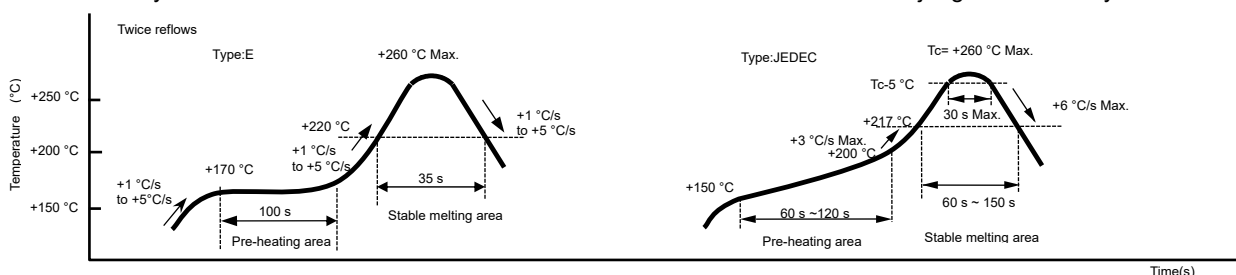
9. Mounting precautions

9.1 Soldering heat resistance

The crystal products except SMD products use solder having a +180°C to +200°C melting point. Heating up the package more than +150°C may deteriorate the characteristics or damage the products. If the crystal products need to be soldered at temperature of more than +150°C, SMD products are recommended. Using higher temperatures over the following reflow conditions to crystal products, even SMD products, may cause the characteristics to deteriorate. The reflow conditions within following profile are recommended. Always check the soldering temperature and time before mounting these products. Also, please check them again when the mounting conditions are changed. *2 for inquiries about heat-resistance if crystal products need to be soldered over the following profile.

- (1) SMD products Reflow profile (example)

The availability of the heat resistance for reflow conditions of JEDEC-STD-020D.01 is judged individually. Please inquire it.



Please make temperature rate as gentle a curve as possible.



Model	Type:E	Type:JEDEC	Remarks
kHz Range Crystal unit			
FC1610AN		✓	
FC1610BN		✓	
FC2012AN		✓	
FC2012SN		✓	
FC-12M		✓	
FC3215AN		✓	
FC-135 / FC-135R	✓	*2	
MC-146	✓	*2	
MC-306	✓	*1	
FC2012AA		✓	
FC-13A	✓	*1	
MC-30A	✓	*1	
MHz Range Crystal unit			
FA1008AN		✓	
FA1210AN		✓	
FA-118T		✓	
FA-128		✓	
FA-20H		✓	
FA-238V	✓	*1	
FA-238	✓	*1	
TSX-3225		✓	
FA2016AA		✓	
FA-238A	*1	✓	
FA1612AS		✓	
FA2016AS		✓	
FA2016ASA		✓	

✓: Standard Spec.

*1: Support is *1.

*2: Customization is possible, but product specifications must be changed. Also, depending on the application and accuracy, it may not be possible.

Model	Type:E	Type:JEDEC	Remarks
SPXO			
SG2016CAN / CAA		✓	
SG2016EGN / VGN		✓	
SG2016EHN / VHN		✓	
SG2016HHN / HGN		✓	
SG-210 STF		✓	
SG2520EGN / VGN		✓	
SG2520EHN / VHN		✓	
SG-3031CM		✓	
SG-3031CMA		✓	
SG3225CAN		✓	
SG3225EAN / VAN / EEN / VEN		✓	
SG3225HBN		✓	
SG5032CAN / CCN		✓	
SG5032EAN / VAN / EEN / VEN		✓	
SG7050CAN / CCN		✓	
SG7050EAN / VAN / EEN / VEN		✓	
Selectable SPXO			
SG-8503CA		✓	
SG-8504CA		✓	
SG-8506CA		✓	
SPSO (Low-jitter SAW)			
EG-2102CB		✓	
EG-2121CB		✓	
XG-2102CA		✓	
XG-2103CA		✓	
XG-2121CA		✓	
XG5032HAN		✓	
Programmable SPXO			
SG-8018CA / CB / CE / CG		✓	
SG-8101CA / CB / CE / CG		✓	
SG-8101CGA		✓	
SG-8200CJ / CG		✓	
SG-8201CJ / CG		✓	
SG-8201CJA		✓	
Spread Spectrum Programmable SPXO			
SG-9101CA / CB / CE / CG		✓	
SG-9101CGA		✓	

✓: Standard Spec.

*1: Support is *1.

*2: Customization is possible, but product specifications must be changed. Also, depending on the application and accuracy, it may not be possible.

Model	Type:E	Type:JEDEC	Remarks
VCXO / VCXO			
VG3225 / 5032 / 7050EFN		✓	
VG3225 / 5032 / 7050VFN		✓	
VG5032EDN / VDN		✓	
VG7050CDN		✓	
VG7050EAN / EBN / ECN		✓	
TCXO			
TG1612SLN		✓	
TG2016SKA		✓	
TG2016SLA		✓	
TG2016SLN		✓	
TG2016SMN		✓	
TG2520CEN		✓	
TG2520SMN		✓	
TG3225CEN		✓	
TG-3541CE		✓	
TG-3541CEA		✓	
TG-5006CE / CG / CJ		✓	
TG5032CFN / SFN		✓	
TG5032CGN / SGN		✓	
TG5032CKN / SKN		✓	
TG5032CMN / SMN		✓	
TG-5510CA / TG-5511CA		✓	
TG-5510CB / TG-5511CB		✓	
TG7050CKN / SKN		✓	
TG7050CMN / SMN		✓	
Multi output Oscillator			
MG7050 series		✓	
Module			
SR3225SAA		✓	
Sensor			
XV-3510CB		✓	
XV4001Bx		✓	
XV4001Kx		✓	
XV7001BB		✓	
XV7011BB		✓	
XV7021BB		✓	
XV7081BB		✓	
XC1011SD		✓	

✓: Standard Spec.

*1: Support is *1.

*2: Customization is possible, but product specifications must be changed. Also, depending on the application and accuracy, it may not be possible.

Model	Type:E	Type:JEDEC	Remarks
Real Time Clock Module			
RA4000CE		√	
RA-4565SA		√	T _c =+250 °C
RA4803SA		√	T _c =+250 °C
RA8000CE		√	
RA8803SA		√	T _c =+250 °C
RA8804CE		√	
RA8900CE		√	
RTC-8564JE	√	*2	
RTC-8564NB	√	*2	
RX-4035LC		√	
RX-4035SA		√	
RX-4045NB	√	*2	
RX-4045SA	√	*2	T _c =+250 °C
RX4111CE		√	
RX-4571LC		√	
RX-4571NB		√	
RX-4571SA		√	T _c =+250 °C
RX-4803LC		√	
RX-4803SA		√	T _c =+250 °C
RX4901CE		√	
RX6110SA		√	T _c =+250 °C
RX8010SJ		√	T _c =+250 °C
RX-8025NB	√	*2	
RX-8025SA	√	*2	T _c =+250 °C
RX-8035LC		√	
RX-8035SA		√	
RX8111CE		√	
RX8130CE		√	
RX-8564LC	√	*2	
RX-8571LC		√	
RX-8571SA		√	T _c =+250 °C
RX-8731LC		√	
RX-8803LC		√	
RX-8803SA		√	T _c =+250 °C
RX8804CE		√	
RX8900CE		√	
RX8900SA		√	T _c =+250 °C
RX8901CE		√	

√: Standard Spec.

*1: Support is *1.

*2: Customization is possible, but product specifications must be changed. Also, depending on the application and accuracy, it may not be possible.

9.2 Shocks by auto mounting

Shocks caused by auto mounting and vacuuming may deteriorate the characteristics and affect the products. Please set the mounting conditions to minimize the shocks as much as possible, and be sure that there is no affect on the characteristics before mounting. Please review the conditions after the conditions are changed. Also please be sure that crystal products don't hit machines or other electric boards, etc. before or after mounting.

9.3 Notes for each package type

(1) Ceramic package products and SON products

Bending the board after soldering ceramic package products and SON products (MC-146,RTC-****NB,RX-****NB) may cause peeling off portions of soldering or package cracks by mechanical stress. Particularly, in the case of cutting boards after soldering these products, please be sure to layout the crystal on a less stressed location and use less stressed cutting method.

(2) Ceramic package products

In the case of soldering ceramic package products on a different expansion-coefficient board (ex. Epoxy Glass), soldering crack at the foot pattern would be expected under repeated temperature changes for a long period. Under these conditions, be sure to check the solderability in advance.

(3) DIP products

Deformed leads cannot be inserted into board holes. Avoid applying stress sufficient to deform leads.

(5) SOJ Products and SOP products

Please avoid applying stress sufficient to deform the leads. Deformed leads may cause incorrect soldering. Particularly SOP products need to be handled with the greatest care.

9.4 Ultrasonic cleaning

(1) Products using AT-cut crystal and SAW resonator/filter can be cleaned by ultrasonic methods. But under some conditions, the crystal characteristics may be affected and internal wiring may be damaged. Please be sure to check the suitability of your system in advance.

(2) Products using tuning-fork crystals, gyro sensors cannot be guaranteed if cleaned using ultrasonic methods, because crystal may be destroyed.

(3) Do not wash open-type products.

(4) With washable products, avoid the use of cleaners or solvents that may negatively affect the product.

(5) By solder flux residual moisture absorption and solidification, it may cause of migration etc. So, it may affect the reliability and quality of the product negatively. Please clean up the residue of flux and dry the PCB.

10. Handling

Do not touch the surface of IC directly with tweezers or any hard tools, jigs.

11. Use environment (temperature and humidity)

Please use the products within rating temperature range in concern temperature distribution in body and seasonal changes. In high humidity environment, it may cause malfunction caused by the dew condensation. Please prevent dew condensation.

■ Crystal unit

1. Drive level

Applying excessive drive level to the crystal units may cause deterioration of characteristics or damage. Circuit design must be such as to maintain a proper drive level.(refer to page "Drive level")

2. Negative resistance

Unless adequate negative resistance is allocated in the oscillation circuit, oscillation or oscillation start up time may increase (refer to page "Allowance for Oscillation".)

3. Load capacitance

Differences in the load capacitance in the oscillation circuit may result in deviations in the oscillation frequency from the desired frequency. Attempting to tune by force may merely cause abnormal oscillation. Before use, please specify the load capacitance of the oscillation circuit.(refer to page "Load capacitance")

■ Crystal Oscillator and real time clock module

All crystal oscillators and real time clock modules are provided with an IC.

1. Noise

Applying excessive level of extraneous noise to power source or input terminal may cause latch up or spurious phenomenon, which results in malfunction and breakdown.

2. Power supply line

Line impedance of a power supply should be as low as possible.

3. Output Load

It is recommended that output load is installed as close as possible to an oscillator (within 20 mm).

4. Treatment of unused input terminals

Unused pins that are left open may collect noise, thereby resulting in malfunction. Also, power consumption may increase when both P-channel and N-channel are turned on, therefore connect unused input terminals to VCC or GND.

5. Heat impact

Repeated large changes in temperature may degrade the characteristics of a deteriorated crystal unit and cause breakage of wires inside the plastic mold. This must be avoided.

6. Mounting direction

Incorrect mounting of the oscillator may cause malfunction and breakdown, so please check the mounting direction when installing.

7. Power on

It is not recommended to power on from intermediate electric potential and / or extreme fast power on. Powering on under such conditions may cause no oscillation and / or malfunction.

■ Sensing device

1. Interference between the sensors

The sensor may catch the interference by board vibration and power supply common impedance.

2. Protection against vibration

Please consider about protection against vibration when the sensor is operated in vibration condition.

PRECAUTIONS IN DESIGNING OSCILLATION CIRCUITS

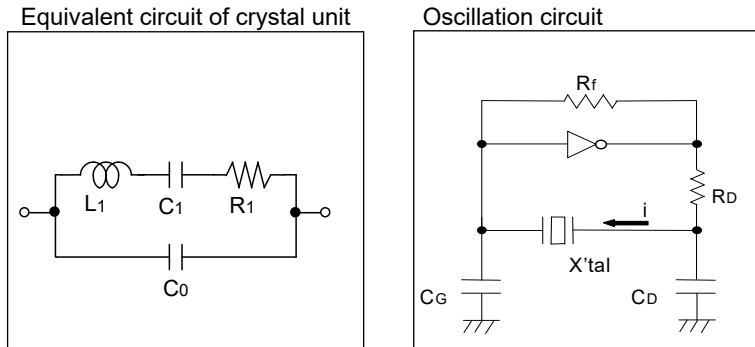
1. Drive level

Drive level denotes electric power required to oscillate a crystal unit, which can be calculated using the following formula.

$$\text{Drive level (P)} = i^2 \cdot R_e$$

Where i stands for current to pass in the crystal unit, R_e for effective resistance of crystal unit, and $R_e = R_1(1 + C_0/C_1)^2$.

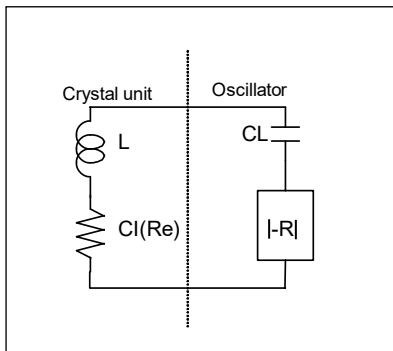
If the Drive level (P) exceeds the specified level, oscillation frequency will shift. This occurs because an excessive level of power causes stress for the crystal and, consequently, temperature rises. If excessive drive level of power is applied to the crystal unit, this may deteriorate or damage the characteristics.



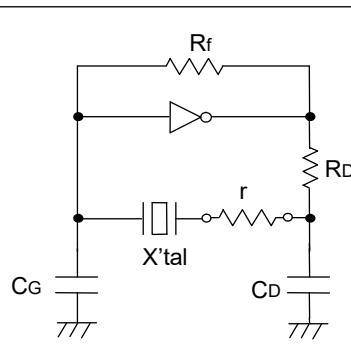
2. Allowance for oscillation

Unless adequate negative resistance is allocated in the oscillation circuit, oscillation start-up time may be increased, or No oscillation may occur. In order to avoid this, provide enough negative resistance in the circuitry design.

● Crystal unit and Oscillator



● Check of Negative resistance



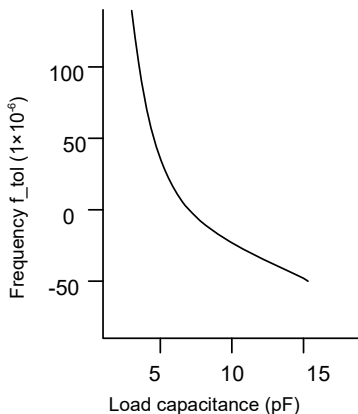
1. Connect the resistance (r) to the circuit in series with the crystal unit.
2. Adjust (r) so that oscillation can start (or stop).
3. Measure (r) when oscillation just starts (or stops) in (2) above.
4. Recommended (r)
 $(r) > C_1 \times 5$

3. Load capacitance

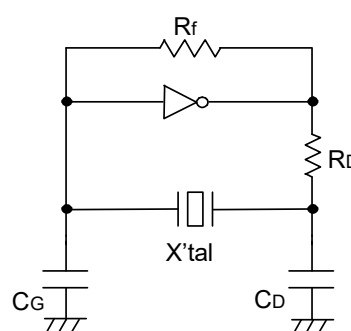
Differences in the load capacitance of the oscillation circuit may result in a different oscillation frequency from the desired one, as shown in the figure below. Approximate expression of the load capacitance of the circuit $CL \cong CG \times CD / (CG + CD) + CS$.

Where CS stands for stray Capacity of the circuit.

● Frequency and load capacitance characteristics



● Reference for setting parameters of oscillation circuit



Symbol	Rf (MΩ)	RD (kΩ)	CG (pF)	CD (pF)
Frequency range				
20 kHz to 60 kHz	20	500	10	10
60 kHz to 165 kHz	10	300	10	10
5.5 MHz to 30 MHz (Fundamental)	1	0.5	5~15	5~15
30 MHz~50 MHz (Fundamental)			5~10	5~10

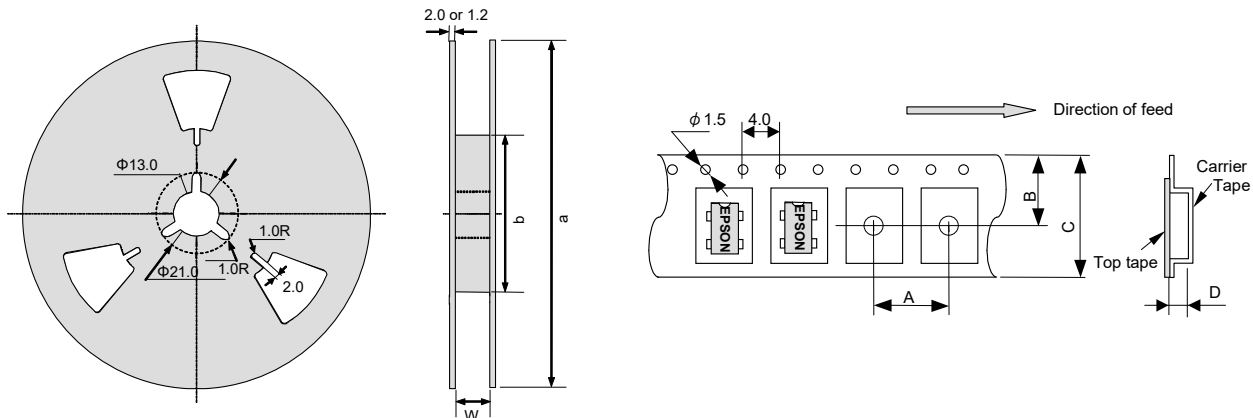
IC: equivalent to TC74HCU04 (Unbuffer)
 IC: equivalent to TC74VHCU04 (Unbuffer) (30 MHz to 50 MHz)
 (TC74HCU04 and TC74VHCU04 are product number of Toshiba Corp.)

PACKING SPECIFICATIONS

For SMD products, recommendation packing quantity is specified as below table. Please order in accordance with packing quantity.

1. SMD

SMD products are packed in the shipping carton as below table in accordance with taping standards EIA-481 and IEC-60286.



RECOMMENDATION PACKING QUANTITY and DIMENSION (Unit: mm)

●Crystal unit

Model	Quantity (pcs/Reel)	Reel dimension			Career Tape dimension				Direction of feed (L=left direction)
		a	b	W	A	B	C	D	
kHz Range Crystal unit									
FC1610AN	5000	Φ180	Φ60	9	4	5.25	8	0.65	L
FC1610BN	5000	Φ180	Φ60	9	4	5.25	8	0.65	L
FC2012AN	5000	Φ180	Φ60	9	4	5.25	8	0.75	L
FC2012SN	5000	Φ180	Φ60	9	4	5.25	8	0.75	L
FC-12M	5000	Φ180	Φ60	9	4	5.25	8	0.75	L
FC3215AN	3000	Φ180	Φ60	13	4	7.25	12	1	L
FC-135 / FC-135R	3000	Φ180	Φ60	13	4	7.25	12	1	L
MC-146	9000	Φ330	Φ80 or Φ100	17	4	9.25	16	1.6	L
MC-306	3000	Φ330	Φ80 or Φ100	17.5	8	9.25	16	2.7	L
FC2012AA	5000	Φ180	Φ60	9	4	5.25	8	0.75	L
FC-13A	3000	Φ180	Φ60	13	4	7.25	12	1	L
MC-30A	3000	Φ330	Φ80 or Φ100	17.5	8	9.25	16	2.7	L
MHz Range Crystal unit									
FA1008AN	6000	Φ180	Φ60	9	4	5.25	8	0.35	L
FA1210AN	6000	Φ180	Φ60	9	4	5.25	8	0.45	L
FA-118T	6000	Φ180	Φ60	9	4	5.25	8	0.5	L
FA-128	5000	Φ180	Φ60	9	4	5.25	8	0.7	L
FA-20H	5000	Φ180	Φ60	9	4	5.25	8	0.75	L
FA-238V	4000	Φ180	Φ60	9	4	5.25	8	1.05	L
FA-238	4000	Φ180	Φ60	9	4	5.25	8	1.05	L
TSX-3225	4000	Φ180	Φ60	9	4	5.25	8	1	L
FA2016AA	5000	Φ180	Φ60	9	4	5.25	8	0.7	L
FA-238A	4000	Φ180	Φ60	9	4	5.25	8	1.05	L
FA1612AS	5000	Φ180	Φ60	9	4	5.25	8	0.7	L
FA2016AS	5000	Φ180	Φ60	9	4	5.25	8	0.7	L
FA2016ASA	3000	Φ180	Φ60	9	4	5.25	8	0.7	L

●SPXO

Model	Quantity (pcs/Reel)	Reel dimension			Career Tape dimension				Direction of feed (L=left direction)
		a	b	W	A	B	C	D	
SG2016CAN / CAA	3000	Φ180	Φ60	9	4	5.25	8	1	L
SG2016EGN / VGN	2000	Φ180	Φ60	9	4	5.25	8	1	L
SG2016EHN / VHN	2000	Φ180	Φ60	9	4	5.25	8	1	L
SG2016HHN / HGN	2000	Φ180	Φ60	9	4	5.25	8	1	L
SG-210 STF	3000	Φ180	Φ60	9	4	5.25	8	1.15	L
SG2520EGN / VGN	2000	Φ180	Φ60	9	4	5.25	8	1.15	L
SG2520EHN / VHN	2000	Φ180	Φ60	9	4	5.25	8	1.15	L
SG2520HHN / HGN	2000	Φ180	Φ60	9	4	5.25	8	1.15	L
SG-3031CM	3000	Φ180	Φ60	13	4	7.25	12	1	L
SG-3031CMA	3000	Φ180	Φ60	13	4	7.25	12	1	L
SG3225CAN	2000	Φ180	Φ60	9	4	5.25	8	1.4	L
SG3225EAN / VAN / EEN / VEN	2000	Φ180	Φ60	9	4	5.25	8	1.4	L
SG3225HBN	2000	Φ180	Φ60	9	4	5.25	8	1.4	L
SG5032CAN / CCN	1000	Φ180	Φ60	13	8	7.25	12	1.4	L
SG5032EAN / VAN / EEN / VEN	1000	Φ180	Φ60	13	8	7.25	12	1.4	L
SG7050CAN / CCN	1000	Φ254	Φ100	17.4	8	9.25	16	2	L
SG7050EAN / VAN	1000	Φ254	Φ100	17.4	8	9.25	16	2	L
SG7050EEN / VEN	1000	Φ180	Φ60	17	8	9.25	16	2	L
SG-850xCA	1000	Φ180	Φ60	17	8	9.25	16	2	L

●SPSO(Low-Jitter SAW)

EG-2102CA	1000	Φ180	Φ60	17	8	9.25	16	2	L
EG-2102CB	1000	Φ180	Φ60	13	8	7.25	12	1.7	L
EG-2121CB	1000	Φ180	Φ60	13	8	7.25	12	1.7	L
XG-2102CA	1000	Φ180	Φ60	17	8	9.25	16	2	L
XG-2103CA	1000	Φ180	Φ60	17	8	9.25	16	2	L
XG-2121CA	1000	Φ180	Φ60	17	8	9.25	16	2	L
XG5032HAN	1000	Φ180	Φ60	13	8	7.25	12	1.7	L

●Programmable & Spread Spectrum

SG-8101CA / SG-8018CA	1000	Φ254	Φ100	17.4	8	9.25	16	2	L
SG-8101CB / SG-8018CB	1000	Φ180	Φ60	13	8	7.25	12	1.4	L
SG-8101CE / SG-8018CE	2000	Φ180	Φ60	9	4	5.25	8	1.4	L
SG-8101CG SG-8018CG / CGA	3000	Φ180	Φ60	9	4	5.25	8	1.15	L
SG-8200CG SG-8201CG	3000	Φ180	Φ60	9	4	5.25	8	1.15	L
SG-8200CJ SG-8201CJ / CJA	3000	Φ180	Φ60	9	4	5.25	8	1	L
SG-9101CA	1000	Φ254	Φ100	17.4	8	9.25	16	2	L
SG-9101CB	1000	Φ180	Φ60	13	8	7.25	12	1.4	L
SG-9101CE	2000	Φ180	Φ60	9	4	5.25	8	1.4	L
SG-9101CG / CGA	3000	Φ180	Φ60	9	4	5.25	8	1.15	L

●Real Time Clock Module

Model	Quantity (pcs/Reel)	Reel dimension			Career Tape dimension				Direction of feed (L=left direction)
		a	b	W	A	B	C	D	
RA-4565SA	1000	Φ330	Φ80 or Φ100	17.5	12	9.25	16	3.65	L
RA4803SA	1000	Φ330	Φ80 or Φ100	17.5	12	9.25	16	3.65	L
RA8803SA	1000	Φ330	Φ80 or Φ100	17.5	12	9.25	16	3.65	L
RTC-8564JE	1000	Φ330	Φ80 or Φ100	17.5	12	9.25	16	2	L
RTC-8564NB	1000	Φ330	Φ80 or Φ100	17.5	12	9.25	16	1.8	L
RX****CE/ RA****CE	2000	Φ180	Φ60	9	4	5.25	8	1.4	L
RX-4035LC	2000	Φ180	Φ60	13	4	7.25	12	1.45	L
RX-4035SA	1000	Φ330	Φ80 or Φ100	17.5	12	9.25	16	3.65	L
RX-4045NB	1000	Φ330	Φ80 or Φ100	17.5	12	9.25	16	1.8	L
RX-4045SA	1000	Φ330	Φ80 or Φ100	17.5	12	9.25	16	3.65	L
RX-4571LC	2000	Φ180	Φ60	13	4	7.25	12	1.45	L
RX-4571NB	1000	Φ330	Φ80 or Φ100	17.5	12	9.25	16	1.8	L
RX-4571SA	1000	Φ330	Φ80 or Φ100	17.5	12	9.25	16	3.65	L
RX-4803LC	2000	Φ180	Φ60	13	4	7.25	12	1.45	L
RX-4803SA	1000	Φ330	Φ80 or Φ100	17.5	12	9.25	16	3.65	L
RX6110SA	1000	Φ330	Φ80 or Φ100	17.5	12	9.25	16	3.65	L
RX8010SJ	1000	Φ330	Φ80	17.5	12	9.25	16	2.95	L
RX-8025NB	1000	Φ330	Φ80 or Φ100	17.5	12	9.25	16	1.8	L
RX-8025SA	1000	Φ330	Φ80 or Φ100	17.5	12	9.25	16	3.65	L
RX-8035LC	2000	Φ180	Φ60	13	4	7.25	12	1.45	L
RX-8035SA	1000	Φ330	Φ80 or Φ100	17.5	12	9.25	16	3.65	L
RX-8564LC	2000	Φ180	Φ60	13	4	7.25	12	1.45	L
RX-8571LC	2000	Φ180	Φ60	13	4	7.25	12	1.45	L
RX-8571SA	1000	Φ330	Φ80 or Φ100	17.5	12	9.25	16	3.65	L
RX-8731LC	2000	Φ180	Φ60	13	4	7.25	12	1.45	L
RX-8803LC	2000	Φ180	Φ60	13	4	7.25	12	1.45	L
RX-8803SA	1000	Φ330	Φ80 or Φ100	17.5	12	9.25	16	3.65	L
RX8900SA	1000	Φ330	Φ80 or Φ100	17.5	12	9.25	16	3.65	L

●TCXO

Model	Quantity (pcs/Reel)	Reel dimension			Career Tape dimension				Direction of feed (L=left direction)
		a	b	W	A	B	C	D	
TG1612SLN	3000	Φ180	Φ60	9.4	4	5.25	8	1.15	L
TG2016SLN	3000	Φ180	Φ60	9.4	4	5.25	8	1.15	L
TG2016SMN	12000	Φ330	Φ100	9.4	4	5.25	8	1	L
	3000	Φ180	Φ60	9	4	5.25	8	1	L
TG2520CEN	10000	Φ330	Φ100	9.4	4	5.25	8	1	L
	3000	Φ180	Φ60	9	4	5.25	8	1	L
TG2520SMN	10000	Φ330	Φ100	9.4	4	5.25	8	1	L
	3000	Φ180	Φ60	9	4	5.25	8	1	L
TG3225CEN	2000	Φ180	Φ60	9	4	5.25	8	1.4	L
TG-3541CE / TG3541CEA	2000	Φ180	Φ60	9	4	5.25	8	1.4	L
TG-5006CE	2000	Φ180	Φ60	9	4	5.25	8	1.4	L
TG-5006CG	2000	Φ180	Φ60	9	4	5.25	8	1.15	L
TG-5006CJ	12000	Φ330	Φ100	9.4	4	5.25	8	1	L
	3000	Φ180	Φ60	9	4	5.25	8	1	L
TG5032CFN /SFN	1000	Φ180	Φ60	13	8	7.25	12	1.4	L
TG5032CGN /SGN	1000	Φ180	Φ60	13	8	7.25	12	1.4	L
TG5032CKN /SKN	1000	Φ180	Φ60	13	8	7.25	12	1.4	L
TG5032CMN /SMN	1000	Φ180	Φ60	13	8	7.25	12	1.4	L
TG-5510CA / TG-5511CA	1000	Φ180	Φ60	17	8	9.25	16	2	L
TG-5510CB / TG-5511CB	1000	Φ180	Φ60	13	8	7.25	12	1.4	L
TG7050xKN / xMN	1000	Φ180	Φ60	17	8	9.25	16	2	L

●VCXO/VCSO

VG3225EFN / VFN	2000	Φ180	Φ60	9	4	5.25	8	1.4	L
VG5032EDN / VDN	1000	Φ180	Φ60	13	8	7.25	12	1.4	L
VG5032EFN / VFN	1000	Φ180	Φ60	13	8	7.25	12	1.4	L
VG7050CDN	1000	Φ180	Φ60	17	8	9.25	16	2	L
VG7050EAN / EBN / ECN	1000	Φ180	Φ60	17	8	9.25	16	2	L
VG7050EFN / VFN	1000	Φ180	Φ60	17	8	9.25	16	2	L

●Multi output oscillator

MG7050 series	1000	Φ180	Φ60	17	8	9.25	16	2.1	L
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●Module

SR3225SAA	3000	Φ180	Φ60	9	4	5.25	8	1.4	L
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●Sensor

XV-3510CB	2000	Φ254	Φ100	13.4	8	7.25	12	2.1	L
XV4001Bx	2000	Φ254	Φ100	13.4	8	7.25	12	2.1	L
XV4001Kx	1000	Φ254	Φ100	13.4	8	7.25	12	3.7	L
XV7011BB / XV7001BB	2000	Φ254	Φ100	13.4	8	7.25	12	2.1	L
XV7021BB / XV7081BB	2000	Φ254	Φ100	13.4	8	7.25	12	2.1	L
XC1011SD	2000	Φ330	Φ80 or Φ100	17.4	8	9.25	16	2.2	L

■ Manufacturing Plant

Plant	Date Operations Commenced	Products
Ina Plant / Seiko Epson Corp.	Jun.1959	Crystal unit, Crystal oscillator, Real time clock module, Surface acoustic wave device, Sensing device
Fujimi Plant / Seiko Epson Corp.	Apr.1980	IMU, Vibration sensor, Accelerometer
Miyazaki Epson Co.,	Jun.1984	Crystal Chip, Synthetic quartz
Epson Atmix Co.,	Oct.1999	Synthetic quartz

Plant	Date Operations Commenced	Products
⑧: Epson Precision Malaysia Sdn. Bhd.	Dec.1974	Crystal unit, Crystal oscillator, Sensing device, Real time clock module
⑨: Epson Precision (Thailand) Ltd.	May.1988	Crystal unit, Surface acoustic wave device Crystal oscillator
⑩: Epson Precision Suzhou Co.,Ltd.	Mar.1997	Crystal unit, Crystal oscillator Real time clock module
⑪: Epson Portland ,Inc.	Jun.2000	Synthetic quartz

■ Business area

